

Department of Information Science & Engineering

Academic Calendar and Lesson Plan

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Prof. & News JEPT. OF INFORMATION SCIENCE & ENGINEERING. SOM Catlege of Engineering & Technology. Ohavalagiri DHARWAD-580 002

Odd Semester 2021-22

Academic Calendar for UG Programmes III, V and VII Sem

şl.	Particulars	Date
No	Registration	28-09-2021 to 30-09-2021
0	Commencement of Teaching	01-10-2021
0	Last date for registration with late fee	04-10-2021
4	Display of Attendance	23-11-2021
5	Internal Assessment - IA- I	
- Land	Communication of performance to the	24-11-2021 to 26-11-2021
6	parents	02-12-2021
7	Last date to drop the course	04-12-2021
8	Display of Attendance	The state of the s
9	Internal Assessment –IA– II	27-12-2021
	Communication of performance to the	28-12-2021 to 30-12-2021
10	parents	05-01-2022
11	Last date to withdraw the course	06-01-2022
12	Teacher – Parents Meet	08-01-2022
13_	Students Feedback	10-01-2022 to 15-01-2022
14	Internal Assessment –IA– III	29-01-2022 to 01-02-2022
15	Last day of teaching for Odd Semester	01-02-2022
16	Final Lab Assessments	05-02-2022 to 10-02-2022
17	Display of consolidated Continuous Internal Evaluation (CIE) and Attendance	07-02-2022
18	Communication of performance to the parents	08-02-2022
19	Semester End Examination	14-02-2022 to 28-02-2022
20	Inter Semester Recess	01-03-2022 to 08-03-2022
21	Declaration of Results	07-03-2022
	Communication of performance to the	08-03-2022
22	parents by putting on website	
23	Makeup SEE for odd semesters	09-03-2022 to 12-03-2022
	Commencement of Even Semester:	14-03-2022

Dean (Academic Program)

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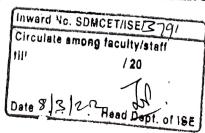
SDM College of Engineering & Technology, Dharwad Even Semester 2021-22

Academic Calendar for B.E IV, VI & VIII Sem. & M.Tech. IV Sem.

SI No	Particulars	Date
110	and the state of t	15-03-2022 &
1	Registration •	16-03-2022
2	Commencement of Teaching	17-03-2022
3	Last date for registration with late fee	18-03-2022
4	Display of attendance	23-04-2022
_		25-04-2022 to
5	Internal Assessment – IA– I	27-04-2022
6	Communication of performance to the parents	04-05-2022
7	Last date to drop the course	04-05-2022
8	Display of attendance	31-05-2022
9	Internal Assessment – IA– II	01-06-2022 to
9		03-06-2022
10	Last date to withdraw the course	04-06-2022
11	Communication of performance to the parents	09-06-2022
12	Parents Meet	11-06-2022
13	Feedback by Students	13-06-2022 to
	recuback by Students	18-06-2022
14	Internal Assessment –IA– III	29-06-2022 to
		01-07-2022
15	Last day of teaching for Even Semester	04-07-2022
16	Final Lab Assessments	05-07-2022 to
		09-07-2022
17	Display of consolidated Continuous Internal Evaluation (CIE) marks & Attendance	07-07-2022
18	Last date for submission of Project Report (M.Tech. Prog)	09-07-2022
19	Semester End Examination (UG Programs)	13-07-2022 to
	Domicoter Brita Britanniation (O'd Frograms)	26-07-2022
20	Summer vacation	27-07-2022 to
		31-08-2022
21	Announcement of Results	01-08-2022
22	Makeup SEE for Even semesters	01-08-2022 &
00		05-08-2022
23	Communication of performance to the parents	02-08-2022
	Commencement of Odd Semester	01-09-2022
	Supplementary Semester	27-07-2022 to
	,	30-08-2022

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SDM College of Engineering & Technology, Dharwad

Academic Calendar for UG Progri Odd Semester 2020-21

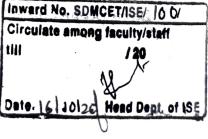
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SI	M. Tech. III Sem and MBA III Sem			
No	(Tentative)			
1	Particulars Registration	Date		
2	Commence	28-08-2020 to 31-08-2020		
3	Commencement of Teaching Last date for registration with late fee Internal Assessment - IA	01-09-2020		
4	Internal Assessment – IA- I	07-09-2020		
5	Communication IA-I	20-10-2020 to 22-10-2020		
6	Communication of performance to the parents	29-10-2020		
7	Last date to drop the course	02-11-2020		
		25-11-2020 to 27-11-2020		
8	Communication of performance to the parents	04-12-2020		
9	Last date to withdraw the course	05-12-2020		
10	Students Feedback	07-12-2020 to 09-12-2020		
11	Teacher - Parents Meet	08-12-2020		
12	Internal Assessment –IA– III	26-12-2020 to 29-12-2020		
13	Last day of teaching for Odd Semester	29-12-2020		
4	Final Lab Assessments	02-01-2021 to 07-01-2021		
5	Display of consolidated Continuous Internal Evaluation (CIE)	07-01-2021		
6	Communication of performance to the parents	08-01-2021		
7	Semester End Examination	13-01-2021 to 27-01-2021		
8	Inter Semester Recess	28-01-2021 to 04-02-2021		
9	Declaration of Results	08-02-2021		
1	Communication of performance to the parents by putting on website	09-02-2021		
- T-	Makeup SEE for odd semesters	15-02-2021 to 19-02-2021		

Commencement of Even Semester: 09-02-2021

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SDM College of Engineering & Technology, Dharwad Even Semester 2020-21

Academic Calendar for II, IV, VI Sem. B.E & II Sem. M.Tech.

S1.	outendar for it, iv, vi sem	The second secon			
No		IV & VI Sem.	II Sem. B.E &		
		B.E	II Sem. M.Tech		
1	Registration	12-03-2021 &	13-05-2021 &		
2	Common compact of many	13-03-2021	15-05-2021		
3	Commencement of Teaching	15-03-2021	17-05-2021		
4	Last date for registration with late fee	18-03-2021	19-05-2021		
	Display of attendance	23-04-2021	21-06-2021		
5	Internal Assessment – IA– I	26-04-2021 to			
6	Communication of performance to the parents	28-04-2021 05-05-2021	25-06-2021 01-07-2021		
7	Last date to drop the course	07-05-2021	02-07-2021		
8	Display of attendance	28-05-2021			
9		31-05-2021 to	24-07-2021		
	Internal Assessment – IA– II	02-06-2021	26-07-2021 to		
10	Last date to withdraw the course	04-06-2021	28-07-2021		
11	Communication of performance to the		30-07-2021		
12	parents	08-06-2021	04-08-2021		
12	Parents Meet	12-06-2021	14-08-2021		
13	Feedback by Students	21-06-2021 to	16-08-2021 to		
	3	26-06-2021	19-08-2021		
14	Internal Assessment –IA– III	01-07-2021 to	27-08-2021 to		
15		05-07-2021	31-08-2021		
	Last day of teaching for Even Semester	05-07-2021	31-08-2021		
16	Display of consolidated Continuous Internal Evaluation (CIE) marks & Attendance	08-07-2021	07-09-2021		
17	Final Lab Assessments	09-07-2021 to	03-09-2021 to		
		17-07-2021	11-09-2021		
18	Semester End Examination	20-07-2021 to	15-09-2021 to		
		31-07-2021	25-09-2021		
19	Summer vacation	02-08-2021 to	26-09-2021 to		
20	Announcement of Devel	30-09-2021	30-09-2021		
	Announcement of Results	05-08-2021	30-09-2021		
21	Communication of performance to the parents	06-08-2021	30-09-2021		
22	Makeup SEE for Even semesters	09-08-2021 &	01-10-2021 &		
		10-08-2021	04-10-2021		
-	Commencement of Odd Semester	01-10-2021	01-10-2021		
	Supplementary Semester	02-08-2021 to			
		25-09-2021			

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SDM College of Engineering & Technology, Dharwad Odd Semester 2019-20

Academic Calendar for UG Programmes

	Academic Caleman, 10,	
S1.	Particulars	Date
No	Registration	27 07 2019 to 31 07 2019
	Induction program for First Semester	01 08 2019 to 14 08-2019
2.2	(Tentative)	01 02 2013 10 11 337 277
	Teaching Commences for odd semester	01-08-2019
3	except I Sem	01 00.2013
4	Last date for registration with late fee	06 08 2019
5	Teaching Commences for L semester	16-08-2019
6	Display of attendance	16-09-2019
7-	Internal Assessment - IA- I	18-09-2019 to 20-09-2019
0	Communication of performance to the	26-09-2019
8	parents	
9	Last date to drop the course	27-09-2019
10	Display of attendance	02-11-2019
11	Internal Assessment -IA- II	04-11-2019 to 06-11-2019
12	Students Feedback	11-11-2019 to 15-11-2019
13	Communication of performance to the	13-11-2019
13	parents	
14	Last date to withdraw the course	13-11-2019
15	Teacher - Parents Meet	23-11-2019
16	Internal Assessment -IA- III	05-12-2019 to 07-12-2019
17	Last day of teaching for Odd Semester	07-12-2019
18	Display of consolidated Continuous	10-12-2019
10	Internal Evaluation (CIE) & Attendance	
19	Communication of performance to the	11-12-2019
19	parents	10 10 0010 4 21 10 0010
20	Semester End Examination	13-12-2019 to 31-12-2019
21	Final Lab Assessments	02-01-2020 to 10-01-2020
22	Inter Semester Recess	11-01-2020 to 20-01-2020
23	Declaration of Results	14-01-2020
24	Communication of performance to the	16-01-2020
24	parents by putting on website	
25	Makeup SEE for odd semesters	18-01-2020 to 20-01-2020
	Commencement of Even Semester:	21-01-2020

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SDM College of Engineering & Technology, Dharwad Academic Calendar for Even Semester 2019-20 B.E. & M.Tech IV Sem

SI.	Particulars	Date
1	Registration	18-01-2020 to 20-01-2020
2	Commencement of Teaching	21-01-2020
_3	Last date for registration with late fee	24-01 2020
-4	Display of attendance	28-02-2020
5	Internal Assessment - IA- I	02-03-2020 to 04-03-2020
6	Communication of performance to the parents	12-03-2020
7	Last date to drop the course	13-03-2020
8	Parents Meet	21-03-2020
9	Display of attendance	03-04-2020
10	Internal Assessment - IA- II	07-04-2020 to 09-04-2020
11	Last date to withdraw the course	16-04-2020
12	Communication of performance to the parents	16-04-2020
13	Feedback by Students	20-04-2020 to 24-04-2020
14	Internal Assessment IA-III	06-05-2020, 07-05-2020 & 08-05-2020
15	Last day of teaching for Even Semester	09-05-2020
16	Final Lab Assessments	13-05-2020 to 23-05-2020
17	Display of consolidated Continuous Internal Evaluation (CIE) marks & Attendance for 8th semester	14-05-2020
18	Display of consolidated CIE marks & Attendance for 2 nd , 4 th & 6 th semesters (Both for UG & PG)	16-05-2020
19	Semester End Examination for 8th semester	18-05-2020 to 26-05-2020
20	Communication of performance to the parents	18-05-2020
21	Semester End Examination for 2 nd , 4 th & 6 th semesters (Both for UG & PG -Project)	28-05-2020 to 13-06-2020
22	Project exam for 8th semester	29-05-2020 to 04-06-2020
23	Results for 8th semester	04-06-2020
24	Makeup SEE for 8th semester semesters	08-06-2020 & 09-06-2020
25	Summer vacation	14-06-2020 to 02-08-2020
26	Announcement of Results for 2^{nd} , 4^{th} & 6^{th} semester (Both for UG & PG)	20-06-2020
27	Makeup SEE for even semesters	22-06-2020 & 23-06-2020

Supplementary Semester: 20-06-2020 to 31-07-2020

Commencement of next Academic Year 2020 - 21: 03-08-2020

SDM College of Engineering & Technology, Dharwad Odd Semester 2018-19

Academic Calendar for B.E., M.Tech. & MBA

No.	No. Particulars	
1	Registration	Date
0	Induction program for First Semester	27-07-2018 to 31-07-2018
2	(Tell(RUVE)	01-08-2018 to 14-08-2018
3	Teaching Commences for odd semester except I Sem	01-08-2018
4	Last date for registration with late fee	
5	reaching Commences for Learnester	06-08-2018
6	Display of attendance	16-08-2018
7	Internal Assessment - 14 1	15-09-2018
8	Communication of performance to the	17-09-2018 to 19-09-2018
9	Parcita	25-09-2018
10	Last date to drop the course	27-09-2018
10	Display of attendance	22-10-2018
11	Internal Assessment -IA- II	23-10-2018, 25-10-2018
-	· ·	& 26-10-2018
12	Communication of performance to the parents	31-10-2018
13	Teacher - Parents Meet	
14	Last date to withdraw the course	03-11-2018
15	Students Feedback	05-11-2018
16	Internal Assessment -IA- III	12-11-2018 to 17-11-2018
17	Last day of teaching for Odd Semester	28-11-2018 to 30-11-2018
18	Final Lab Assessments	30-11-2018
	Display of consolidate in Consolidat	03-12-2018 to 10-12-2018
19	Display of consolidated Continuous Internal Evaluation (CIE) & Attendance	05-12-2018
20	Communication of performance to the parents	05-12-2018
21	Semester End Examination	13 12 2018 + 22 12 221
22	Inter Semester Recess	13-12-2018 to 28-12-2018
23	Declaration of Results	29-12-2018 to 10-01-2019
0.4	Communication of performance to the	09-01-2019
24	parents by putting on website	10-01-2019
25	Makeup SEE for odd semesters	11-01-2019 to 18-01-2019
	Commencement of Even Semester :	1 ** 01-2019 to 18-01-2019

^{* 20-10-2018} may be declared as holiday and 14-10-2018 as working day

Supplementary Semester Calendar for B.E./M.Tech/MBA (Tentative)

SI. No.	Particulars	VII & VIII Sem (B.E.)	I to VI Sem (B.E.), M.Tech & MBA
1	Registration	27.05.2019 to 30.05.2019	03.06.2019 to 10.06.2019
2	Teaching Commences	30-05-2019	12-06-2019
3	Registration with special permission by Principal	01.06.2019	12.06.2019
4	Internal Assessment (IA) – I	13-06-2019 & 14-06-2019	24-06-2019 & 25-06-2019
5	Internal Assessment (IA) - II	24-06-2019 & 25-06-2019	03-07-2019 & 04-07-2019
6	Internal Assessment (IA) - III	08-07-2019 & 09-07-2019	11-07-2019 & 12-07-2019
7	Display of consolidated Continuous Internal Evaluation (CIE) marks & Attendance	10-07-2019	15-07-2019
8	Supplementary SEE	11-07-2019 to 15-07-2019	0 17-07-2019 to 23-07-2019
9	Declaration of results	20-07-2019	27-07-2019

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SDM College of Engineering & Technology, Dharwad Academic Calendar for Even Semester 2018-19

B.E., M.Tech & MBA

S1.	Particulars	Date
No.		09-01-2019 to 11-01-2019
1	Registration Commencement of Teaching	11-01-2019
2	Last date for registration with late fee	18-01-2019
3		18-02-2019
4	Display of attendance Internal Assessment – IA– I	20-02-2019 to 22-02-2019
5	Internal Assessment	28-02-2019
6	Communication of portorization	01-03-2019
7	Last date to drop die course	09-03-2019
	Parents Meet	15-03-2019 & 16-03-2019
_	Ilisigilla – 2015	30-03-2019
-	Internal Assessment – IA– II	01-04-2019 to 03-04-2019
	Internal riododdinaria	06-04-2019 -
-	Communication of performance to the parents	10-04-2019
		22-04-2019 to 27-04-2019
	Feedback by Students Internal Assessment –IA– III	30-04-2019 to 03-05-2019
		03-05-2019
1.0	Last day of teaching for Every	06-05-2019 to 18-05-2019
. I	Final Lab Assessments Display of consolidated Continuous Internal Evaluation (CIE) marks & Attendance for 8th eemester	07-05-2019
9 8	Semester End Examination for 8th semester	08-05-2019 to 17-05-2019
T	Display of consolidated CIE marks & Attendance or 2 nd , 4 th & 6 th semesters (Both for UG & PG)	08-05-2019
	communication of performance to the parents	08-05-2019
	roject exam for 8th semester	20-05-2019 to 25-05-2019
S	emester End Examination for 2 nd , 4 th & 6 th emesters (Both for UG & PG)	20-05-2019 to 03-06-2019
-	esults for 8th semester	30-05-2019
+	ummer vacation	04-06-2019 to 31-07-2019
A	nnouncement of Results for 2 nd , 4 th & 6 th emester (Both for UG & PG)	10-06-2019

Supplementary Semester: 12-06-2019 to 27-07-2019

Commencement of next Academic Year 2019 - 20: 01-08-2019

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SDM College of Engineering & Technology, Dharwad Odd Semester 2017-18

Academic Calendar for B.E. (I, III, V & VII), M.Tech./MBA (III Sem)

Sl. No.	• Particulars	Date
1	Registration	24-07-2017 to 27-07-2017
2	Induction program for First Semester (Tentative)	01-08-2017
3	Teaching Commences for odd semester	28-07-2017
4	Last date for registration with late fee	04-08-2017
5	Internal Assessment - IA - I	11-09-2017 to 13-09-2017
6	Display of attendance and IA-I marks	18-09-2017
7	Communication of performance to the parents	20-09-2017
8	Last date to drop the course	22-09-2017
9	Internal Assessment - IA - II	12-10-2017 to 14-10-2017
10	Display of attendance and IA – II marks	17-10-2017
11	Communication of performance to the	19-10-2017
	Parents Teacher - Parents Meet	27-10-2017
12	Last date to withdraw the course	28-10-2017
13	Students Feedback	20-11-2017 to 25-11-2017
14	Internal Assessment - IA - III	28-11-2017 to 30.11.2017
15	Internal Assessment - IA III	01-12-2017
16	Last day of teaching for Odd Semester	04-12-2017 to 11-12-2017
17 18	Final Lab Assessments Display of consolidated Continuous	06-12-2017
19	Internal Evaluation (CIE) & Attendance Communication of performance to the	06-12-2017
13	parents	15-12-2017 to 02-01-2018
20	Semester End Examination	03-01-2018 to 15-01-2018
21.	Inter Semester Recess	13-01-2018
22	Declaration of Results	and the state of t
	Communication of performance to the	16-01-2018
23	parents by putting on website	17-01-2018 to 19-01-2018
24	err for odd semesters	A CONTRACTOR OF THE PARTY OF TH
	Commencement of Even Semeste	er: 10-01-2010

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SDM College of Engineering & Technology, Dharwad - 02

Academic Calendar of Even Semester - 2017-18 for B.E. (II, IV, VI & VIII Sem), M.Tech IV Semester

	Sl. No.	Particulars	Date
	1	Registration	11-01-2018 to 12-01-2018
	2	Commencement of Teaching	16-01-2018
	3	Last date for registration with late fee	16-01-2018
	4 '	Internal Assessment –IA– I	21-02-2018 to 23-02-2018
	5	Display of attendance and IA–I marks	27-02-2018
Ī	6	Communication of performance to the parents	28-02-2018
	7	Last date to drop the course	01-03-2018
İ	8	Parents Meet	10-03-2018
	9	Insignia – 2018	23-03-2018 & 24-03-2018
10-1-0	,10	Internal Assessment -IA- II	02-04-2018 to 04-04-2018
	11	Last date to withdraw the course	07-04-2018
1	12	Display of attendance and IA-II marks	09-04-2018
l	13	Communication of performance to the parents	10-04-2018
	14	Feedback by Students	23-4-2018 to 27-04-2018
	15	Last day of teaching for Even Semester	12-05-2018
	16	TA TIT	14-05-2018 to 16-05-2018
	17	Display of consolidated Continuous Internal Evaluation (CIE) marks & Attendance for 8th semester	18-05-2016
	18	Semester End Examination for 8th semester	21-05-2018 to 30-05-2018
	19	Final Lab Assessments	21-05-2018 to 30-05-2018
	20	Display of consolidated CIE marks & Attendance for 2 nd , 4 th & 6 th semesters	21-05-2018
	21	Communication of performance to the parents	22-05-2018
	22	Project exam for 8th semester	31-05-2018 to 02-06-2018
	23	Semester End Examination for 2 nd , 4 th & 6 th semesters	04-06-2018 to 15-06-2018
	24	Results for 8th semester	09-06-2018
	25	Summer vacation	16-06-2018 to 31-07-2018
	26	Announcement of Results for 2 nd , 4 th & 6 semester	23-06-2018

Supplementary Semester: 16-06-2018 to 28-07-2018 Commencement of next Academic Year 2018-19: 01-08-2018

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SDM College of Engineering & Technology, Dharwad Odd Semester 2021-22

Academic Calendar for III Sem M.Tech (PG Programmes)

Sl. No.	Particulars	Date	
1	Registration	18-10-2021 to 21-10-2021	
2	Commencement of Teaching	21-10-2021	
3	Last date for registration with late fee	23-10-2021	
4	Display of Attendance	02-12-2021	
5	Internal Assessment – IA– I	03-12-2021 & 04-12-2021	
6	Communication of performance to the parents	09-12-2021	
7	Last date to drop the course	11-12-2021	
8	Display of Attendance	01-01-2022	
9	Internal Assessment -IA- II	03-01-2022 & 04-01-2022	
10	Communication of performance to the parents	08-01-2022	
11	Last date to withdraw the course	10-01-2022	
12	Teacher - Parents Meet	15-01-2022	
13	Students Feedback	01-02-2022 to 05-02-2022	
14	Internal Assessment -IA- III	11-02-2022 & 12-02-2022	
15	Last day of teaching for Odd Semester	14-02-2022	
16	Final Lab Assessments	17-02-2022 & 18-02-2022	
17	Display of consolidated Continuous Internal Evaluation (CIE) and Attendance	21-02-2022	
18	Communication of performance to the parents	22-02-2022	
19	Semester End Examination	23-02-2022 to 07-03-2022	
20	Inter Semester Recess	08-03-2022 to 19-03-2022	
21	Declaration of Results 12-03-2022		
22	Communication of performance to the parents by putting on website	14-03-2022	
00	Makeup SEE for odd semesters	17-03-2022 & 18-03-2022	
23	Commencement of Even Semester		

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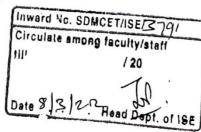
SDM College of Engineering & Technology, Dharwad Even Semester 2021-22

Academic Calendar for B.E IV, VI & VIII Sem. & M.Tech. IV Sem.

SI.	Particulars	Date
		15-03-2022 &
1	Registration •	16-03-2022
2	Commencement of Teaching	17-03-2022
3	Last date for registration with late fee	18-03-2022
4	Display of attendance	23-04-2022
5	Internal Assessment – IA– I	25-04-2022 to 27-04-2022
6	Communication of performance to the parents	04-05-2022
7	Last date to drop the course	04-05-2022
8	Display of attendance	31-05-2022
9	Internal Assessment – IA– II	01-06-2022 to 03-06-2022
10	Last date to withdraw the course	04-06-2022
11	Communication of performance to the parents	09-06-2022
12	Parents Meet	11-06-2022
13	Feedback by Students	13-06-2022 to 18-06-2022
14	Internal Assessment –IA– III	29-06-2022 to 01-07-2022
15	Last day of teaching for Even Semester	04-07-2022
16	Final Lab Assessments	05-07-2022 to 09-07-2022
17	Display of consolidated Continuous Internal Evaluation (CIE) marks & Attendance	07-07-2022
18	Last date for submission of Project Report (M.Tech. Prog)	09-07-2022
19	Semester End Examination (UG Programs)	13-07-2022 to 26-07-2022
20	Summer vacation	27-07-2022 to 31-08-2022
21	Announcement of Results	01-08-2022
22	Makeup SEE for Even semesters	01-08-2022 & 05-08-2022
23	Communication of performance to the parents	02-08-2022
	Commencement of Odd Semester	01-09-2022
		27-07-2022 to
	Supplementary Semester	30-08-2022 to
		00 00 2022

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SDM College of Engineering & Technology, Dharwad

Academic Calendar for UG Progri Odd Semester 2020-21

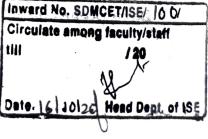
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SI	M. Tech. III Sem and MBA	mes III to VII Sem,		
No	(Tentative)			
1	Particulars Registration	Date		
2	Commence	28-08-2020 to 31-08-2020		
3	Commencement of Teaching Last date for registration with late fee Internal Assessment - IA	01-09-2020		
4	Internal Assessment – IA- I	07-09-2020		
5	Communication IA-I	20-10-2020 to 22-10-2020		
6	Communication of performance to the parents	29-10-2020		
7	Last date to drop the course	02-11-2020		
		25-11-2020 to 27-11-2020		
8	Communication of performance to the parents	04-12-2020		
9	Last date to withdraw the course	05-12-2020		
10	Students Feedback	07-12-2020 to 09-12-2020		
11	Teacher - Parents Meet	08-12-2020		
12	Internal Assessment –IA– III	26-12-2020 to 29-12-2020		
13	Last day of teaching for Odd Semester	29-12-2020		
4	Final Lab Assessments	02-01-2021 to 07-01-2021		
5	Display of consolidated Continuous Internal Evaluation (CIE)	07-01-2021		
6	Communication of performance to the parents	08-01-2021		
7	Semester End Examination	13-01-2021 to 27-01-2021		
8	Inter Semester Recess	28-01-2021 to 04-02-2021		
9	Declaration of Results	08-02-2021		
1	Communication of performance to the parents by putting on website	09-02-2021		
- T-	Makeup SEE for odd semesters	15-02-2021 to 19-02-2021		

Commencement of Even Semester: 09-02-2021

Dean (Academic Program)





SDM College of Engineering & Technology, Dharwad Even Semester 2020-21

Academic Calendar for II, IV, VI Sem. B.E & II Sem. M.Tech.

S1.	outendar for it, iv, vi sem	The second secon			
No		IV & VI Sem.	II Sem. B.E &		
		B.E	II Sem. M.Tech		
1	Registration	12-03-2021 &	13-05-2021 &		
2	Common compact of many	13-03-2021	15-05-2021		
3	Commencement of Teaching	15-03-2021	17-05-2021		
4	Last date for registration with late fee	18-03-2021	19-05-2021		
	Display of attendance	23-04-2021	21-06-2021		
5	Internal Assessment – IA– I	26-04-2021 to			
6	Communication of performance to the parents	28-04-2021 05-05-2021	25-06-2021 01-07-2021		
7	Last date to drop the course	07-05-2021	02-07-2021		
8	Display of attendance	28-05-2021			
9		31-05-2021 to	24-07-2021		
	Internal Assessment – IA– II	02-06-2021	26-07-2021 to		
10	Last date to withdraw the course	04-06-2021	28-07-2021		
11	Communication of performance to the		30-07-2021		
12	parents	08-06-2021	04-08-2021		
12	Parents Meet	12-06-2021	14-08-2021		
13	Feedback by Students	21-06-2021 to	16-08-2021 to		
	3	26-06-2021	19-08-2021		
14	Internal Assessment –IA– III	01-07-2021 to	27-08-2021 to		
15		05-07-2021	31-08-2021		
	Last day of teaching for Even Semester	05-07-2021	31-08-2021		
16	Display of consolidated Continuous Internal Evaluation (CIE) marks & Attendance	08-07-2021	07-09-2021		
17	Final Lab Assessments	09-07-2021 to	03-09-2021 to		
		17-07-2021	11-09-2021		
18	Semester End Examination	20-07-2021 to	15-09-2021 to		
		31-07-2021	25-09-2021		
19	Summer vacation	02-08-2021 to	26-09-2021 to		
20	Announcement of Devel	30-09-2021	30-09-2021		
	Announcement of Results	05-08-2021	30-09-2021		
21	Communication of performance to the parents	06-08-2021	30-09-2021		
22	Makeup SEE for Even semesters	09-08-2021 &	01-10-2021 &		
		10-08-2021	04-10-2021		
-	Commencement of Odd Semester	01-10-2021	01-10-2021		
	Supplementary Semester	02-08-2021 to			
		25-09-2021			

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SDM College of Engineering & Technology, Dharwad

Odd Semester 2019-20

Revised Academic Calendar for I Semeter M.Tech.

SI No	Dortlanlare	Date
1	Registration	26-09-2019 to 27-09-2019
	Induction program	27-09-2019
3	Teaching Commences for odd semester	30-09-2019
1	Last date for registration with late fee	05-10-2019
5	Display of attendance	18-11-2019
6	Internal Assessment - IA- I	19-11-2019 & 20-11-2019
ř	Communication of performance to the parents	26-11-2019
5	Last date to drop the course	30-11-2019
()	Display of attendance	18-12-2019
10	Internal Assessment -IA- II	19-12-2019 & 20-12-2019
1 1	Communication of performance to the parents	27-12-2019
1.2	Last date to withdraw the course	31-12-2019
13	Students Feedback	08-01-2020 & 09-01-2020
. 4	Internal Assessment -IA- III	21-01-2020 % 22-01-2020
15	Last day of teaching for Odd Semester	22-01-2020
б	Final Lab Assessments	27-01-2020
7	Display of consolidated Continuous Internal Evaluation (CIE) & Attendance	28-01-2020
8	Communication of performance to the parents	29-01-2020
9	Semester End Examination	30-01-2020 to 08-02-2020
0	Inter Semester Recess	09-02-2020 to 29-02-2020
1	Declaration of Results	17-02-2020
2	Communication of performance to the parents by putting on website	18-02-2020
}	Makeup SEE for odd semesters	24-02-2020 to 25-02-2020
	Commencement of Even Semester:	01-03-2020

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SDM College of Engineering & Technology, Dharwad Academic Calendar for Even Semester 2019-20 B.E. & M.Tech IV Sem

Sl.	Particulars	Date	
1	Registration	18-01-2020 to 20-01-2020	
2	Commencement of Teaching	21-01-2020	
3	Last date for registration with late fee	24-01-2020	
-4	Display of attendance	28-02-2020	
5	Internal Assessment - IA- I	02-03-2020 to 04-03-2020	
6	Communication of performance to the parents	12-03-2020	
7	Last date to drop the course	13-03-2020	
8	Parents Meet	21-03-2020	
9	Display of attendance	03-04-2020	
10	Internal Assessment - IA- II	07-04-2020 to 09-04-2020	
11	Last date to withdraw the course	16-04-2020	
12	Communication of performance to the parents	16-04-2020	
13	Feedback by Students	20-04-2020 to 24-04-2020	
14	Internal Assessment IA-III	06-05-2020, 07-05-2020 & 08-05-2020	
15	Last day of teaching for Even Semester	09-05-2020	
16	6 Final Lab Assessments 13-05-2020 to 23		
17	Display of consolidated Continuous Internal Evaluation (CIE) marks & Attendance for 8th semester	14-05-2020	
18	Display of consolidated CIE marks & Attendance for 2nd, 4th & 6th semesters (Both for UG & PG)	16-05-2020	
19	Semester End Examination for 8th semester	18-05-2020 to 26-05-2020	
20	Communication of performance to the parents	18-05-2020	
21	Semester End Examination for 2 nd , 4 th & 6 th semesters (Both for UG & PG -Project)	28-05-2020 to 13-06-2020	
22	Project exam for 8th semester	29-05-2020 to 04-06-2020	
23	Results for 8th semester	04-06-2020	
24	Makeup SEE for 8th semester semesters	08-06-2020 & 09-06-2020	
25	Summer vacation	14-06-2020 to 02-08-2020	
26	Announcement of Results for 2 nd , 4 th & 6 th semester (Both for UG & PG)	20-06-2020	
27	Makeup SEE for even semesters	22-06-2020 & 23-06-2020	

Supplementary Semester: 20-06-2020 to 31-07-2020

Commencement of next Academic Year 2020 - 21: 03-08-2020

SDM College of Engineering & Technology, Dharwad Odd Semester 2018-19

Academic Calendar for B.E., M.Tech. & MBA

No.	Particulars	ech. & MBA
1	Registration	Date
0	Induction program for First Semester	27-07-2018 to 31-07-2018
2	(Tell(RUVE)	01-08-2018 to 14-08-2018
3	Teaching Commences for odd semester except I Sem	01-08-2018
4	Last date for registration with late fee	
5	reaching Commences for Learnester	06-08-2018
6	Display of attendance	16-08-2018
7	Internal Assessment - 14 1	15-09-2018
8	Communication of performance to the	17-09-2018 to 19-09-2018
9	Parcita	25-09-2018
10	Last date to drop the course	27-09-2018
10	Display of attendance	22-10-2018
11	Internal Assessment -IA- II	23-10-2018, 25-10-2018
-	· ·	& 26-10-2018
12	Communication of performance to the parents	31-10-2018
13	Teacher - Parents Meet	
14	Last date to withdraw the course	03-11-2018
15	Students Feedback	05-11-2018
16	Internal Assessment -IA- III	12-11-2018 to 17-11-2018
17	Last day of teaching for Odd Semester	28-11-2018 to 30-11-2018
18	Final Lab Assessments	30-11-2018
	Display of consolidate in Consolidat	03-12-2018 to 10-12-2018
19	Display of consolidated Continuous Internal Evaluation (CIE) & Attendance	05-12-2018
20	Communication of performance to the parents	05-12-2018
21	Semester End Examination	13 12 2018 + 22 12 221
22	Inter Semester Recess	13-12-2018 to 28-12-2018
23	Declaration of Results	29-12-2018 to 10-01-2019
24	Communication of performance to the	09-01-2019
	parents by putting on website	10-01-2019
25	Makeup SEE for odd semesters	11-01-2019 to 18-01-2019
	Commencement of Even Semester :	1 ** 01-2019 to 18-01-2019

^{* 20-10-2018} may be declared as holiday and 14-10-2018 as working day

Supplementary Semester Calendar for B.E./M.Tech/MBA (Tentative)

SI. No.	Particulars	VII & VIII Sem (B.E.)	I to VI Sem (B.E.), M.Tech & MBA
1	Registration	27.05.2019 to 30.05.2019	03.06.2019 to 10.06.2019
2	Teaching Commences	30-05-2019	12-06-2019
3	Registration with special permission by Principal	01.06.2019	12.06.2019
4	Internal Assessment (IA) – I	13-06-2019 & 14-06-2019	24-06-2019 & 25-06-2019
5	Internal Assessment (IA) - II	24-06-2019 & 25-06-2019	03-07-2019 & 04-07-2019
6	Internal Assessment (IA) - III	08-07-2019 & 09-07-2019	11-07-2019 & 12-07-2019
7	Display of consolidated Continuous Internal Evaluation (CIE) marks & Attendance	10-07-2019	15-07-2019
8	Supplementary SEE	11-07-2019 to 15-07-2019	0 17-07-2019 to 23-07-2019
9	Declaration of results	20-07-2019	27-07-2019

Dean (Academic Program)

SDM College of Engineering & Technology, Dharwad Academic Calendar for Even Semester 2018-19

B.E., M.Tech & MBA

S1.	Particulars	Date	
No.		09-01-2019 to 11-01-2019	
1	Registration Commencement of Teaching	11-01-2019	
2	Last date for registration with late fee	18-01-2019	
3		18-02-2019	
4	Display of attendance Internal Assessment – IA– I	20-02-2019 to 22-02-2019	
5	Internal Assessment	28-02-2019	
6	Communication of portorization	01-03-2019	
7	Last date to drop die course	09-03-2019	
	Parents Meet	15-03-2019 & 16-03-2019	
_	Ilisigilla – 2015	30-03-2019	
-	Internal Assessment – IA– II	01-04-2019 to 03-04-2019	
	Internal riododdinaria	06-04-2019 -	
-	Communication of performance to the parents	10-04-2019	
		22-04-2019 to 27-04-2019	
	Feedback by Students Internal Assessment –IA– III	30-04-2019 to 03-05-2019	
		03-05-2019	
1.0	Last day of teaching for Every	06-05-2019 to 18-05-2019	
. I	Final Lab Assessments Display of consolidated Continuous Internal Evaluation (CIE) marks & Attendance for 8th eemester	07-05-2019	
9 8	Semester End Examination for 8th semester	08-05-2019 to 17-05-2019	
T	Display of consolidated CIE marks & Attendance or 2 nd , 4 th & 6 th semesters (Both for UG & PG)	08-05-2019	
	communication of performance to the parents	08-05-2019	
	roject exam for 8th semester	20-05-2019 to 25-05-2019	
Semester End Examination for 2 nd , 4 th & 6 th 20-05-20 semesters (Both for UG & PG)		20-05-2019 to 03-06-2019	
-	esults for 8th semester	30-05-2019	
+	ummer vacation	04-06-2019 to 31-07-2019	
A	nnouncement of Results for 2 nd , 4 th & 6 th emester (Both for UG & PG)	10-06-2019	

Supplementary Semester: 12-06-2019 to 27-07-2019

Commencement of next Academic Year 2019 - 20: 01-08-2019

Dan (Academic Program)

SDM College of Engineering & Technology, Dharwad Odd Semester 2017-18

Academic Calendar for B.E. (I, III, V & VII), M.Tech./MBA (III Sem)

Sl. No.	• Particulars	Date
1	Registration	24-07-2017 to 27-07-2017
2	Induction program for First Semester (Tentative)	01-08-2017
3	Teaching Commences for odd semester	28-07-2017
4	Last date for registration with late fee	04-08-2017
5	Internal Assessment - IA - I	11-09-2017 to 13-09-2017
6	Display of attendance and IA-I marks	18-09-2017
7	Communication of performance to the parents	20-09-2017
8	Last date to drop the course	22-09-2017
9	Internal Assessment - IA - II	12-10-2017 to 14-10-2017
10	Display of attendance and IA – II marks	17-10-2017
11	Communication of performance to the	19-10-2017
	Parents Teacher - Parents Meet	27-10-2017
12	Last date to withdraw the course	28-10-2017
13	Students Feedback	20-11-2017 to 25-11-2017
14	Internal Assessment - IA - III	28-11-2017 to 30.11.2017
15	Internal Assessment - IA III	01-12-2017
16	Last day of teaching for Odd Semester	04-12-2017 to 11-12-2017
17 18	Final Lab Assessments Display of consolidated Continuous	06-12-2017
19	Internal Evaluation (CIE) & Attendance Communication of performance to the	06-12-2017
19	parents	15-12-2017 to 02-01-2018
20	Semester End Examination	03-01-2018 to 15-01-2018
21.	Inter Semester Recess	13-01-2018
20 Declaration of Results		and the state of t
23	Communication of performance to the	16-01-2018
	parents by putting on website	17-01-2018 to 19-01-2018
24	err for odd semesters	A CONTRACTOR OF THE PARTY OF TH
	Commencement of Even Semeste	er: 10-01-2010

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Dean (Academic Program)

SDM College of Engineering & Technology, Dharwad - 02

Academic Calendar of Even Semester - 2017-18 for B.E. (II, IV, VI & VIII Sem), M.Tech IV Semester

	Sl. No.	Particulars	Date
	1	Registration	11-01-2018 to 12-01-2018
	2	Commencement of Teaching	16-01-2018
	3	Last date for registration with late fee	16-01-2018
	4 '	Internal Assessment –IA– I	21-02-2018 to 23-02-2018
	5	Display of attendance and IA–I marks	27-02-2018
Ī	6	Communication of performance to the parents	28-02-2018
	7	Last date to drop the course	01-03-2018
İ	8	Parents Meet	10-03-2018
	9	Insignia – 2018	23-03-2018 & 24-03-2018
10-1-0	,10	Internal Assessment -IA- II	02-04-2018 to 04-04-2018
	11	Last date to withdraw the course	07-04-2018
1	12	Display of attendance and IA-II marks	09-04-2018
l	13	Communication of performance to the parents	10-04-2018
	14	Feedback by Students	23-4-2018 to 27-04-2018
	15	Last day of teaching for Even Semester	12-05-2018
	16	TA TIT	14-05-2018 to 16-05-2018
	17	Display of consolidated Continuous Internal Evaluation (CIE) marks & Attendance for 8th semester	18-05-2016
	18	Semester End Examination for 8th semester	21-05-2018 to 30-05-2018
	19	Final Lab Assessments	21-05-2018 to 30-05-2018
	20	Display of consolidated CIE marks & Attendance for 2 nd , 4 th & 6 th semesters	21-05-2018
	21	Communication of performance to the parents	22-05-2018
	22	Project exam for 8th semester	31-05-2018 to 02-06-2018
	23	Semester End Examination for 2 nd , 4 th & 6 th semesters	04-06-2018 to 15-06-2018
	24	Results for 8th semester	09-06-2018
	25	Summer vacation	16-06-2018 to 31-07-2018
	26	Announcement of Results for 2 nd , 4 th & 6 semester	23-06-2018

Supplementary Semester: 16-06-2018 to 28-07-2018 Commencement of next Academic Year 2018-19: 01-08-2018

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Circulate among faculty/staff

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Date.

SDM College of Engineering & Technology, Dharwad 580 002

Department of Information Science & Engineering

Lesson Plan Academic year 2021-22

Semester: III

Branch: Information Science& Engineering

Duration of Semester: October 2021 to January 2021

Subject: Computer Organization and Architecture

Subject code: 18UISC304

Faculty name: Ms. Arati S. Nayak

Designation: Assistant Professor

About the course, Course plan, CTA-Evaluation method, CO, PO-PEO's. Discussion on different Online courses like NPTEL, Coursera, Udemy, Activity to be conducted, Discussion about Topics studied in last semester related to computers and architecture. Definition of Computer Organization and Architecture, Computer types, Functional units: Input unit, Memory unit Arithmetic & logic unit, Output unit, Control unit Basic Operational Concepts Bus Structures Performance: Processor clock, Basic Performance equation Pipelining & Superscalar operation, Clock rate Performance measurement Memory Locations & Addresses: Byte addressability, Big-endian & Little-endian assignments Memory Operation, Instruction & Instruction Sequencing: Register Transfer Notation Basic Instruction Types, instruction execution and straight-line sequencing condition codes, branching, Addressing Modes Assembly Language: Assembly Directives, Logic Instruction Shift & Rotate Instructions Assembly language programming Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories Internal Organization of Memory Chips- Static and Dynamic memories Structure of larger memories Read Only Memories: ROM, PROM, EPROM, EPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions, Mapping functions	Class No.	Chapter	Planned topics
conducted, Discussion about Topics studied in last semester related to computers and architecture. Definition of Computer Organization and Architecture, Computer types, Functional units: Input unit, Memory unit Arithmetic & logic unit, Output unit, Control unit Basic Operational Concepts Bus Structures Performance: Processor clock, Basic Performance equation Pipelining & Superscalar operation, Clock rate Performance measurement Memory Locations & Addresses: Byte addressability, Big-endian & Little-endian assignments Memory Operation, Instruction Sequencing: Register Transfer Notation Instructions & Programs Machine Instruction Types, instruction execution and straight-line sequencing condition codes, branching, Addressing Modes Assembly Language: Assembly Directives, Logic Instruction Shift & Rotate Instructions Assembly language programming Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories The Memory System The Memory System Read Only Memories: ROM, PROM, EPROM, EEPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,			
Functional units: Input unit, Memory unit Arithmetic & logic unit, Output unit, Control unit Basic Operational Concepts Bus Structures Performance: Processor clock, Basic Performance equation Pipelining & Superscalar operation, Clock rate Performance measurement Memory Locations & Addresses: Byte addressability, Big-endian & Little-endian assignments Memory Operation, Instruction & Instruction Sequencing: Register Transfer Notation Basic Instruction Types, instruction execution and straight-line sequencing condition codes, branching, Addressing Modes Assembly Language: Assembly Directives, Logic Instruction Shift & Rotate Instructions Assembly language programming Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories Structure of larger memories Read Only Memories: ROM, PROM, EPROM, EEPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	1	conducted, Dis	scussion about Topics studied in last semester related to computers and
Basic Operational Concepts Bus Structures Performance: Processor clock, Basic Performance equation Pipelining & Superscalar operation, Clock rate Performance measurement Memory Locations & Addresses: Byte addressability, Big-endian & Little-endian assignments Memory Operation, Instruction & Instruction Sequencing: Register Transfer Notation Basic Instruction Types, instruction execution and straight-line sequencing condition codes, branching, Addressing Modes Assembly Language: Assembly Directives, Logic Instruction Shift & Rotate Instructions Assembly language programming Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories Structure of larger memories Read Only Memories: ROM, PROM, EPROM, EEPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	2		
Introduction Bus Structures Performance: Processor clock, Basic Performance equation Pipelining & Superscalar operation, Clock rate Performance measurement Memory Locations & Addresses: Byte addressability, Big-endian & Little-endian assignments Memory Operation, Instruction & Instruction Sequencing: Register Transfer Notation Machine Instructions & Programs Basic Instruction Types, instruction execution and straight-line sequencing condition codes, branching, Addressing Modes Assembly Language: Assembly Directives, Logic Instruction Shift & Rotate Instructions Assembly language programming Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories Structure of larger memories Read Only Memories: ROM, PROM, EPROM, EPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	3		Arithmetic & logic unit, Output unit, Control unit
Performance: Processor clock, Basic Performance equation	4	T.,,	Basic Operational Concepts
Pipelining & Superscalar operation, Clock rate Performance measurement Memory Locations & Addresses: Byte addressability, Big-endian & Little-endian assignments Memory Operation, Instruction & Instruction Sequencing: Register Transfer Notation Basic Instruction Types, instruction execution and straight-line sequencing condition codes, branching, Addressing Modes Assembly Language: Assembly Directives, Logic Instruction Shift & Rotate Instructions Assembly language programming Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories The Memory System Read Only Memories: ROM, PROM, EPROM, EEPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	5	Introduction	Bus Structures
Performance measurement Memory Locations & Addresses: Byte addressability, Big-endian & Little-endian assignments Memory Operation, Instruction & Instruction Sequencing: Register Transfer Notation Basic Instruction Types, instruction execution and straight-line sequencing condition codes, branching, Addressing Modes Assembly Language: Assembly Directives, Logic Instruction Shift & Rotate Instructions Assembly language programming Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories The Memory System The Memory System Speed, Size & Cost: Cache Memories: Mapping functions,	6	1	Performance: Processor clock, Basic Performance equation
Memory Locations & Addresses: Byte addressability, Big-endian & Little-endian assignments Memory Operation, Instruction & Instruction Sequencing: Register Transfer Notation Basic Instruction Types, instruction execution and straight-line sequencing condition codes, branching, Addressing Modes Assembly Language: Assembly Directives, Logic Instruction Shift & Rotate Instructions Assembly language programming Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories The Memory System The Memory Structure of larger memories Read Only Memories: ROM, PROM, EPROM, EEPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	7	1	Pipelining & Superscalar operation, Clock rate
Byte addressability, Big-endian & Little-endian assignments Memory Operation, Instruction & Instruction Sequencing: Register Transfer Notation Basic Instruction Types, instruction execution and straight-line sequencing condition codes, branching, Addressing Modes Assembly Language: Assembly Directives, Logic Instruction Shift & Rotate Instructions Assembly language programming Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories The Memory System The Memory Structure of larger memories Read Only Memories: ROM, PROM, EPROM, EEPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	8	1	Performance measurement
Machine Instructions & Programs Memory Operation, Instruction & Instruction Sequencing: Register Transfer Notation Basic Instruction Types, instruction execution and straight-line sequencing condition codes, branching, Addressing Modes Assembly Language: Assembly Directives, Logic Instruction Shift & Rotate Instructions Assembly language programming Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories The Memory System The Memory System Structure of larger memories Read Only Memories: ROM, PROM, EPROM, EEPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	9		Memory Locations & Addresses:
Machine Instructions & Programs Basic Instruction Types, instruction execution and straight-line sequencing	10	-	Byte addressability, Big-endian & Little-endian assignments
Programs Basic Instruction Types, instruction execution and straight-line sequencing condition codes, branching, Addressing Modes Assembly Language: Assembly Directives, Logic Instruction Shift & Rotate Instructions Assembly language programming Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories The Memory System Structure of larger memories Read Only Memories: ROM, PROM, EPROM, EEPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	11		
Assembly Language: Assembly Directives, Logic Instruction Shift & Rotate Instructions Assembly language programming Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories Structure of larger memories Read Only Memories: ROM, PROM, EPROM, EEPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	12		Basic Instruction Types, instruction execution and straight-line sequencing
Shift & Rotate Instructions Assembly language programming Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories The Memory Structure of larger memories Read Only Memories: ROM, PROM, EPROM, EEPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	13		condition codes, branching, Addressing Modes
Shift & Rotate Instructions Assembly language programming Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories Structure of larger memories Read Only Memories: ROM, PROM, EPROM, EEPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	14		Assembly Language: Assembly Directives, Logic Instruction
Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories Structure of larger memories Read Only Memories: ROM, PROM, EPROM, EEPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	15		
Some Basic Concepts, Semiconductor RAM Memories Internal Organization of Memory Chips- Static and Dynamic memories Structure of larger memories Read Only Memories: ROM, PROM, EPROM, EEPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	16		Assembly language programming
The Memory Structure of larger memories Read Only Memories: ROM, PROM, EPROM, EPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	17		
20 System Read Only Memories: ROM, PROM, EPROM, EPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	18		Internal Organization of Memory Chips- Static and Dynamic memories
20 System Read Only Memories: ROM, PROM, EPROM, EPROM, Flash memory Speed, Size & Cost: Cache Memories: Mapping functions,	19	The Memory	Structure of larger memories
22 Mapping functions	21		Speed, Size & Cost: Cache Memories: Mapping functions,
	22		Mapping functions

23		Page replacement algorithms
24		Performance considerations: Interleaving, Hit Rate & Miss Penalty
25		Arithmetic and Logic Unit
26		Multiplication of Positive numbers
27		Signed-Operand Multiplication - Booth Algorithm
28	Arithmetic	Signed-Operand Multiplication- Fast Multiplication- Bit-pair Recoding of Multipliers
29	Anumene	Integer division
30		Integer division
31		Floating point operations-representations
32		Floating point operations-operations
33	Basic	Some Fundamental Concepts: Register Transfers
34	Processing	Performing an Arithmetic of Logic operation
35	-	fetching a Word from Memory, Storing a Word in Memory
36	-	Control signals for various operations-examples
37	_	Branch instruction: Multiple-Bus Organization
	Pipelining and	Basic concepts
38	Large	Types of hazards
39	computer	Unit forms of Parallel Processing
40	Systems:	Unit forms of Paramet Processing

Planned Portion for IAs			
IA-Test No	Planned Action	Equivalent Hours of Teaching	
IA - I	Introduction, Machine Instructions & Programs	12 Hours	
1A- 11	Machine Instructions & Programs, Arithmetic, The Memory System	15 Hours	
lA- lll	The Memory System, Basic Processing, Pipelining and Large computer Systems:	12 Hours	

Course Instructor

(Ms. Arati S.Nayak)

(Dr. Jagadeesh D. Püjah)

SOM College of Lin

SDM College of Engineering & Technology, Dharwad – 580 002

Department of Information Science & Engineering

Lesson Plan

Academic year 2021-22

Semester: III Branch: Information Science& Engineering Duration of Semester: October 2021 to January 2021 Subject: Data Structures Lab

Subject code: 18UISL305

Faculty Name: Ms. Arati S. Nayak , Mr. Panveer Ahmed.

Designation: Assistant Professor

Class	Chapter	Planned topics
No.	About the course,	Course plan, CTA-Evaluation most 1, Go
1	conducted, Discus	sion about the Topics studied in last semester related to C programming. To Calculate Standard Deviation. To Check Prime or Armstrong Number Using User-defined Function.
	(Use of Looping construct, modular	To Find Largest Element in an Array.
	approach)	String Manipulations without using Library Functions.
2		Sort strings in the dictionary order.
	\	To Multiply two Matrices by Passing Matrix to a Function.
3		To demonstrate the difference between structures and unions. To demonstrate dynamic memory allocation of structures.
		To add two distances in the inch-feet system.
		To Swap Numbers in Cyclic Order Using Call by Reference.
		To create nodes dynamically and to perform insertion operations.
4	Singly Linked List	To perform deletion operations.
5	LIST	Fo perform different operations (for ex. largest, prime check) on singly linked list.
		To create nodes dynamically and to perform insertion operations.
6	Doubly linked	To perform deletion operations.
	list	To reverse a doubly linked list.
7		Fo perform different operations (for ex. sorting, largest, prime check) on

doubly linked list.

To evaluate postfix expression.

and To reverse a word.

Stack

Applications

		CPU scheduling THO.	was ques
0	Applications	Handling of interrupts -Priority Queue ((primity apraise)
		CPU scheduling -Round Robin	((rentar glucu)
10		implementing both stacks and queues.	
	D	Reverse a sentence using recursion	
11	Recursion	Program to Find G.C.D Using Recursion	
12		To create Binary Search Tree and to perform tree.	operations on the constructed
12	Tree	To search for a key in Binary Search Tree.	
13		To print the maximum and minimum element	t in BST.

Planned Portion for CTA		
Task Assigned	Planned Action	
Test 1	Basic C concepts	
Quiz - Pest 2	Basic C concepts, Pointers, Structures and Union, Singly Linked List, doubly linked list, Stack and Applications, Queue, Type of queue and Applications, Recursion, Tree	
Lab Performance	Continuous lab evaluation.	

Ms. Arati S.Nayak
Mr. Tanweer Ahmed

Department of Lee Department of Information Science & Engineering

Lesson Planning

Semester: VII Semester Academic year 2020-21

Duration of Semester: Oct 2021 to Jan 2022 Branch: Information Science& Engineering

Storage Management Subject code: 18UISC701

Faculty name: Dr. Anita Dixit

Designation: Asst Profe

Class No.	Chapter	Planned topics
1		Introduction to Information Storage
		Management – Evolution of Storage Technology
2		Data Centre Infrastructure-Key
		Challenges in managing Information
3		Data Center Environment: Application, Database Management System(DBMS), Host(compute), Connectivity, Storage, Disk Drive Components,
4	Unit-I	Disk Drive Performance, Host Access to Data
5		Direct-Attached Storage, Storage Design Based On Application,
		Disk Native Command Queuing, Introduction to Flash Drives
6	-	VMwore ESVi
7	-	Puta Protection: PAID: RAID Implementation Methods
9	-	DAID A groy Components RAID Techniques, RAID Tevers
9	+	RAID Impact on Disk Performance, RAID Comparison,
10		
		Intelligent Storage Systems: Components of an Intelligent
11		Storage System, Storage Provisioning
		Types of intelligent Storage Systems, Concepts in Practice: EM
		Symmetrix and VNX.
12		
		Fibre Channel Storage Area Networks: Fibre Channel: Overview,
13		The SAN and Its Evolution
13		CROCANI PO C
	Unit-II	Components of FC SAN, FC Connectivity, Switched Fabric
14		Ports, Fibre Channel Architecture,
• •		Estric Services Switched fabric Login Types Zoning EC SAN
15		Fabric Services, Switched fabric Login Types, Zoning, FC SAN SAN Topologies, Virtualization in SAN
16		SAN Topologies, virtualization in SAN
16		Concepts in Practice: EMC Connectrix
17		Concepts in Fraction. Exite connectify
		EMC VPLEX
18		
		FeeE: SCSL FCID

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20		L LG Capara hurnoga	January 3
31		Network-Attached Storage: General-purpose S	servers versus
21		Devices Benefits of NAS, File Systems and network F	ile Sharing
22		Components of NAS, NAS I/O Operation, NA	AS Implementations
23		NAS File-Sharing Protocols, factors Affecting	NAS Performance.
24		File-Level Virtualization,	
25		Concepts in Practice: EMC Isilon and EMC V	/NX gateway
26	Unit-III	Object-Based and unified Storage: Object-Based	sed Storage Devices
27		Content- Addressed Storage, CAS use Cases, u	inified Storage,
28		Concepts in Practice: EMC atoms, EMC VN	K, and EMC centera
29		Introduction to Business Continuity. Informat	ion Availability
30		BC Terminology, BC Planning life Cycle,	
31		Backup and Archive : Backup Purpose, Back	up Considerations
32		Backup Granularity, RecoveryConsiderations	s, Backup Methods
	-	Backup Architecture, Backup and Restore Or	peration,
34		Backup Topologies, Backup in NAS Environ Targets	ments, Backup
35	7	Data Dedupulication for Backup, Backup in	Virtualized
		Environments	v irtualized
36		Backup in Virtualized Environments, Data A Solution Architecture	rchive, Archiving
37	Unit-IV	Concepts in Practice: EMC Networker, EMC Data domain	C Avamar, and EMC
38		Uses of Local Replicas, Replica Consistency Technologies, Tracking Changes to Source a and Restart Considerations, Creating Multip	and Daulius D
39		Local Replication in Virtualized Environme Practice: EMC Time Finder. Remote Replic Remote Replication, Remote Replication Te	nt, Concepts in
40		Three-Site Replication, Data Migration Replication and Migration in a Virtu Concepts in Practice: EMC SRDF, EMC NRecover Point	Solutions, Remote
41		Securing the Storage Infrastructure: Inform Framework	ation Security
42		Risk Triad, Storage Security	
43		Domains, Security implementations in Stor	rage Networking
44	Unit-V	Securing Storage Infrastructure in Virtualize Environments	zed and Cloud
45		Concepts in practice: RSA and VMware S	•
46		Managing the Storage Infrastructure: Mon	ecurity
		mmastructure,	
47		Management Activities, Storage Infrastruc	cture
48	- 1	Management Challenges,	

US N 49	Developing an Idea Solution,
50	Information Lifecycle Management, Storage Tiering
51	Storage Infrastructure Management Activities
52	Case study on storage infra structure

	Planned Portion for IAs	Equivalent Hours of Teaching
IA – Test No	Planned Action	
I-IA	Introduction to Information Storage, Storage system environment, Data Protection,	15
II-IA	Intelligent storage system. Charge area network, Network Attached	20
II-IA	storage content address storage, storage virtualization back up and archive,	17
III-IA	Local replication, storage security and management managing the storage infrastructure	

Course Instructor
(Dr. Anita Dixit)

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(Dr. Jagadeesh D. Pujari)

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SDM College of Engineering & Technology, Dharwad - 02

Department of Information Science & Engineering

Lesson Planning Academic year 2021-22

Semester: V1 Semester

Branch: Information Science& Engineering

Duration of Semester: October 2021 to January 2022

Subject: Management Entrepreneurship and IPR

Subject code: 18UHUC500

Faculty name: Dr. Vandana Shreenivas Bhat

Designation: Assistant Professor

Class No.	Unit	Planned topics	
1	1-Management	Introduction, Definition, Importance, Nature and Characteristics of Management.	
2		Management Functions, Roles of Manager, Levels of Management.	
3		Managerial Skills.	
4		Management & Administration, Management as a Science, Art & Profession.	
5		Planning- Nature, Importance and Purpose of Planning,	
6		Types of Plans.	
7		Steps in Planning.	
8		Limitations of Planning.	
9		Decision Making, Meaning.	
10		Types of decisions.	
11		Steps in decision making.	
12	II-Organizing and Staffing	Introduction, Meaning, Nature and Characteristics of Organization.	
13		Process of Organization.	
14		Principles of Organization, Departmentalization.	
15		Committees – meaning, Types of Committees.	

Department of Information Science & Engineering

37		Sickness in SSI sector, Problems for Small Scale Industries.
36		Growth and Performance of Small Scale Industries in India,
35		Government policy and development of the Small Scale sector in India
34	IV-Modern Small Business Enterprises	Role of Small Scale Industries, Concepts and definitions of S Enterprises.
33		Problems faced by Entrepreneurs and capacity building for Entrepreneurship.
32		Entrepreneurial development cycle.
31		Entrepreneurial Development models.
30		Myths of Entrepreneurship.
29		Comparison between Entrepreneur and Intrapreneur.
28		Classification of Entrepreneurs.
27		Characteristics of successful Entrepreneur,
26		Importance of Entrepreneurship, concepts of Entrepreneurship
25		Entrepreneurship:-Introduction, Definition of Entrepreneur,
24		Social Audit, Business Ethics and Corporate Governance.
23		Social Responsibilities of Business towards Different Groups
22	III-Social Responsibilitie s of Business	Introduction, Meaning of Social Responsibility,
21		Techniques of Coordination. Controlling – Meaning, Steps in Controlling.
20		Communication – Meaning and Importance, Coordination- Meaning an Importance.
19		Leadership Styles, Motivation Theories.
18		Directing and Controlling- Introduction, Meaning and Nature of Directing.
16		Centralization Vs Decentralization of Authority and Responsibility. Span of Control, Nature and Importance of Staffing, Process of Selection and Recruitment.

38		Impact of Globalization on SSI, Impact of WTO/GATT on SSIs.	
39		Ancillary Industry and Tiny Industry.	
40		Institutional Support for Business Enterprises: Introduction of ISBE	
41		Policies & Schemes of Central– Level Institutions.	
42		State-Level Institutions.	
43	V-Intellectual Property Rights	Introduction Meaning and forms of intellectual property right.	
44		Copyright -Meaning of copyright, content of copy right, ownership and rights.	
45		Period of copyright, assignment and relinquishment of copyright, license	
46		Infringement of copy right, fair use, offenses and penalties	
47		Patents- Concept of patent, patentable inventions, procedure for obtaining patent	
48		Rights and obligations of patent holders, infringements and remedies, offenses and penalties	
49		Designs- Definition of design, procedure for registration.	
50		Procedure for registration, rights conferred by registration.	
51		Trademarks- Introduction-meaning of trademark- procedure for obtaining trademark.	
52		Revision of previous question papers	

	Planned Portion for IAs	
IA – Test No	Planned Action	Equivalent Hours of Teaching
I-IA	Unit –I & Unit- 2 Management, Organizing and Staffing	18 Hrs
II-IA	Unit – 3 & Unit 4 Organizing & Staffing (Entrepreneurial Development models, Entrepreneurial development cycle, Problems faced by Entrepreneurs and capacity building for Entrepreneurship.)& Social Responsibilities of Business, & Modern Small Business Enterprises.	
I-IA	Unit 4 & 5 Modern Small Business Enterprises(Institutional Support for Business Enterprises: Introduction, Policies & Schemes of Central– Level Institutions, State-Level Institutions. & Intellectual Property Rights.	16 Hrs

Date: 27-09-2021

**Course Instructor*

(Dr. Vandana S. Bhat)

HOD-ISE

(Dr. Jagadeesh D. Pujari)

Prof. & Head

SDM College of Engineering & Technology, Dhavelaght, DMARWAD-580 002.

	Planned Portion for IAs	
IA – Test No I-IA	Planned Action Unit –I & Unit- 2 Management, Organizing and Staffing	Equivalent Hours of Teaching 18 Hrs
H-1A	Unit – 3 & Unit 4 Organizing & Staffing (Entrepreneurial Development models, Entrepreneurial development cycle, Problems faced by Entrepreneurs and capacity building for Entrepreneurship.)& Social Responsibilities of Business, & Modern Small Business Enterprises.	18 Hrs
-IA	Unit 4 & 5 Modern Small Business Enterprises(Institutional Support for Business Enterprises: Introduction, Policies & Schemes of Central– Level Institutions, State-Level Institutions. & Intellectual Property Rights.	16 Hrs

Date: 27-09-2021 Willet Course Instructor

(Dr. Vandana S. Bhat)

HOD-ISE

(Dr. Jagadeesh D. Pujari)

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