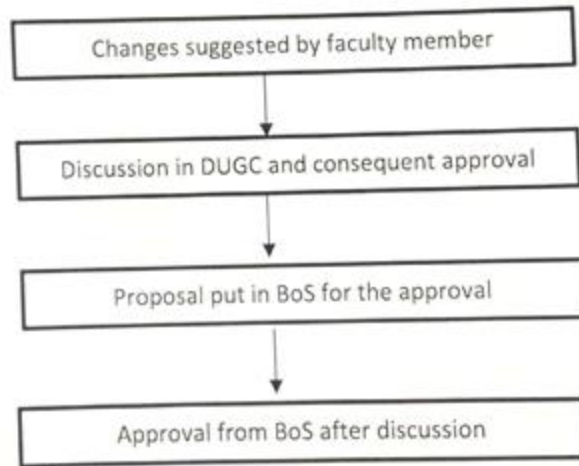


**SDM College of Engg. & Tech. Dharwad**  
**Department of Electrical and Electronics Engineering**  
*(Professional Competence with Positive Attitude)*

**Teacher's Feedback**

Every semester faculties handling the subject are allowed to change the syllabus up to 10%. The changes are discussed in the DUGC and then proposed for approval in BOS. Here is the flow of the process



Suggestion(BOS meeting date)	Action Taken (Course code, page in syllabus book)
1) To add few experiments in Control systems in "Measurement and circuit simulation Laboratory" with application of Sensors. (26/6/2021)	The control systems experiments are added in the VI semester laboratory course "Sensors and Control Systems laboratory". In VI semester(18UEEL606,pp37-38)
2)Leakage and fringing to add in Basic Electrical Engineering of I/II semester(27-7-2020)	Already present in Basic Electrical Engineering

S.D.M. COLLEGE OF ENGINEERING AND TECHNOLOGY, DHARWAD -2  
Department of Electrical & Electronics Engineering

Date: 14-03-2020

All faculty members are hereby requested to attend the meeting  
on 16-03-2020 (Monday) at 10.00 am in HOD Room.

Agenda:

1. Presentation of course syllabus assigned to individual faculty.
2. Any correction made in the III & IV semester as well as VII & VIII semester syllabus (variation of 5 to 10% syllabus)
3. Any other matter with the permission of the chair.

*S. Pankaj*  
HOD EEE

SSD	CMC	MRK	GDK	RLC	SNV
<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
SJ	VRS	SSS	NC	SUN	MSS
<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
TMT	PSV	SPA	PPK		
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**S D M College of Engineering & Technology, Dharwad**

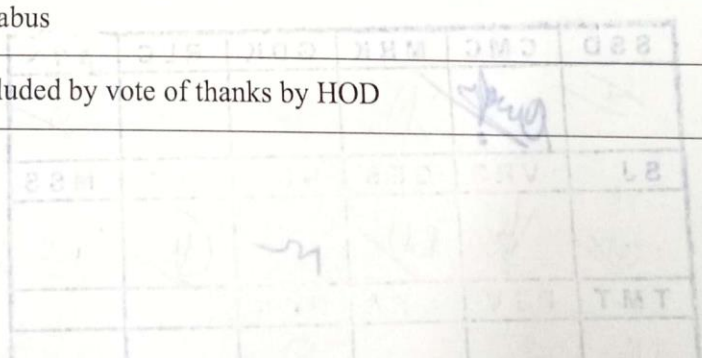
<b>Name of Dept./ Committee:</b>		Electrical & Electronics Engineering			
<b>Meeting Called by:</b>		<b>HOD</b>			
<b>Meeting Date:</b>	16/3/2020	<b>Time:</b>	10 to 11 AM 2.30 to 5.00 PM	<b>Venue:</b>	HOD's Room
	17/3/2020				
	18/3/2020				
	19/3/2020				

**Agenda:**

- [1] Presentation of course syllabus assigned to individual faculty
- [2] Any corrections made in the III & IV semester as well VII & VIII semester Syllabus (variation of 5 to 10 % )
- [3] Any another matter with the permission of the chair

**Minutes of Meeting**

<b>Agenda No.</b>	<b>Details/Decisions/Resolutions</b>
	HOD welcomed all the members
<b>1</b>	Individual faculty members have presented their assigned course syllabus of V & VI Semesters in terms of Five Units along with CO/ PO mapping in the meeting and after in depth discussion each course syllabus along with CO/PO mapping is finalized
<b>2</b>	Few faculty has submitted the minor corrections in III & IV semester as well VII & VIII semester Syllabus
	Meeting was concluded by vote of thanks by HOD



Circular

All the HoDs are requested to convene the DUGC meetings in respective departments to discuss about the restructuring of course contents of UG programs. The following guidelines shall be followed while reorganizing the course contents.

1. For 2018 scheme, the contents of all the courses shall be spread across five units of approximately equal number of teaching hours.
2. The COs definition will be the liberty of the course teacher. There can be one CO per unit syllabus or more number of COs per unit syllabus.
3. The number of hours for 4 credits course will be 52 and the range of teaching hours per unit shall be 9 -12 hours.
4. The number of hours for 3 credits course will be 39 and the range of teaching hours per unit shall be 7 - 9 hours.
5. The number of hours for 2 credits course will be 26 and the range of teaching hours per unit shall be 5-6 hours.
6. There shall be a maximum of 2 chapters within the unit. However, it is the freedom of the course instructor to decide the number of chapters and distribution of teaching hours within the unit.
7. Two questions from each unit will be set in SEE. Each question shall carry 20 marks with a maximum of 3 sub divisions.
8. The restructure of the course contents shall be done for 2018 scheme. For 2015 scheme, it is to be done for all the courses of VII and VIII semesters. Further, In 2015 scheme, for III to VI it is to be done for the courses wherein there is no equivalent course in 2018 scheme and also for the courses which have been dropped in 2018 scheme.
9. The template for course contents is sent to the email Id of all HoDs, Kindly adhere to the template.

In the case of any doubts in this regard, kindly contact the office of the Dean (Academic program).

Further,

10. It is observed by the undersigned that the students' attendance is not satisfactory in few departments. The HoDs are requested to call the parents of the students (including reregistered students) having attendance less than 60% as on date and convey them to advise their wards to be regular for the classes.
11. All the faculty and staff are requested to wear college identity card without fail during college hours.

*Deppmala*  
Principal

Copy to all HoDs

*Circulate  
Among faculty members*

*9/3/2020*

SSD	CMC	MRK	GDK	RLC	
<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	
SJ	VRS	BSS	NC	SGN	MSS
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**SDM College of Engineering & Technology, Dharwad – 02**  
Department of Electrical & Electronics Engineering

Sl. No.	Name of the faculty	Subject code	Subject Assigned	Signature
1	Prof. S. S. Desai	15UEEC503	Electromagnetic Theory	<i>JD</i>
		15UEEE852	Power System Operation & Control	<i>JD</i>
		<del>18UEEC100</del>	<del>Basic Electrical Engineering</del>	
2	Prof. C. M. Chelli	15UEEE753	Embedded Systems	<i>Chelli</i>
		15UEEE651	Computer Organization	<i>Chelli</i>
		18UEEC100	Basic Electrical Engineering	<i>Chelli</i>
3	Prof. M R Kappali	15UEEC502	Power Electronics	<i>M R Kappali</i>
		15UEEE751	Energy Auditing & Demand Side Management	<i>M R Kappali</i>
		151UEEC800	Industrial Drives and Applications	<i>M R Kappali</i>
4	Prof G. D. Kamalapur	18UEEC302	Electrical & Electronic Measurements	<i>G D Kamalapur</i>
		18UEEC403	Control Systems	
		15UEEC601	High Voltage Engineering	
		18UEEC100	Basic Electrical Engineering →	
5	Dr. R L Chakrasali	18UEEC303	Digital Electronics and Verilog	<i>R L Chakrasali</i>
		15UEEC603	Digital System Design using VHDL	<i>R L Chakrasali</i>
		15UEEE755	Computer Communication Networks	<i>R L Chakrasali</i>
		15UEEE856	Analog and Digital Communication	<i>R L Chakrasali</i>
6	Dr. S. G. Ankaliki (HOD)	18UEEC300	Network Analysis	<i>S G Ankaliki</i>
		15UEEE854	Electrical Power Quality	<i>S G Ankaliki</i>
7	Prof. Sunil Joshi	18UEEC301	Analog Electronics	<i>Sunil Joshi</i>
		15UEEC701	VLSI Circuits & Design	<i>Sunil Joshi</i>
		18UEEC401	Microcontrollers	<i>Sunil Joshi</i>
		15UEEE853	ARM Processors	<i>Sunil Joshi</i>
8	Prof. V.R. Sheelavant	15UEEE759	PIC Micro Controllers	<i>V R Sheelavant</i>
		18UEEC304	Electrical Power Generation, Transmission & Distribution	<i>V R Sheelavant</i>
		15UEEC504	Digital Signal Processing	<i>V R Sheelavant</i>
		18UEEC100	Basic Electrical Engineering	<i>V R Sheelavant</i>
		15UEEC602	Switchgear & Protection	<i>V R Sheelavant</i>
		15UEEE762	Testing & Commissioning of Electrical Equipment	<i>V R Sheelavant</i>
15UEEE857	Micro Electro Mechanical Systems (MEMS)	<i>V R Sheelavant</i>		

*Not handled*

*? Not taken*

9	Prof. B. S. Shaiavadi	15UEEC505 <del>15UEEC500</del>	Renewable Energy Sources <del>Basic Electrical Engg</del>	8/11
10	Prof. Nandakumar C	15UEEC501	Electrical Machines - II (AC Machines)	Nand
		18UEEC402	Electrical Machines - I (DC Machines & Transformers)	Nand
		15UEEE858	Reliability Engineering	Nand
11	Prof. Shravankumar Nayak	15UEEC500	Linear ICs and Applications (Not applicable)	Shr
		18UEEC404	Linear ICs and Applications	Shr
		15UEEE851	Modern Trends in Transmission Systems	Shr
12	Prof. Manjula Sureban	18UEEC400	Signals and Systems	Manjula
		15UEEE856	Power Systems Dynamics & Stability	
		15UEEE757	AI and its Applications to Power System	
		15UEEE859	Power System Dynamics and Stability	
13	Prof. Tejaswi M Timsani	15UEEE761	Power System Planning	Tejaswi
		15UEEE756	Electrical estimation, specification and costing	
		18UEEC100	Basic Electrical Engineering	
		15UHUC600	MEPIP	
14	Prof. Pradeep Vibhuti	15UEEE653	Data Structures and Algorithms	Pradeep
		15UEEE754	Digital Signal Processing Algorithms and Architecture	
		15UEEE855	Software Engineering	
		15UEEE860	Operating System	
15	Prof. Sanjeeth P A	15UEEE861	Modern Power System Protection	Sanjeeth
		15UEEC600	Power System Analysis & stability, CTPS	
16	Prof. Praveen	15UEEE760	Non-linear System Analysis	Praveen
		15UEEC801	Electrical Machine Design	
		15UEEE752	Electrical Drawing and CAD	

Note: All faculty members are hereby informed to arrange the syllabus of assigned course in 5 units and CO/PO mapping within one week.

29/12/20  
HOD EEE

SDM College of Engineering and Technology, Dharwad

Electrical and Electronics Engineering

For Circulation among the faculty only.

A meeting was conducted among the undersigned faculty members to get more clarity regarding the depth of teaching in the course " Basic Electrical Engineering; 18UEEC100/200" on 11<sup>th</sup> Feb.2020 with the permission of H.O.D. Dr. S.G. Ankiliki and the following resolutions were arrived at:

Chapter -1] DC circuits: Definitions : DC circuit , series connection , parallel connection , voltage and current division formula. Problem solving by circuit reduction method. Here circuits must not be having star and delta connected resistances.

Explanation of KVL and KCL. Definitions of loop and mesh. Solving problem on DC circuits having maximum of three meshes using mesh analysis only. Using calculator to solve the simultaneous equations is permitted.

Chapter -2] Magnetic circuits : Electromagnetic induction, Faraday's laws and applications. Statically (self induced and mutually induced emf ) and dynamically induced emf explanation. Problem solving is not included.

Definitions of magnetic circuits, magneto motive force, flux, flux density, reluctance, magnetic field intensity. Problems on series magnetic circuits with and without air gap.

Comparison of electric and magnetic circuits along with the knowledge of similarities and dissimilarities.

Derivation of lifting of magnet:  $F = B^2 A / (19.62\mu_0) \text{ Kg}$ .





Chapter -3] AC circuits : EMF equation-  $v = V_m \sin\omega t$ , Definitions: Instantaneous value, cycle, time period, frequency relation between T and f, Average value ( $V_{av}=0.636 V_m$ ), RMS value ( $V=.707 V_m$ ) No derivations, form factor. Phasor algebra meaning of j - operator. Conversions between cartesian and polar forms. Purely R, L and C circuits only to be discussed to recall the facts.

R-L, R-C, R-L-C series circuits, voltage and current, power relations derivations, waveforms, phasor representations. Real power, reactive power and apparent power concepts. Numericals on R-L, R-C, R-L-C series circuits.

Numerical on Parallel connected impedances only.

P.F. improvement using capacitor in parallel. Relevant numericals.  
Chapter-4] 3 phase AC circuits: Advantages of 3 phase systems over single phase systems.  
Explanation for 3 wire and 4 wire (star and delta) systems.  
Definitions of phase sequence, balanced supply and balanced load.  
Derivations for line and phase quantities using phasor diagram only.  
Power in 3 phase circuits . Problems on star and delta connected system to calculate line current, phase current, real power, reactive power and apparent power. Measurement of power is excluded.

Faculty members:

- 1) Prof. S S Desai. 
- 2) Prof. C M Chelli. 
- 3) Prof. V R Sheelavant 
- 4) Prof. B S Shalavadi  (Bhalavadi)

S. Ankaliki  
Head of the Department  
of Electrical & Electronics Engineering,  
S.D.M. College of Engg & Technology  
Chavtagiri, DHARWAD-589 002