

**SDM College of Engineering and Technology, Dharwad**  
**Department of Electronics and Communication Engineering**  
**III Semester Scheme**

Course Code	*Course Category	Course Title	Teaching		Examination				
			L-T-P (Hrs/Week)	Credits	CIE	Theory (SEE)		Practical (SEE)	
					Max. Marks	**Max. Marks	Duration in Hrs.	Max. Marks	Duration in Hrs.
21UECM300	BS	Engineering Mathematics-III	2 - 2 - 0	3	50	100	3	-	-
21UECC300	PC	Analog Electronic Circuits	3 - 0 - 0	3	50	100	3	-	-
21UECC301	PC	Digital Circuit Design	3 - 0 - 0	3	50	100	3	-	-
21UECC302	PC	Network Analysis	3 - 0 - 0	3	50	100	3	-	-
21UECC303	PC	Control Systems	3 - 0 - 0	3	50	100	3	-	-
21UAEE3-X	AE	<b>Ability Enhancement course</b>	2 - 0 - 0	2	50	50	2	-	-
21UAEE350	AE	Basics of Data Science							
21UAEE351	AE	Linear ICs and Applications							
21UHUC300	HU	Universal Human Values-I	2 - 0 - 0	2	50	50	2	-	-
21UECL305	PC	Analog Electronic Circuits Laboratory	0 - 0 - 3	1.5	50	--	-	50	3
21UECL306	PC	Digital Circuits Design Laboratory	0 - 0 - 3	1.5	50	-	-	50	3
21UHUC301	***HU	Kannada	2 - 0 - 0	1	50	50	2	-	-
21UMBA301	****BS	Mathematics	3 - 0 - 0	Audit	50	-	-	-	-
Total			<b>20/23 - 2 - 6</b>	<b>23</b>	<b>550</b>	<b>650</b>		<b>100</b>	

\* BS- Basic science ES- Engineering Science HU- Humanities, languages and Management AE- Ability enhancement course PC- Program core

\*\* Semester End Examination conducted for 100 marks will be reduced to 50 marks

\*\*\* Students of all branches will be divided into 2 groups, and each group will take either CIPE or Kannada in 3<sup>rd</sup> and 4<sup>th</sup> semester respectively.

\*\*\*\* Bridge course on Mathematics for Lateral entry students.

21UAEE3 - X : “-“ is the number assigned to the department. 1- CV, 2-CSE, 3-CH, 4-EE, 5-EC, 6-ISE and 7-ME ( Assuming departments offer different Ability Enhancement course for their students).

### IV Semester Scheme


Course Code	*Course Category	Course Title	Teaching		Examination				
			L-T-P (Hrs/Week)	Credits	CIE	Theory (SEE)		Practical (SEE)	
					Max. Marks	**Max. Marks	Duration in Hrs.	Max. Marks	Duration in Hrs.
21UECM400	BS	Engineering Mathematics-IV	3 - 0 - 0	3	50	100	3	-	-
21UECC400	PC	Communication Theory	3 - 0 - 0	3	50	100	3	-	-
21UECC401	PC	Digital Signal Processing	3 - 0 - 0	3	50	100	3	-	-
21UECC402	PC	HDL Programming	3 - 0 - 0	3	50	100	3	-	-
21UECC403	PC	ARM Processor	3 - 0 - 0	3	50	100	3	--	--
<b>21UHUA400</b>	<b>***HU</b>	<b>Constitution of India and Professional Ethics</b>	<b>2-0-0</b>	<b>Audit</b>	<b>50</b>	<b>--</b>	<b>--</b>	<b>--</b>	<b>--</b>
<b>21UHUC402</b>	<b>HU</b>	<b>Universal Human Values-II</b>	<b>2 - 0 - 0</b>	<b>2</b>	<b>50</b>	<b>50</b>	<b>2</b>	<b>--</b>	<b>--</b>
21UECL404	PC	Digital Signal Processing Laboratory	0 - 0 - 3	1.5	50	--	--	50	3
21UECL405	PC	HDL Programming Laboratory	0 - 0 - 3	1.5	50	--	--	50	3
21UECL406	PC	Introductory Project	0-0-2	1	50	--	--	--	--
21UMBA401	****BS	Mathematics	3-0-0	Audit	50	-	-	-	-
Total			<b>18/21 -2 -8</b>	<b>21</b>	<b>550</b>	<b>550</b>		<b>100</b>	

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\*\* Semester End Examination conducted for 100 marks will be reduced to 50 marks

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\*\*\*\* Bridge course on Mathematics for Lateral entry students

  
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