SDM College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Date: 25/03/2017

Report of the BOS MEETING-2017

The BOS meeting of the year 2017 was convened on 25/3/2017 for UG &PG rogrammes of our Department. We invited all the 6 External BOS members for the BOS neeting. 3-BOS members had attended the meeting. They are 1) Dr. U Sripathi, NITK Surtakal) Dr.Kumarswami, SIT, Tumakur and 3) Dr. R.B.Shettar, BVBCET, Hubli. However, other 3- nembers had expressed their inability to attend meeting.

Dr. G.A.Bidkar, Chair person of the meeting welcomed the members and briefed about he achievements made by our students and faculty in the last one year. As a part of preliminary preparation for BOS meeting, the department had requested our faculty members to prepare cheme and syllabus of various semester of UG & PG programmes. They had prepared ccordingly. The same was presented before external and internal BOS members for discussion nd approval. We also took approval for PO's of PG programme (Digital Electronics).

All the members especially external members went through the scheme and syllabus meticulously and suggested few changes /modifications which have been recorded in the minutes of meeting.

First of all, the committee reviewed the scheme and syllabus of M.Tech (Digital Electronics). They also suggested to fine tune the syllabus under different specialization like VLSI, Signal Processing and Communication. This is the first time review of M.Tech Syllabus after department getting autonomous status to PG programe.

Later, they also reviewed the scheme/ syllabus of BE(ECE) programme and suggested few changes/modifications. Overall they appreciated the effort made by our faculty members with regard to preparing scheme/syllabus and approved the same with few changes/modifications.

Last but not least, Dr.S.S.Kerur, BOS Member Secretary thanked all for their continued support and help.

Dr.S S Kerur
BOS, Member Secretary

Dr.G A Bidkar
Chairperson of BOS and HOD
Prof. & Head
Dept. of Electronics & Communication Engg
SDM College of Engineering & Technology
Dharwad-580 002

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SDM College of Engineering & Technology, Dharwad-02

Department of Electronics & Communication Engineering

ATTENDANCE SHEET

BOS MEETING 25.03.2017

BOS meeting is convened on 25.03.2017 in Main Laboratory. The following faculty members attended the same.

Sl.N	Name of the faculty	Signature
1	Prof. Dr.Vijaya C	
2	Dr. G.A Bidakar	
3	Dr. Shreedhar A.Joshi	
4	Dr.Satish S. Bhairannawar	
5	Mrs. Savitri Raju	
6	Dr. Hemalata V. Bhujle	
7	Dr. S.S.Kerur	
8	Mr. K.N.Hosur	
9	Dr. S.V.Viraktamath	
10	Mrs. Jayashree.C.Nidagundi	
11	Mrs. Sharada C. Sajjan	
12	Mr. S.S.Navalgund	
13	Mrs. R.H.Korti	
14	Mrs. Mala L.M	
15	Mr.Vinayak Miskin	
16	Mr. Mahendra M.Dixit	
17	Mrs. Sumangala Bhavikatti	
18	Mrs. Reshma Nadaf	
19	Mrs.Preeti S.Bellerimath	
20	Mr. Raghuram K.M.	
21	Mr. Sunil S.Mathad	

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ATTENDANCE SHEET

BOS MEETING

25.03.2017

22	Mr. M.Vijay Kumar	
23	Mr. Bairu K Saptalakar	
24	Mr. S.S Ravishankar	
25	Mr. Kotresh M.	
26	Mr. Shrikanth K.Shirakol	
27	Ms. Channakka Lakkannavar	
28	Mrs. Sangeetha Gouda	
29	Mr. Vyas R Murnal	
30	Ms. Megha S	
31	Ms. Suchita H.S.	

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SDM College of Engineering & Technology, Dharwad-02
Department of Electronics & Communication Engineering

ATTENDANCE SHEET

BOS MEETING **25.03.2017**

BOS meeting is convened on 25.03.2017. The following are the External and Internal BOS members attended the meeting.

EXTERNAL MEMBERS – (BOS)

S.No.	Name	Representation in BOS	Contact Details	Signature
1	Dr. R Kumaraswamy.	Member	Professor, Dept. of E&CE SIT, Tumkur Mobile: 9480344624 Email: hyrkswamy@gmail.com	R. 25/3/17
2	Mr. Govind S. Kulkarni	Member	Manager Operations, CISCO, Bangalore Mail Stop BGL12/1 Cessna Business Park, Kadubeesanahalli Varthur Hobli, Sarjapur Marathalli ORR BANGALORE, Mobile: 9880475121 Email: govinkul@cisco.com	Ab
3	Mr. Dharamraj M.N	Member	Design Architect, Intel Mobile Communications India Pvt. Ltd. Unit 1, 10th Floor, Discover Building, Intel Tech Park, Whitefield Mobile:9880396249 Email:dharmaray.nedalgi@intel.com	Ab
4	Dr.U. Sripati	Member	Professor, Dept. of E&CE National Institute of Technology Karnataka, Surathkal, Mangalore - 575 025, Karnataka, India Mobil: 9845056189 Email: sripati_acharya@yahoo.co. in, sripati.acharya1@gmail.com	U Sripati
5	Dr.R.B.Shettar	Member	Professor,Dept.,of BVBCET, Hubli 9448745669(M), raj@bvb.edu	
6	Dr. Harish Kittur	Member	Professor, Department of VLSI, VIT University, Vellore, Tamil Nadu 09566814544. harish13579@rediffmail.com harish.kittur@yahoo.com or hmkittur@gmail.com	Ab

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S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Date: 28/03/2017

Minutes of Meeting – BOS (25/03/2017) [UG Programme]

Flow of BOS meeting:

- External BOS Members
  1. Dr. Sripathi, NITK, Surathkal [Dr. SP]
  2. Dr. Kumarswamy, SIT, Tumkur [Dr. KS]
  3. Dr. Rajashekar B. Shettar, BVBCET, Hubli [Dr. RBS]
- Welcome of all BOS members and Faculty by Dr. G.A. Bidkar, HOD, ECE
- Self Introduction of Faculty
- Agenda:

  Approval of Vision and Mission, PO's and PSO's of PG

  PG Scheme and Syllabus discussion

  UG Scheme and Syllabus discussion
- Valedictory :

  Address by Dr. S.S. Kerur, Member Secretary, BOS

  Views by External BOS members

  Views by HOD

  Vote of Thanks - Dr. S.S. Kerur, Member Secretary, BOS

Minutes of meeting of BOS held on 25/03/2017(UG) :

1. Dr. G.A. Bidkar welcomed the BOS committee members and faculty. He briefed about the department and achievements of faculty and students.

2. Faculty introduced themselves in front of BOS committee.

3. PG course vision, mission and Scheme & Syllabus were discussed rigorously. Change in content of some subjects suggested.

4. UG Scheme and Syllabus discussion

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Irst Year:

Subject:- Basic Electronics

- Dr. SP suggested to include AM and FM broadcast topics in Chapter7 and to include applications of op-amp as motivating component for students.

- Dr. RBS suggested to remove "Introduction to IC's" in chapter 6.

Second Year:

III-Semester

Subject:- Analog Electronic Circuits

- Dr. RBS suggested to include MOSFET's instead of BJT's and their syllabus will be sent and suggested to focus on lab component including MOSFET's

- Dr. SP suggested to change the reference books

Subject:- Network Analysis

- Dr. SP suggested to change the reference books and their syllabus will be sent for ref.

- Dr. RBS felt the content was heavy from student point of view

Subject:- Digital Circuit Design

- Dr. SP suggested to update the reference books and their syllabus will be sent for ref.

- Also some topics in Logic levels and families to be changed i.e. various logics

Subject:- Signals and Systems

- Dr. SP suggested to update the reference books and their syllabus will be sent for ref.

Subject:- Electromagnetic Theory

- Dr. SP suggested Co-ordinate systems and transformation from on c.s. to another, Coulomb's law, Electric Field Intensity should be given less emphasis

- Also he suggested to focus more on Time Varying Fields, Maxwell's Equations and include Wave Propagation as a chapter.

- Dr. SP suggested to update the reference books and their syllabus will be sent for ref.

Subject:- AEC Lab

- Dr. RBS suggested to include experiments related to MOSFET's instead of BJT's

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IV-Semester

Subject:- Analog Communication

- Dr. SP suggested to remove some topics such as TV fundamentals, color tv in chapter5

- He suggested to include concepts of Noise Figure, Noise Temperature etc..

- Suggested to refer books authored by 'Proakis & Salehi' and 'bernard sklar'

Subject:- HDL Programming Using Verilog

- Dr. RBS suggested to include Project component

Subject:- LIC and Communication Lab

- Dr. SP suggested to include some experiments to validate sampling theorem, nyquist theorem and also include experiments on Push-pull, Class-A, Class-B Amplifiers

- Also suggested to include experiments to build a small system for example such as Frequency Synthesizer using PLL, Applications of PLL etc...

- Also suggested to include reference book authored by 'taub and schilling'

Subject:- HDL Programming Lab

- Dr. RBS suggested to upgrade to Spartan 6/7 boards

- Also emphasized on inclusion of more interfacing experiments and application oriented experiments

Third Year:

- Faculty discussed to include some subjects by removing some subjects – not clear

- Also faculty discussed to interchange Laboratories between V and VI semesters – not clear

- Committee stressed on offering Stream Wise Electives for students, so that students can opt electives in Communication, VLSI, Signal Processing fields

V-Semester

- Discussion: Inclusion of "IC Fabrication Technology" Elective during V-sem

- Discussion: Inclusion of "Image Processing" Elective during V-sem, instead of "Pattern Recognition" i.e Swapping of subjects

- Discussion: Shifting of ARM Lab to 5th semester

- Finally committee left the decision for the department to take suitable action based on the Resources and Faculty Strength available

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Subject:- Digital Communication

- Dr. SP suggested to refer books authored by 'John Proakis & Salehi' and 'bernard sklar'

Subject:- CMOS VLSI Design

- Dr. RBS told to rethink about chapter-01, because of repetition in other subject

- Also told he will be sending their syllabus for reference and suggested to use book written by Campbell for Fabrication Chapter

Subject:- Advanced Digital System Design

- Committee were of the opinion to merge and reframe chapters-2&6

Subject:- Digital Switching Systems

- Committee were of the opinion to reduce teaching hours for chapter-02 or remove it

→ Inclusion of WSN's subject discussed

Subject:- System Verilog

- Dr. RBS told to update the reference books

Subject:- Pattern Recognition

- Swapping with subject "Image Processing"

Subject:- Solid State Devices and Technology

- Dr. RBS pointed that there is no technology part in the subject and some topics/content found to be irrelevant

- Suggested to change the content and float the same elective or different one.

- Also told he will be sending syllabus for reference

VI-Semester

Subject:- Microwave and RADAR

- Committee felt the syllabus heavy from students point of view as a single semester course

Subject:- Satellite Communication

- Dr. SP suggested to remove Chapter-02

- Also he suggested to include this elective in 7^{th} or 8^{th} semester

- Also suggested to include electives like WSN's here

Subject:- Digital Signal Compression

- Dr. SP suggested to update reference books

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Fourth Year:

VII-Semester

- Dr. RBS suggested to remove "Power Electronics" from elective list and include other electives like "Verification & Testing of VLSI Designs" and any other electives can be added

- Swapping of subjects "AWP" to 6th semester and "Satellite Communication" to 8th semester

- Moving of "Adaptive Signal Processing" to Elective-V and "OFC" to Elective-VI

Subject:- ASIC Design

- Dr. RBS suggested to include Xilinx and Altera devices in chapter-07

Subject:- Antenna & Wave Propagation

- Dr. SP suggested to include reference books by the author "Balanis"

Subject:- Power Electronics

- This elective to be removed

- Dr. SP suggested Chapter 3&4 can be removed and other topics relevant to ECE can be included

Subject:- Automotive Electronics

- Committee suggested to reduce the contents in the syllabus and take approval from KPIT

VIII-Semester

Subject:- Wireless Communication

- Remove topics related to 1G in Chapter-01

Subject:- High Speed System Design

- Dr. RBS suggested to remove this and think of any other alternative subject

Subject:- Error Control Coding

- Committee suggested to remove convolutional codes and include LDPC codes

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5. **Valedictory Remarks** – by Dr. S.S. Kerur

6. **Few Words by External BOS Members**

i. Dr. Sripathi:

- He expressed his happiness by saying that the content, design, organization of syllabi was good
- He advised to include Mini-project spanning from 3rd to 7th semester as credit course
- He suggested peer to peer mentoring among students
- Involvement of faculty in R&D work of Societal Concern

ii. Dr. Rajashekar B. Shettar:

- He suggested to include "Digital Control Systems" Course at PG level

iii. Dr. Kumarswamy:

- Advised for Stream Wise Electives
- Highlighted the use of Autonomy for the growth of department and institute

7. **Views by HOD:**

- Regarding BOS
- Thanked all the members and faculty for smooth & successful conduction of BOS

8. **Vote of Thanks:** by Dr. S.S. Kerur

- Thanked all the members and faculty for smooth & successful conduction of BOS.

Dr. S S Kerur
BOS (Member Secretary)

Dr.G A Bidkar
HOD & Chairman(BOS)

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SDM College of Engineering & Technology, Dharwad-02

Department of Electronics & Communication Engineering

CIRCULAR

Date:17.05.2018

Annual BOS Meeting for the year 2018-19 of the Department of Electronics and Communication engineering will be held **on 19.05.2018 at 9.30AM** in our department. All the faculty and BOS members (Internal & External) are requested to attend the same. The lunch is arranged at Digital Electronics Laboratory (1.15PM) for all the faculty and BOS members and Non-Teaching staff members. All are requested to join for the same.

Agenda of the meeting:

1. Welcome by the Chairperson and introduction about department and self introduction of all the members.
2. To read and confirm the proceedings of previous meeting held on 25.03.2017.
3. Review of I / II semester Basic Electronics syllabus (up to 10%)- UG Course.
4. Review of III / IV semester syllabus (up to 10%) and also introduction and deletion of new subjects - UG Course.
5. Review of V / VI semester syllabus (up to 10%) and also introduction and deletion of new subjects - UG Course.
6. Review of VII / VIII semester syllabus (up to 100%) and also introduction and deletion of new subjects - UG Course.
7. Review and syllabus framing of PG Course (Digital Electronics) and approval for the syllabus of PG course for the year 2017-18 and also PO and PSO.
8. Any other matter with permission of the Chairperson.
9. Member Secretary read the proceedings of the meeting and also propose vote for thanks.

Member Secretary,
Prof. Dr. S. S. Kerur

HOD & Chairperson of BOS
Prof. Dr. G. A. Bidkar

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S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Date: 22/05/2018

Minutes of Meeting – BOS (19/05/2018)

Flow of BOS meeting:

- External BOS Members
  1. Mr. Dharmaraj M.N., Intel, Bengaluru [Mr. DMN]
  2. Dr. Rohini, NIE, Mysore [Dr. R]
  3. Dr. Shiparamatti, BEC, Bagalkot [Dr. S]
  4. Dr. Prashanth Patavardhan, GIT, Belgavi [Dr. PP]
  5. Mr. Manjunath Koti, Robert Bosch, Bengaluru [Mr. MK]
  6. Mr. Sameer Kulkarni, L&T, Bengaluru [Mr. SK]
- Welcome of all BOS members and Faculty by Dr. G.A. Bidkar, HOD, ECE
- Self Introduction of Faculty and External BOS members
- Agenda:

  UG Scheme and Syllabus discussion

  PG Scheme and Syllabus discussion
- Valedictory :

  Address by Dr. S.S. Kerur, Member Secretary, BOS

  Views by External BOS members

  Views by HOD

  Vote of Thanks - Dr. S.S. Kerur, Member Secretary, BOS

Minutes of meeting of BOS held on 19/05/2018(UG) :

1. Dr. G.A. Bidkar welcomed the BOS committee members and faculty. He briefed about the department and achievements of faculty and students.

2. Faculty introduced themselves in front of BOS committee.

3. Approval of modified PEO's.

4. UG Scheme and Syllabus discussion.

5. PG Scheme and Syllabus discussion.

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First Year:

Subject:- Basic Electronics

- Dr. R suggested to remove semiconductor diodes topic and stressed to include topics on applications of electronics. Also suggested to include self learning component on applications of electronics. Suggested to remove tutorial.

- Dr. KNH attended the meeting at VTU and mentioned about the proceedings of the meeting regarding changes in the syllabus. Changes are removal of large signal amplifiers, removal of module 3 , removal of principle of mobile phone in module 5 and inclusion of Communication Systems.

- Dr. S suggested to remove basic logic gates topic.

- Dr. P said to refine the syllabus to match the credits and plan to accommodate orientation program of 3weeks. Suggested to follow guidelines of VTU.

Second Year:

III-Semester

Subject:- Engineering Mathematics - III

- Mr. MK and Mr. SK suggested to stress on the applications and importance of Fourier Series and Fourier Transform, Z-transform.

Subject:- Analog Electronic Circuits

- Mr. DMN suggested to remove the JFET of chapter 3 & 4 and include MOS related or any other topics of relevance.

- Dr. R suggested to include self learning component.

Subject:- Network Analysis

- Mr. MK suggested to include factor of tolerances and effect of temperature w.r.t. R, L & C circuits.

Subject:- Digital Circuit Design

- Mr. DMN pointed out vast content in chapter 6 : logic families and suggested to look into it.

- Mr. P is of the opinion to allot mini-project for students individually.

Subject:- Signals and Systems

- Dr. R suggested to work on the MATLAB tool / SCILAB for some applications and carry the same to DSP. Suggested 3-2-0 for both S&S and DSP.

- Dr. S suggested removal of solving differential and difference equations in time domain representation.

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Subject:- Electromagnetic Theory

- Dr. R suggested to make the students register for NPTEL course and allot CTA marks for the same.

Subject:- AEC Lab

- Dr. R suggested to club Hartley and Colpitt's as one experiment. Also suggested to give small projects for students from various magazines such as EFY.

- Mr. DMN is of the opinion to remove JFET related experiments.

IV-Semester

Subject:- Control Systems

- Dr. PP suggested to remove Nyquist Plot

Subject:- LIC's and Applications

- Mr. DMN found the content of the syllabus very vast.

Subject:- Microcontroller

- Dr. PP shared the experience of their college. They cover 8085 and 8051briefly and move towards advanced processors and controllers

- Dr. R was of the opinion to make the students work on various tools such as Galileo, Arduino etc..

Subject:- HDL Using Verilog

- Mr. DMN expressed his views, whether topics are covered at physical design level or not.

Third Year:

V-Semester

- Mr. MK suggested to make OS as core, as RTOS is core in higher sem.

- Dr. PP suggested to include Advanced C and C++ for all branches.

- Dr. R suggested to keep S&S in 4^{th} sem and DSP in 5^{th} sem as there will be continuity.

- Mr. MK suggested to include Artificial Intelligence as a separate subject along with Fuzzy Logic.

- Dr. R suggested to have workshops on AI, ML and DL through student chapters (IEEE) for students.

- Dr. PP and Mr. SK were of the opinion to include 'Python' as a separate subject and 'C' may be removed.

- Dr. PP suggested to float IoT as an elective. Also shared the experience on how electives are opted in their college using whatsapp group and google forms.

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- Dr. R stressed on the sequencing of electives in various semesters stream wise.

Subject:- Microcontroller & VLSI Laboratory

- Dr. S suggested for flash board experiments to be included.

- Restart numbering after 5th experiment

Subject:- Digital Switching Systems

- Dr. R suggested to include laboratory component for digital switching and wi communication

Subject:- Operating System

- Mr. SK suggested that students must be asked to design some small OS using 'C'.

VI-Semester

- Dr. PP shared his experience that one of the subject 'Soft Computing' is formed and offered elective consisting of Fuzzy, ANN, Genetic Algorithm in their college. One case study and implementation is also covered. Also suggested to include AI in 7th semester as elective.

Subject:- Management, Entrepreneurship and IPRs

- Dr. PP suggested to include Payroll component

Subject:- ARM Processor

- Committee suggested to reduce the number of chapters around 6 to 8 chapters.

Subject:- Analog and Mixed Mode VLSI Design

- Change LTP to 4-0-0

- Dr. DMN: Elaborate on first four chapters only. Chapter 5 & 6 is found to be vast, if analog part covered.

Subject:- Mini Project

- Content missing

Fourth Year:

VII-Semester

Subject:- CCN Lab

- Dr. PP : Use NS-2 instead of 'C' programs

Subject:- Automotive Electronics

- Mr. MK suggested to include electric and hybrid vehicles. Also suggested to include classification of sensors and actuators. He also told that syllabus will be sent for reference.

Subject:- Multimedia Communication

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- Some topics should be removed as they are covered in previous semester, such as audio and video compression.

Subject:- Design for Testability

- Mr. MK suggested to make this subject mandatory, because of it's gaining importance.

VIII-Semester

- Mr. MK stressed on internship for students during vacations.

- Mr. PP also stressed on internship for students for 3months and include IPR as self study subject.

- Prof. R.L. Chakrasali briefed the hurdles and obstacles in providing internship for final year students and 3weeks induction program for first year students.

Subject:- Low Power VLSI

- Mr. DMN pointed out that chapter 1 MOS circuits is already covered in lower semesters.

Subject:- High Speed System Design

- Mr. DMN pointed that chapter 8 be dealt at introductory level.

Subject:- MEMS

- Dr. S suggested to include simulation using software tools as activity beyond syllabus.

PG scheme and syllabus was discussed.

Minutes of meeting of BOS held on 19/05/2018(PG) :

Committee members suggested to remove redundant content of all subjects. They also suggested to change the references of the most of the subjects especially where ever we feel the content of the syllabus is very vast. And also felt to include few more elective subjects.

5. **Valedictory Remarks** – by Dr. S.S. Kerur

6. **Few Words by External BOS Members**

i. Dr. Prashant Patavardhan

- He expressed his happiness by saying that the content, design, organization of syllabi was good.

- He also suggested to cut down the syllabus in some of the subjects which were found to be very vast.

ii. Dr. Rohini

- Madam suggested to cut down the syllabus in some of the subjects which were found to be very vast.

- Madam also suggested to float open electives and industry driven electives

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Ex: Courses on Multimedia, Big data analysis, Entrepreneurship(1 or 2 credit), IoT(2-0-4)

- Madam stressed on number of CO's and their attainment.

- Also suggested to make the students register for NPTEL, MOOC etc... courses for subjects like S&S, EMT.

- Advised for Stream Wise Electives

- Highlighted the use of Autonomy for the growth of department and institute

iii. Mr. Dharamraj M. N.

- He expressed his happiness by saying that the content, design, organization of syllabi was good.

- He stressed to remove the contents in the syllabus which were not relevant in present scenario, such as JFET.

iv. Mr. Sameer Kulkarni

- He expressed his happiness by saying that the content, design, organization of syllabi was good.

v. Mr. Manjunath Koti

- He stressed on Automotive electronics and advancement in this field to be a part of curriculum.

7. **Views by HOD:**

- Regarding BOS

- Thanked all the members and faculty for smooth & successful conduction of BOS

8. **Vote of Thanks:** by Dr. S.S. Kerur

- Thanked all the members and faculty for smooth & successful conduction of BOS

BOS Member Secretary

Dr. S S Kerur

HOD

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SDM College of Engineering & Technology, Dharwad-02 Department of E & C Engineering

ATTENDANCE SHEET BOS MEETING Date: 19.05.2018

BOS meeting is convened on 19.05.2018 in Main Laboratory. The following faculty members attended th same

Sl. No	Name of the faculty	Signature
1	Prof. Dr. Vijaya C	
2	Dr. G.A.Budakar	
3	Dr. Shreedhar A.Joshi	
4	Dr. Satish S. Bhairannawar	
5	Mrs. Savitri Raju	
6	Dr. Hemalata V. Bhujle	
7	Dr. S.S.Kerur	
8	Dr. K.N.Hosur	
9	Dr. S.V.Viraktamath	
10	Mrs. Jayashree C. Nidagundi	
11	Mrs. Sharada C. Sajjan	
12	Mr. S.S.Navalgund	
13	Mrs. Mala L. Muddannavar	
14	Mr. Vinayak Mirkin	
15	Mr. Mahendra M.Dixit	
16	Mrs. Sumangala Bhavikatti	
17	Mrs. Reshma Nadaf	
18	Mrs.Preeti S.Bellerimath	
19	Mr. Raghuram K.M	(AB) Leave.
20	Mr. Sunil S.Mathad	
21	Mr. M.Vijay Kumar	
22	Mr. Basu K.Saptalakar	
23	Mr. S.S.Ravishankar	
24	Mr. Kotresh M	
25	Mr. Shrikanth K.Shirakol	
26	Mrs. Sangeetha Gouda	
27	Mr. Vyas R.Murnal	
28	Ms. Megha G. Sidenur	
29	Ms. Suchita H.S	AB (on Leave)

Prof. & Head
Dept.of Electronics & Communi. tion Engg
SDM College of Engineering & Technology
Dharwad-580 002

KURAKUNDI RAGHAVENDRARAO GOPINATH

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SDM College of Engineering & Technology, Dharwad-02
Department of Electronics & Communication Engineering

ATTENDANCE SHEET

BOS MEETING

Date: 19.05.2018

BOS meeting is convened on 19.05.2018. The following are the External and Internal BO members attended the meeting.

EXTERNAL MEMBERS – (BOS)

S.No.	Name	Representation in BOS	Contact Details	Signatur
1	Dr. Rohini Nagapadma	VTU Nominee	Professor and Head, Dept of ECE, NIE, Mysuru Rohini_nagapadma@yahoo.co.in Mobile No.:9980607517	
2	Dr. B. G. Sheeparmatti	Academic Institution	Professor, Dept. of ECE, BEC, Bagalkot b.g.sheeparamatti@gmail.com sheepar@yahoo.com Mobile No.:9448789302	
3	Dr. Patawardan,	Academic Institution	Professor and Head, Dept. of ECE, GIT, Belgavi pppatavardhan@git.edu Mobile No.: 9448941840	
4	Mr. Manjunath C Koti	Industry Representation	Senior Program Manager RBEI, 602, Adugodi, Hosur Road, Bengaluru-560 030 Mobile No: 9900097853 Manjunath.koti@in.bosch.com	
5	Mr. Dharamraj M.N	PG Alumni	Engineering Manager, Intel Mobile Communications India Pvt. Ltd. Unit 1, 10th Floor, Discover Building, Intel Tech Park, Whitefield dharmaray.nedalgi@intel.com Mobile No.: 9880396249	
6	Mr. Sameer Kulkarni	Special Invitee	Project Leader , L & T Services, Bengaluru. Sameerpkulkarni23@gmail.com	

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HoD E&C

Visvesvaraya Technological University

Jnana Sangama", Belagavi - 590 018.

Dr. H.N. Jagannatha Reddy, BE,ME,Ph.D.
REGISTRAR

Phone: (0831) 2405468
Fax : (0831) 2405467

Ref No. VTU/Aca/A12/2018-19/ 58/E

Date: 4 APR 2018

To,

Dr. Rohini Nagapadma
Professor & HoD,
Dept., of E&CE,
The National Institute of Engineering,
Mysuru

Sir,

Sub: Nomination to the Board of studies (Electronics & Communication Engineering) of SDM College of Engineering & Technology, Dharwad (Autonomous) from 27/03/2018 to 26/03/2020.
Ref: Hon'ble Vice Chancellor's approval dated 27/03/2018

With reference to the above, I am pleased to nominate you as the Nominee of Visvesvaraya Technological University, Belagavi, to the "Board of Studies (Electronics & Communication Engineering)" of SDM College of Engineering & Technology, Dharwad (Autonomous).

You are requested to accept the same and attend the meetings of the "Board of Studies (Electronics & Communication Engineering)" of the college as and when requested by the principal of the college and ensure that the views and guidelines of the University are duly taken into account in the deliberations and decisions of the Board of Studies.

Thanking you,

Yours faithfully,

REGISTRAR

Copy Fwc's to:
The Principal, SDM College of Engineering & Technology, Dharwad., with a request to send meeting notices of the Board of studies to Dr. Rohini Nagapadma, from time to time. Please note that the TA/DA/Hospitality of V.T.U Nominee for attending Board of studies (Electronics & Communication Engineering) meetings have to be borne by your college.

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SDM College of Engineering & Technology, Dharwad-02
Department of Electronics & Communication Engineering

CIRCULAR

Date:18.05.2019

Annual BOS Meeting for the year 2019-20 of the Department of Electronics and Communication Engineering will be held **on 18.05.2019 at 9.00AM** in our department. All the faculty and BOS members (Internal & External) are requested to attend the same. The lunch is arranged at CDRF (1.15PM) for all the faculty and BOS members and Non-Teaching staff members. All are requested to join for the same.

Agenda of the meeting:

1. Welcome by the Chairperson and introduction about department and self introduction of all the members.
2. To read and confirm the proceedings of previous meeting held on 19.05.2018.
3. Review of I / II semester Basic Electronics syllabus (up to 10%)- UG Course.
4. Review of III / IV semester syllabus (100%) and also introduction and deletion of new subjects - UG Course.
5. Review of V / VI semester syllabus (up to 10%) and also introduction and deletion of new subjects - UG Course.
6. Review of VII / VIII semester syllabus (up to 10%) and also introduction and deletion of new subjects - UG Course.
7. Review and syllabus framing of PG Course (Digital Electronics) and approval for the syllabus of PG course for the year 2019-20 and also PO and PSO.
8. Any other matter with permission of the Chairperson.
9. Member Secretary read the proceedings of the meeting and also propose vote for thanks.

Member Secretary,
Prof. Dr. K. N. Hosur

HOD & Chairperson of BOS
Prof. Dr. Vijaya C

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S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Proceedings of BOS meeting held on 18.05.2019

Agenda No.	Particulars
1	**Welcome by Chairperson and report on progress of the department:** HOD, Prof. Dr. Vijaya C. extended welcome to all the BOS members. All the members introduced themselves to BOS committee. HOD highlighted the developments in the department after the previous BOS. She said syllabus of UG and PG as approved by previous BOS meeting has been implemented during 2018-19 academic year. Many faculty and students successfully completed NPTEL certificate courses and three faculty have been awarded PhD during the latest VTU convocation.
2	**Confirmation of proceedings of previous BOS meeting:** HOD read out the proceedings of previous BOS meeting and obtained confirmation for the same.
3	**Review of I / II Semester Syllabus:** **Course: Basic Electronics** • Dr. Shiparamatti suggested to include one chapter on brief introduction to "Embedded Systems" or as a topic in one of the chapters. • Dr. Prashanth Patavardhan suggested to merge 1st and 2nd chapters by removing some contents. Also he suggested interchange the order of chapter 4 & 5.
4	**Review and syllabus framing of III / IV Semester:** **III Semester:** **Course: Electromagnetic Theory** • Approved without change. **Course: Digital Circuit Design** • Dr. Prashanth Patavardhan suggested to change number of credits to 4 and Network Analysis as 3 credit course. • However, other members opined that there is no need to change the credits. • He suggested to include topic on ALU design. **Course: Network Analysis** • Mr. Basavaraj Mudigoudar suggested the Introduction of bread board and basic components. • Faculty agreed to include the same in Activity Beyond Syllabus.

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Course: Analog Electronic Circuits

- Dr. Shiparamatti suggested to remove the chapter on BJT and cover FET in depth.
- Mr. Basavaraj Mudigoudar suggested to include one chapter on Low Power Electronics.
- Mr. Manjunath Koti suggested to include applications of FET etc. in Activity Beyond Syllabus, either as seminar / assignment / experiment.

Course: Signals and Systems

- Dr. Shiparamatti suggested to discuss the properties of Fourier representation only with respect to DTFT.

Course: AEC Lab

- Dr. Shiparamatti stressed to conduct all the experiments using simulation tools, as an activity beyond syllabus and conduct experiments using speakers.
- Faculty expressed that experiments using simulation tools is usually taken up as an activity beyond syllabus for theory course and agreed to conduct experiments using speakers so as to get to know the sounds of different frequencies.

Course: DCD Lab

- No Change suggested.

IV Semester:

Course: Communication System I

- Mr. Basavaraj Mudigoudar proposed to reduce the number of hours allocated to Chapter on AM and to increase number of hours for Chapter on FM and also opined that sampling concepts to be stressed also suggested increase no. of hours to chapter 7.
- Faculty agreed to relook in to the matter.

Course: Control Systems

- No Change suggested. However suggested to relook into allocation of no. of hours of some of the topics.
- Faculty agreed to relook in to the matter.

Course: Microcontroller

- Mr. Manjunath Koti proposed to include single and multi-core programming.
- Dr. Shiparamatti advised to add IDE concepts.

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- Mr. Basavaraj Mudigoudar advised for code optimization techniques to be included.
- Faculty expressed that single and multi-core programming is part of courses in higher semester syllabus. Faculty felt that IDE concepts and code optimization techniques may become too involved topics at 4th semester level and number of hours may not be sufficient to cover the topics.

Course: HDL Programming Using Verilog

- Mr. Basavaraj Mudigoudar recommended for HDL to ASIC bridge using Cadence tool under CTA activity.
- Faculty expressed that students will be learning Cadence tool only in higher semesters and hence may be taken up as part of project work.

Course: LIC's and Applications

- Mr. Basavaraj Mudigoudar and Mr. Manjunath Koti advised to include one chapter on Transducers, Sensors and problems based on sensors and its applications.
- He suggested to include audio application related to noise cancellation.
- Faculty agreed to include the same as part of activity beyond syllabus.

Course: HDL Programming Lab

- Mr. Basavaraj Mudigoudar recommended for HDL to ASIC bridge using Cadence tool as activity beyond syllabus.
- Faculty expressed that students will be learning Cadence tool only in higher semesters and hence may be taken up as part of project work.

Course: LIC Lab

- No Change suggested.

Course: Introductory Project

- Dr. Shiparamatti stressed on survey type projects which include understanding of the specifications of various components and working of equipment such as power supply, AFO etc.
- Dr. Prashanth Patavardhan suggested to include simple design thinking concepts. He stressed upon model based projects and its poster presentation as part of evaluation.
- Dr. Basavaraj Mudigoudar suggested that project should include basics of all the courses that they are studying in III / IV semesters, such as power dissipation in circuits, EMI/ EMC standards etc. and learn the art of the technical presentation.

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	• Mr. Manjunath Koti stressed upon survey of various national and international standards / Industry standards in communication Systems, signal processing / VLSI field. • Faculty noted all the points and assured to carry out introductory project course in a meaningful way. • Dr. Prashanth Patavardhan suggested to shift Electromagnetic Theory to 5th semester and Management, Entrepreneurship and IPR course to 3rd semester. He pointed about IOT and embedded lab, where the theoretical aspects are covered for IOT. • Faculty expressed that exchange of Electromagnetic Theory and Management, Entrepreneurship and IPR may not be possible since the pattern of the scheme is fixed at the college level. • Mr. Basavaraj Mudigoudar and Mr. Manjunath Koti suggested to include one subject related to PCB design and manufacturing with corresponding lab in 6th / 7th semester level. They suggested a book for the same with author as Mr. Vikas Akalwadi. Mr. Manjunath Koti suggested to include one chapter on "design for reliability" in DFT subject. • Mr. Manjunath Koti suggested to add business case as one chapter in Management subject. • He suggested to include Micropython in "Scientific Computing using Python" so that students can carry out interfacing experiments. • Dr. Shiparamatti recommended to include Text Books and Reference Books in all the subjects. • Faculty agreed to relook in to the matter and incorporate the same during next revision of syllabus.
5	**Approval for scheme of V to VIII Semester (175 credits scheme):** The members went through the scheme and approved the same.
6	**Review of V / VI Semester Syllabus:** The syllabus of V /VI semesters have been accepted as it is without any change. Members appreciated that Python programming suggested in the previous BOS has been taken up by the students.
7	**Review of VII / VIII Semester Syllabus:** The syllabus of **VII / VIII** semesters have been accepted as it is without any change.
8	**Review of I / II Semester PG Syllabus:** The syllabus of **I / II Semester PG** have been accepted as it is without any change.

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	Members appreciated that Machine Learning suggested in the previous BOS has been taken up by the students.
9	**Review of III Semester PG Syllabus:** The syllabus of **III Semester PG** have been accepted as it is without any change.
10	Any other matter with the permission of Chairperson: HOD requested the industry representatives and alumni to help strengthen the placement activity and internship activity to the students.
11	**Vote of Thanks:** Member Secretary, by Dr. K.N. Hosur proposed vote of thanks to all the members for their valuable inputs to strengthen the curriculum. He thanked all the faulty for framing the syllabus.

Member Secretary (BOS)
Prof. Dr. K. N. Hosur

HOD& Chairperson of BOS
Prof. Dr. Vijaya C.

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SDM College of Engineering & Technology, Dharwad-02
Department of Electronics & Communication Engineering

Details of the External members BOS 2018 to 2020

S. No	Name of the BOS members	Institution belongs to	Mobile No	Email-ID
1	Dr. Rohini Nagapadma Professor and Head	Dept of ECE, NIE, Mysuru	9980607517	Rohini_nagapadma@yahoo.co.in ABSENT
2	Dr. B. G. Sheeparmatti Professor	Dept. of ECE, BEC, Bagalkot b.g.sheeparamatti@gmail.com	9448789302	sheepar@yahoo.com,
3	Dr. Patawardan, Professor and Head	Dept. of ECE, GIT, Belgavi	9448941840	pppatavardhan@git.edu
4	Mr. Manjunath C Koti Senior Program Manager,	RBEI, 602, Adugodi, Hosur Road, Bengaluru- 560 030	9900097853	Manjunath.koti@in.bosch.com
5	Mr. Dharamraj M.N Engineering Manager	Intel Mobile Communications India Pvt. Ltd. Unit 1, 10th Floor, Discover Building, Intel Tech Park, Whitefield	9880396249	dharmaray.nedalgi@intel.com ABSENT
6	Mr. Basavaraj Mudigoudar, Director	Sirnaik Green Tech. Pvt. Ltd., 217, 3rd Main, 5th Cross, Indira Nagar, Bengaluru	9740416614	Basavarajsm@gmail.com

S. No.	Name	Representation
	INTERNAL MEMBERS (BOS)	
1	Prof. Dr.Vijaya C	Chairperson
2	Dr. K. N. Hosur	Member Secretary
3	Dr. G. A. Bidkar	Member
4	Dr. Shreedhar A. Joshi	Member
5	Dr. Satish S. Bhairannawar	Member
6	Mrs. Savitri Raju	Member
7	Dr. Hemalata V. Bhujle	Member
8	Dr. S. S. Kerur	Member
9	Dr. S. V. Viraktamath	Member
10	Dr. Sharada C. Sajjan	Member
11	Mr. S. S. Navalgund	Member

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SDM College of Engineering & Technology, Dharwad-02
Department of Electronics & Communication Engineering

ATTENDANCE SHEET

BOS MEETING **Date: 18.05.2019**

BOS meeting is convened on 18.05.2019. The following are the External and Internal BOS members attended the meeting.

EXTERNAL MEMBERS – (BOS)				
S.No.	**Name**	**Representation in BOS**	**Contact Details**	**Signature**
1	Dr. Rohini Nagapadma	VTU Nominee	Professor and Head, Dept of ECE, NIE, Mysuru Rohini_nagapadma@yahoo.co.in Mobile No.:9980607517	ABSENT
2	Dr. B. G. Sheeparmatti	Academic Institution	Professor, Dept. of ECE, BEC, Bagalkot b.g.sheeparamatti@gmail.com sheepar@yahoo.com Mobile No.:9448789302	
3	Dr. Patawardan	Academic Institution	Professor and ~~Head~~, Dept. of ECE, GIT, Belgavi pppatavardhan@git.edu Mobile No.: 9448941840	
4	Mr. Manjunath C Koti	Industry Representation	Senior Program Manager RBEI, 602, Adugodi, Hosur Road, Bengaluru- 560 030 Mobile No: 9900097853 Manjunath.koti@in.bosch.com	
5	Mr. Dharamraj M. N.	PG Alumni	Engineering Manager, Intel Mobile Communications India Pvt. Ltd. Unit 1, 10th Floor, Discover Building, Intel Tech Park, Whitefield dharmaray.nedalgi@intel.com Mobile No.: 9880396249	ABSENT.
6	Mr. Basavaraj Mudigoudar,	Special Invitee	Director, Sirnaik Green Tech. Pvt. Ltd., 217, 3rd Main, 5th Cross, Indira Nagar, Bengaluru Basavarajsm@gmail.com Mobile No.: 9740416614	

INTERNAL MEMBERS (BOS)			
S. No.	**Name**	**Representation**	**Signature**
1	Prof. Dr.Vijaya C	Chairperson	
2	Dr. K. N. Hosur	Member Secretary	
3	Dr. G. A. Bidkar	Member	
4	Dr. Shreedhar A. Joshi	Member	
5	Dr. Satish S. Bhairannawar	Member	
6	Mrs. Savitri Raju	Member	
7	Dr. Hemalata V. Bhujle	Member	
8	Dr. S. S. Kerur	Member	
9	Dr. S. V. Viraktamath	Member	
10	Dr. Sharada C. Sajjan	Member	
11	Mr. S. S. Navalgund	Member	

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SDM College of Engineering & Technology, Dharwad-02 Department of E & C Engineering

ATTENDANCE SHEET **BOS MEETING** **Date:** 18.05.2019

BOS meeting is convened on 18.05.2019 in PG Room. The following faculty members attended the same.

Sl. No	Name of the faculty	Signature
1	Prof. Dr.Vijaya C	
2	Dr. G.A Bidakar	
3	Dr. Shreedhar A.Joshi	
4	Dr. Satish S. Bhairannawar	
5	Mrs. Savitri Raju	
6	Dr. Hemalata V. Bhujle	
7	Dr. S.S.Kerur	
8	Dr. K.N.Hosur	
9	Dr. S.V.Viraktamath	
10	Mrs. Jayashree.C.Nidagundi	
11	Dr. Sharada C. Sajjan	
12	Mr. S.S.Navalgund	
13	Mrs. Mala L.Muddannavar	Leave
14	Mr. Vinayak Miskin	
15	Dr. Mahendra M.Dixit	
16	Mrs. Sumangala Bhavikatti	
17	Mrs. Reshma Nadaf	
18	Mrs.Preeti S.Bellerimath	
19	Mr. Raghuram K.M.	Leave
20	Mr. Sunil S.Mathad	
21	Mr. M.Vijay Kumar	
22	Mr. Bairu K Saptalakar	
23	Mr. S.S Ravishankar	
24	Mr. Kotresh M.	Leave
25	Mr. Shrikanth K.Shirakol	
26	Mrs. Sangeetha Gouda	
27	Mr. Vyas R Murnal	
28	Ms. Megha G. Sidenur	
29	Channakka L.	

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SDM College of Engineering & Technology, Dharwad-02
Department of Electronics & Communication Engineering

CIRCULAR

Date: 11.07.2020

Annual BOS Meeting for the year 2020-21 of the Department of Electronics and Communication Engineering will be held **on 11.07.2020 at 9.30 AM** in our department. All the faculty and BOS members (Internal & External) are requested to attend the same.

Agenda of the meeting:

1. Welcome by the Chairperson and introduction about department and self introduction of all the members.
2. To read and confirm the proceedings of previous meeting held on 18.05.2019.
3. Review of I / II semester Basic Electronics syllabus (10%, 175 credits scheme) - UG Course.
4. Review of III / IV semester (10%, 175 credits scheme) - UG Course.
5. Review and syllabus framing of V/ VI semester (100%, 175 credits scheme)-UG Course.
6. Review of VII / VIII semester syllabus (10%, 200 credit scheme) and also introduction and deletion of new subjects - UG Course.
7. Approval for scheme of VII to VIII semester (175 credits scheme)- UG Course
8. **Review of Program outcomes-PG Course**
9. **Review of scheme for the academic year 2020-21-PG Course**
10. **Review of syllabus framing of I / II semester for the academic year 2020-21 (100%, 88 credits scheme) - PG Course.**
11. **Review of III/ IV semester for the academic year 2020-21 (10%, 88 credits scheme) - PG Course**
12. Any other matter with permission of the Chairperson.
13. Member Secretary read the proceedings of the meeting and also propose vote of thanks.

Member Secretary,
Prof. Dr. K. N. Hosur

PG Coodinator,
Dr. Sharada C. Sajjan

HOD & Chairperson of BOS
Prof. Dr. Vijaya C

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SDM College of Engineering & Technology, Dharwad

Department of Electronics & Communication Engineering

Proceedings of the online BOS Meeting held on 11.07.2020

Agenda No.	Particulars
1	**B.E (Electronics & Communication Engineering)** Welcome by Chairperson and report on progress of the department: HOD, Prof. Dr. Vijaya C. extended warm welcome to all the BOS members. All the members introduced themselves to BOS Committee. HOD highlighted the developments in the department after the previous BOS. She said syllabus of UG and PG as approved by previous BOS meeting has been implemented during 2019-20, academic year. Many faculty and students completed NPTEL certification courses and three faculty have been awarded Ph.D during the latest VTU convocation.
2	Confirmation of proceedings of previous BOS meeting. HOD read out the proceedings of previous meeting and obtained approval for the same.
3	**Review of I\II Semester Syllabus:** Course: Basic Electronics Mr. Chanabasappa Baligar suggested to include 78XX fixed IC voltage regulator in the module-I.
4	**Review of III/IV semester syllabus UG Course:** The syllabus of III/IV semester have been accepted as it is without any change.
5	**Review and syllabus framing of V/VI semester UG Course:** V Semester: Course: Management, Entrepreneurship and IPR Approved without change. Course: CMOS VLSI Design Mr. Syad Salman suggested to include introduction of FinFETs in the module-I Course: Communication Systems-II Mr. Chanabasappa Baligar suggested to include Communication Systems-I as pre-requisite Dr. Sridhar Kuntoji suggested to include B. P. Lathi as one of the reference books. Course: Digital Signal Processing Dr. Deepak K T suggested to include Allan E Oppenheim and Allan Willsky as one of the reference books.

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Course: Information Theory and Coding

Dr. Sridhar Kuntoji suggested to include Shu Lin, Daniel J, Costello Jr, Pearson/Prentice Hall, 2nd edition, 2004

Course: Object Oriented Programming using C++

Approved without change.

Course: Telecommunication Networks

Approved without change.

Course: Scientific Computing using Python

Approved without change.

Course: Sensors and Transducers

Dr. Anitha Kanavalli suggested to introduce the subject prior to IOT

Course: Communication Systems Laboratory

Approved without change.

Course: DSP Laboratory

Dr. Deepak K T suggested to carry out few experiments using DSP TMS 6713 kit.

Course: Minor Project-I

Approved without change.

Course: Soft Skills/Aptitude

This subject will be handled by CIII

VI Semester:

Course: Analog & Mixed Mode VLSI Design

Approved without change.

Course: Embedded Systems Design

Dr. Deepak K T suggested to change the subject title as IOT & Embedded Systems and also subject should give an impression on ARM CORTEX core rather than embedded systems.

Course: System Verilog

Approved without change.

Course: Advanced Digital System Design

Approved without change.

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	Course: Digital Image Processing Dr. Sridhar Kuntoji suggested to add part of computer vision syllabus so that helpful for Machine learning and Deep learning subjects for future semesters Course: Operating Systems Dr. Anitha Kanavalli suggested to include what is Operating Systems and little portion of RTOS device driver development. Courses: Speech Processing, ROBOTICS, Data Structure using C++, Artificial Intelligence, Cryptography, Soft Computing, Automotive Electronics, Multimedia Communication, VLSI Laboratory, Minor Project-II. Approved without change. Course: Soft Skills/Aptitude This subject will be handled by CIII
6	**Review of VII/VIII Semester syllabus:** The syllabus of VII/VIII semester have been accepted as it is without any change.
7	**M. Tech (Digital Electronics)** **Review of Program outcomes** Program outcomes accepted without change
8	**Review of new scheme for the academic year 2020-21 (100%, 88 credits, new scheme)** • More electives to be added such as Digital System Design with FPGAs, Electronics System Packaging, Sensor Data Processing, Defence Electronics Syste Systems, etc • Include research component in each subject. • Subjects like machine learning, deep learning, data analytics, statistical signal processing /detection or estimation theory may be added as electives. • Every course should have small project.
9	**Review of syllabus framing of I/II semester for the academic year 2020-21 (100%, 88 credits, new scheme)** • Applied Mathematics: There is introduction to Linear Algebra in 2nd chapter and Linear Algebra in 3rd chapter. Relook. • Automotive Electronics: Include Colligence Avoidance Radar related to vehicle at the end. • VLSI for Signal Processing: Equalization, FIR filters (basically high pass filter) required for high speed communication may be added.

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	• Advanced Embedded System Design: RTOS, 4th chapter, comparison among various RTOS available in the market in terms of architecture, scheduling, latency, etc to be included. • Digital Signal Compression: include Information theory and coding as prerequisite, Compare with multimedia communication, include John Abranhson as reference. • Image and video processing: Include K. P. Soman for reference • IOT applications: Include Rajkamal for reference • Multimedia Communication: Include 5G compression techniques • For Digital Electronics Lab 2, suggestions from Sayed sir to be taken.
10	**Review of III/IV semester for the academic year 2020-21 (10%, 88 credits, old scheme)** • ACA: more orientation to be on parallel processing • Increase the no. of electives such as Parallel Processing , CUDA, GPUs • Artificial Neural Networks: Connect to Deep Neural Networks. Include reference book by Shivanandan • Cryptographic Systems: Include Pulper and Baker as reference book • No. of COs to be uniform for both theory and lab • Include Number theory in signal processing at 2nd or 3rd semester • Include Multirate concepts
11	**Vote of Thanks:** Member Secretary, Dr. K. N. Hosur proposed vote of thanks to all members for their valuable inputs to strengthen the curriculum. He thanked all the faculty for framing the syllabus.

Member Secretary of BOS	PG Coordinator	HOD & Chairperson of BOS
Dr. K. N. Hosur	Dr. Sharada C. Sajjan	Prof. Dr. Vijaya C.

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SDM College of Engineering & Technology, Dharwad-02
Department of Electronics & Communication Engineering

ATTENDANCE SHEET
BOS MEETING **Date: 11.07.2020**

BOS meeting is convened on 11.07.2020. The following are the External and Internal BOS members attended the meeting.

S. No	Name of the BOS members	Representation in BOS	Institution belongs to	Mobile No	Email-ID	Signature
1	Dr. Deepak K T	Academic Institution	Asst. Prof., Dept. of ECE, IIIT, Dharwad	9900456936	Deepak@iiitdwd.ac.in	
2	Dr. Anita Knanvalli	Academic Institution	Professor and Head, Dept. of CSE, Ramaiah Institute of Technology, MSRIT Post, M S Ramaiah Nagar, Bengaluru -560 054	9845899681	anithak@msrit.rdu	
3	Mr. Channabasappa Baligar	Industry Representation	Scientist E LRDE, DRDO, Ministry of Defense, C V Raman Nagar, Bengaluru-560093	9806098880	baligarc@yahoo.co.in	
4	Mr. Syad Salman	PG Alumani	Sankalpa Semi-Conductors, Hubballi M: 9742248384 Email-Id:	9742248384	syedsalman11@gmail.com	
5	Dr. Sridhar Kuntoji	VTU Nominee	Professor Dept. of ECE, BEC, Vidayagiri, Bagalkot, Karnataka 587102	7259704913	Shridharskuntoji@yahoo.co.in	

INTERNAL MEMBERS (BOS)

Sl No	Representation in BOS	Name of the BOS members	Signature
1	Chairperson	Dr. Vijaya C.	
2	Internal Members UG	Dr. K. N. Hosur, Member Secretary	
		Dr. G. A. Bidkar	
		Dr. Shreedhar A. Joshi	
		Mrs. Savitri Raju	
		Dr. S. S. Kerur	
3	Internal Members PG	Dr. Satish S. Bhairannawar	
		Dr. Hemalata V. Bhujle	
		Dr. S. V. Viraktamath	
		Dr. Sharada C. Sajjan	
		Dr. S. S. Navalgund	

KURAKUNDI RAGHAVENDRARAO GOPINATH

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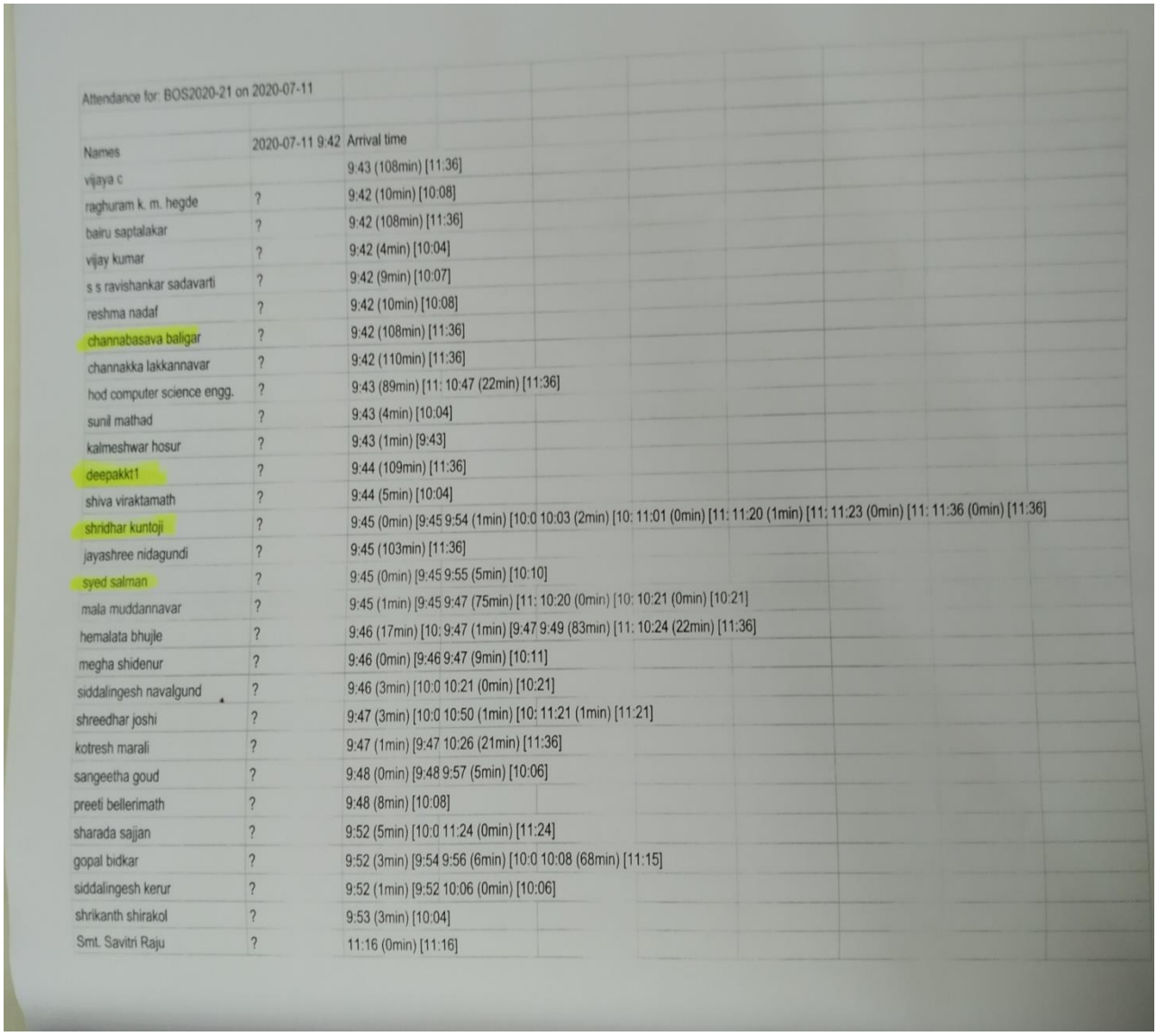

Attendance for: BOS2020-21 on 2020-07-11

Names	2020-07-11 9:42	Arrival time
vijaya c		9:43 (108min) [11:36]
raghuram k. m. hegde	?	9:42 (10min) [10:08]
bairu saptalakar	?	9:42 (108min) [11:36]
vijay kumar	?	9:42 (4min) [10:04]
s s ravishankar sadavarti	?	9:42 (9min) [10:07]
reshma nadaf	?	9:42 (10min) [10:08]
channabasava baligar	?	9:42 (108min) [11:36]
channakka lakkannavar	?	9:42 (110min) [11:36]
hod computer science engg.	?	9:43 (89min) [11: 10:47 (22min) [11:36]
sunil mathad	?	9:43 (4min) [10:04]
kalmeshwar hosur	?	9:43 (1min) [9:43]
deepakkt1	?	9:44 (109min) [11:36]
shiva viraktamath	?	9:44 (5min) [10:04]
shridhar kuntoji	?	9:45 (0min) [9:45 9:54 (1min) [10:0 10:03 (2min) [10: 11:01 (0min) [11: 11:20 (1min) [11: 11:23 (0min) [11: 11:36 (0min) [11:36]
jayashree nidagundi	?	9:45 (103min) [11:36]
syed salman	?	9:45 (0min) [9:45 9:55 (5min) [10:10]
mala muddannavar	?	9:45 (1min) [9:45 9:47 (75min) [11: 10:20 (0min) [10: 10:21 (0min) [10:21]
hemalata bhujle	?	9:46 (17min) [10: 9:47 (1min) [9:47 9:49 (83min) [11: 10:24 (22min) [11:36]
megha shidenur	?	9:46 (0min) [9:46 9:47 (9min) [10:11]
siddalingesh navalgund	?	9:46 (3min) [10:0 10:21 (0min) [10:21]
shreedhar joshi	?	9:47 (3min) [10:0 10:50 (1min) [10: 11:21 (1min) [11:21]
kotresh marali	?	9:47 (1min) [9:47 10:26 (21min) [11:36]
sangeetha goud	?	9:48 (0min) [9:48 9:57 (5min) [10:06]
preeti bellerimath	?	9:48 (8min) [10:08]
sharada sajjan	?	9:52 (5min) [10:0 11:24 (0min) [11:24]
gopal bidkar	?	9:52 (3min) [9:54 9:56 (6min) [10:0 10:08 (68min) [11:15]
siddalingesh kerur	?	9:52 (1min) [9:52 10:06 (0min) [10:06]
shrikanth shirakol	?	9:53 (3min) [10:04]
Smt. Savitri Raju	?	11:16 (0min) [11:16]

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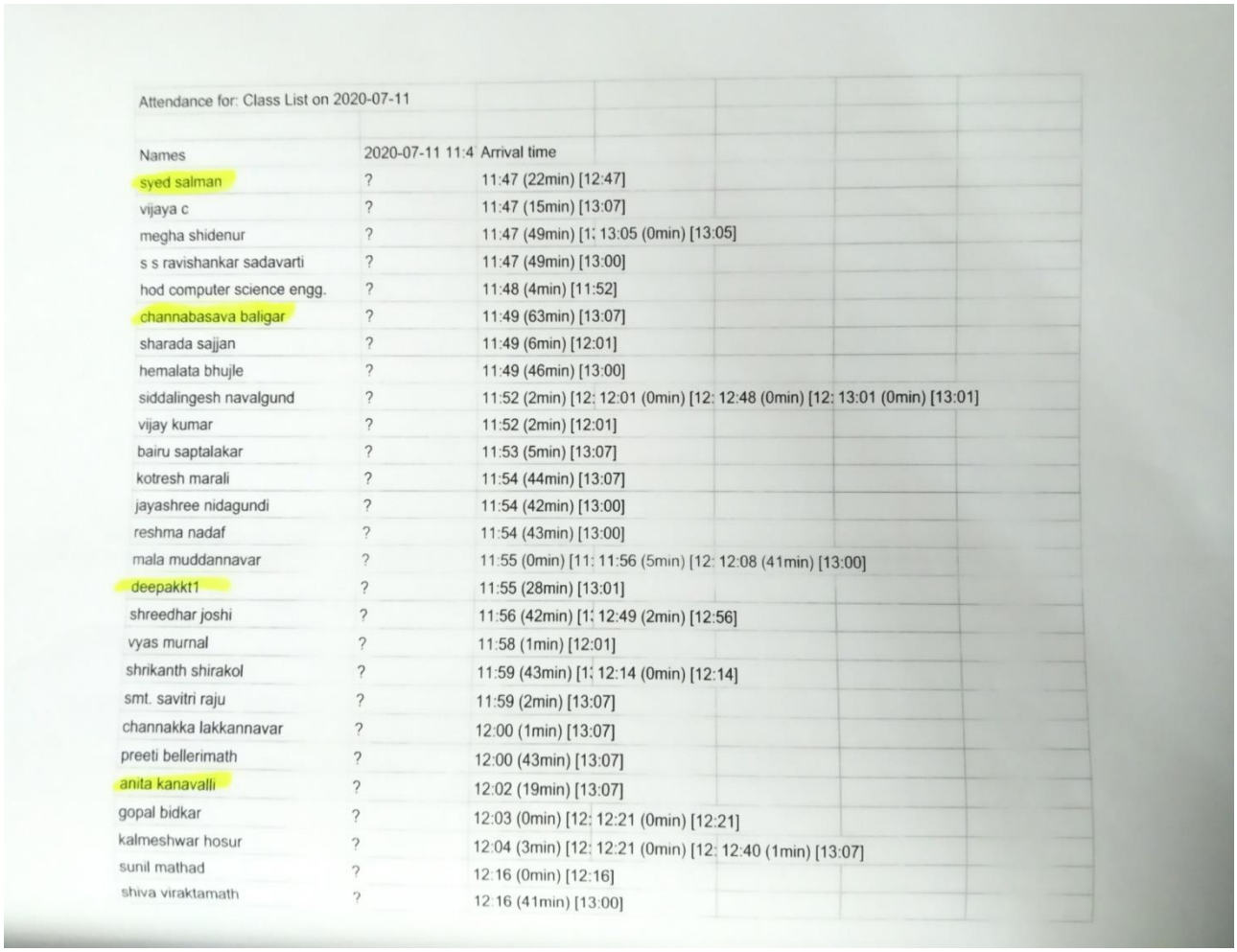
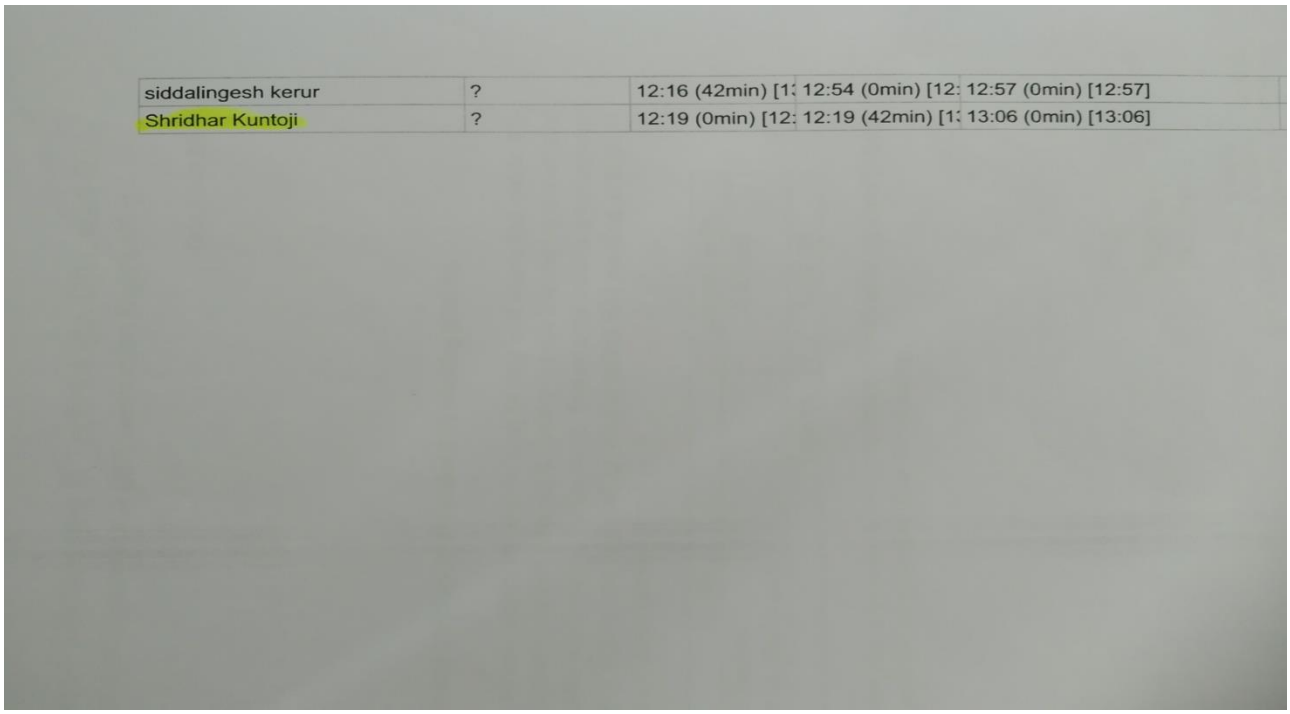


Attendance for: Class List on 2020-07-11

Names	2020-07-11 11:4	Arrival time
syed salman	?	11:47 (22min) [12:47]
vijaya c	?	11:47 (15min) [13:07]
megha shidenur	?	11:47 (49min) [1: 13:05 (0min) [13:05]
s s ravishankar sadavarti	?	11:47 (49min) [13:00]
hod computer science engg.	?	11:48 (4min) [11:52]
channabasava baligar	?	11:49 (63min) [13:07]
sharada sajjan	?	11:49 (6min) [12:01]
hemalata bhujle	?	11:49 (46min) [13:00]
siddalingesh navalgund	?	11:52 (2min) [12: 12:01 (0min) [12: 12:48 (0min) [12: 13:01 (0min) [13:01]
vijay kumar	?	11:52 (2min) [12:01]
bairu saptalakar	?	11:53 (5min) [13:07]
kotresh marali	?	11:54 (44min) [13:07]
jayashree nidagundi	?	11:54 (42min) [13:00]
reshma nadaf	?	11:54 (43min) [13:00]
mala muddannavar	?	11:55 (0min) [11: 11:56 (5min) [12: 12:08 (41min) [13:00]
deepakkt1	?	11:55 (28min) [13:01]
shreedhar joshi	?	11:56 (42min) [1: 12:49 (2min) [12:56]
vyas murnal	?	11:58 (1min) [12:01]
shrikanth shirakol	?	11:59 (43min) [1: 12:14 (0min) [12:14]
smt. savitri raju	?	11:59 (2min) [13:07]
channakka lakkannavar	?	12:00 (1min) [13:07]
preeti bellerimath	?	12:00 (43min) [13:07]
anita kanavalli	?	12:02 (19min) [13:07]
gopal bidkar	?	12:03 (0min) [12: 12:21 (0min) [12:21]
kalmeshwar hosur	?	12:04 (3min) [12: 12:21 (0min) [12: 12:40 (1min) [13:07]
sunil mathad	?	12:16 (0min) [12:16]
shiva viraktamath	?	12:16 (41min) [13:00]



siddalingesh kerur	?	12:16 (42min) [1: 12:54 (0min) [12: 12:57 (0min) [12:57]
Shridhar Kuntoji	?	12:19 (0min) [12: 12:19 (42min) [1: 13:06 (0min) [13:06]

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SDM College of Engineering & Technology, Dharwad-02
Department of Electronics & Communication Engineering

CIRCULAR

Date:23.06.2021

Annual BOS Meeting for the year 2021-22 of the Department of Electronics and Communication Engineering will be held **on 24.06.2021 at 3.00 PM** in our department. All the faculty and BOS members (Internal & External) are requested to attend the same.

Agenda of the meeting:

1. Welcome by the Chairperson.
2. To read and confirm the proceedings of previous meeting held on 11.07.2020.
3. Review of I / II semester Basic Electronics syllabus (up to 10%) - UG Course.
4. Review and syllabus framing of III to VI semester (10%, 175 credits scheme) - UG Course.
5. Approval for scheme and syllabus of VII to VIII semester (100%, 175 credits scheme)- UG Course
6. Review of I / II semester PG course (Digital Electronics) syllabus (up to 10%, 88 credits scheme).
7. Approval for scheme and syllabus of III/ IV semester (100%, 88 credits scheme) - PG Course
8. Any other matter with the permission of Chairperson.
9. Inputs for NEP 2020 implementation.
10. Member Secretary to propose vote of thanks.

Link will be shared with all internal and external members Google Meet joining info Video call link: https://meet.google.com/eyj-oxxj-uqd

23/6/21

Member Secretary,
Prof. Dr. K. N. Hosur

23/6/2021

HOD & Chairperson of BOS
Prof. Dr. Vijaya C

Prof. & Head
Dept. of Electronics & Communication Engg
SDM College of Engineering & Technology
Dharwad-580 002

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SDM COLLEGE OF ENGINEERING AND TECHNOLOGY, DHARWAD
Department of Electronics and Communication Engineering

Date:23.06.2021

CIRCULAR

All the faculty are hereby informed to attend BOS meeting online on 24/06/2021@ 3pm using the below indicated link that will be shared with you. The faculty concerned with final year UG syllabus framing and 2nd year PG syllabus framing are requested to note the modifications discussed during the meeting.

ECE dept BOS meeting 24/06/2021, 3 pm

Thursday, June 24, 2021, 3:00 – 4:00pm

Google Meet joining info

Video call link: https://meet.google.com/eyj-oxxj-uqd

V.C 23/6/2021

Prof. Dr. Vijaya C
HOD, ECE

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SDM COLLEGE OF ENGINEERING AND TECHNOLOGY

Department of Electronics and Communication Engineering

Minutes of the BOS meeting held on 24-06-2021

- Dr. Vijaya. C HOD, welcomed all BOS members, faculty members and briefed about department and college. She said syllabus of UG and PG as approved by previous BOS meeting has been implemented during 2020-21
- Confirmation of proceedings of previous BOS meeting. HOD read out the proceedings of previous meeting and obtained approval for the same.
- Dr. K.N Hosur , BOS member secretary presented UG syllabus to members.
- Ist to VIth semester syllabus accepted as it is without modifications..
- Dr. Deepak K.T, IIIT Dharwad suggested to introduce deep learning subject as elective for final year.
- Dr. Shreedhar Kuntoji, BEC, Bagalkot suggested to include more practical oriented teaching [illegible].
- VIIIth semester syllabus accepted as it is.
- Dr. Sharada Sajjan, PG coordinator presented PG syllabus.
- Ist and IInd Semester PG syllabus accepted as it is.
- Mr. Syad Salman, Sankalp Semiconductor Hubli suggested to introduce FinFETS in VLSI subjects.
- Dr. Deepak K.T, Dr. Shreedhar Kuntoji gave inputs for the implementation of NEP 2020.
- Dr. K. N Hosur, BOS member secretary proposed vote of thanks to all members for their valuable inputs to strengthen the curriculum. He thanked all the faculty for framing the syllabus.

Member Secretary	PG Coordinator	HOD, ECE
Dr. K.N. Hosur	Dr. Sharada C. Sajjan	Dr. Vijaya C

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SDM College of Engineering & Technology, Dharwad-02
Department of Electronics & Communication Engineering

ATTENDANCE SHEET
BOS MEETING

Date:24.06.2021

BOS meeting is convened on 24.06.2021. The following are the External and Internal BOS members attended the meeting.

Name of the BOS members	Representation in BOS	Institution belongs to	Mobile No	Email-ID	Signature
Dr. Deepak K T	Academic Institution	Asst. Prof., Dept. of ECE, IIIT, Dharwad	9900456936	Deepak@iiitdwd.ac.in	
Dr. Anita Knanvalli	Academic Institution	Professor and Head, Dept. of CSE, Ramaiah Institute of Technology, MSRIT Post, M S Ramaiah Nagar, Bengaluru -560 054	9845899681	anithak@msrit.rdu	online Attendance
Mr. Channabasappa Baligar	Industry Representation	Scientist E LRDE, DRDO, Ministry of Defense, C V Raman Nagar, Bengaluru-560093	9806098880	baligarc@yahoo.co.in	
Mr. Syad Salman	PG Alumani	Sankalpa Semi-Conductors, Hubballi M: 9742248384 Email-Id:	9742248384	syedsalman11@gmail.com	
Dr. Sridhar Kuntoji	VTU Nominee	Professor Dept. of ECE, BEC, Vidayagiri, Bagalkot, Karnataka 587102	7259704913	Shridharskuntoji@yahoo.co.in	

INTERNAL MEMBERS (BOS)

Sl No	Representation in BOS	Name of the BOS members	Signature
1	Chairperson	Dr. Vijaya C.	
2	Internal Members UG	Dr. K. N. Hosur, Member Secretary	
		Dr. Shreedhar A. Joshi	
		Mrs. Savitri Raju	
		Dr. S. S. Kerur	
3	Internal Members PG	Dr. Satish S. Bhairannawar	
		Dr. Hemalata V. Bhujle	
		Dr. S. V. Viraktamath	
		Dr. Sharada C. Sajjan	
		Dr. S. S. Navalgund	

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