



ವಿಸ್ವವರಾಜ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಲಗಾವಿ
 VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI
 KARNATAKA, INDIA

PH
19 0698

Professional Degree Certificate

This is to certify that Mr./Ms. BAIRUK SAPTALAKAR

*is qualified for the award of the degree of
 DOCTOR OF PHILOSOPHY IN FACULTY OF ELECTRICAL & ELECTRONICS ENGINEERING SCIENCES W.E.F. 03-12-2022
 of this University for having fulfilled the requirements for the award
 of the said Degree by submitting the thesis titled :*

ARCHITECTURAL OPTIMIZATION OF IMAGE PROCESSING ALGORITHM ON FPGA

with University Seat No. 2SD13PEN11

*The degree will be conferred on him / her at the next
 convocation or thereafter,*

Belagavi

Date : 06-12-2022



Reg BE

Registrar (Evaluation)

Bairuk

(B.K. Saptalakar)

BE

Prof. S. Vinay

Dept of Electronics, Visvesvaraya Technological University, Belagavi

W. D. No. 100/2022

12/06/2022