

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: PG 1st SEM

TIME TABLE

Academic Year: 2026-27

Hall No.: PG Room

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm
MONDAY		26PDEE1052	BREAK	26PDEE107	26PDEC104	26PDEC102	BREAK	26PDEC101	26PDEC103
TUESDAY		26PDEE1052		26PDEC103	26PDEE107			26PDEL106	
WEDNESDAY		26PDEC103		26PDEE1052	26PDEC104			26PDEI104-Integrated Lab	
THURSDAY		26PDEC102		26PDEC104	26PDEC103	26PDEE1052			
FRIDAY									
SATURDAY		26PDEC102		26PDEC101	26PDEC101				

Note: TIME-TABLE IS EFFECTIVE FROM :

S.No.	Course Code	Course Title	Course Credits	CI		LI	Course Instructor
				L	T	P	
1	26PDEC101	Applied Mathematics	3	42	0	0	Dr. Preeti J & Dr. Basavaraj H.
2	26PDEC102	Digital VLSI Design	3	42	28	0	Dr. S. S Kerur
3	26PDEC103	Digital Circuit Testing & Testability	4	42	0	0	-
4	26PDEI104	Digital System Design using Verilog	3	42	0	28	Prof. Reshma Nadaf
5	26PDEE1052	Data Compression	4	42	0	0	X
6	26PDEE107	Research Methodology & IPR	2	28	0	0	Y
7	26PDEL106	Digital VLSI Design Lab	1	0	0	24	SSK,SSM
		Total Credits	20				

HOD ECE

Dean (Academic Program)

DEAN (ACADEMIC)

SDM COLLEGE OF ENGG. & TECH.,
DHARWAD.

Principal

SDM College of Engg. & Tech.,
Dhavalagiri, Dharwad-580002

Prof. & Head

Dept. of Electronics & Communication Engg.
SDM College of Engineering & Technology
Dhavalagiri-Dharwad-580002

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: PG III SEM

TIME TABLE

Academic Year: 2026-27
Hall No.: PG Room

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm
MONDAY		24PDEC300	BREAK	24PDEE357	24PDEE351		BREAK		
TUESDAY		24PDEE357		24PDEE351	24PDEC300	24PDEE356			
WEDNESDAY		24PDEE356		24PDEE351	24PDEC300				
THURSDAY		24PDEC300		24PDEE356	24PDEE357	24PDEE351			
FRIDAY		24PDEE356		24PDEE357					
SATURDAY									

Note: TIME-TABLE IS EFFECTIVE FROM : 1.09.2026

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	24PDEC300	Advances in VLSI Design	4	4-0-0	Dr. J. C. Nidagundi
2	24PDEL301	Project Phase-I	6	0-0-6	SCS
3	24PDEE351	Error Control Coding	4	4-0-0	Dr. S. V. Viraktamath
4	24PDEE356	Digital Circuit Testing & Testability	4	4-0-0	Dr. S.S. Mathad
5	24PDEE357	Analog & Mixed Mode VLSI Circuits	4	4-0-0	Dr. K. N. Hosur

HOD ECE

Prof. & Head
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SDM College of Engineering & Technology,
Dhavalagiri-Dharwad-580007

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