SDME Society (Regd.) Ujire, D.K.
SDM COLLEGE OF ENGINEERING & TECHNOLOGY
Dhavalagiri, DHARWAD – 580 002 (India)
Autonomous Institution Affiliated to Visvesvaraya Technological University, Belagavi

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Dr. I. Sridhar
Principal

Ref No: SDMCET/Exam/2026-27
Date: 19 / 11 / 2026

Semester End / Backlog Examination, January 2027
M.Tech.-III Semester: Examination Schedule

Timing: 9:30AM to 12:30PM

Exam Date & day	Course Code				
	CADS	CSE	PSE	EVT	DE
04-01-2027 Monday	24PCDC301	24PCSC301	24PEPC300	24PEVC300	24PDEC300
06-01-2027 Wednesday	24PCDE327	24PCSE325	24PEPE311	24PEVE311	24PDEE351
08-01-2027 Friday	24PCDE328	24PCSE331	24PEPE313	24PEVE312	24PDEE356
11-01-2027 Monday	--	--	--	--	24PDEE357

Note: Internship / Project Phase-I examination for M.Tech – III Semester will be held from 29-12-2026 to 30-12-2026.

19/11/2026
Controller of Examinations
Controller of Examinations
S.D.M. College of Engg & Tech.,
Dharwad-2

Principal 19/11/26
PRINCIPAL
S.D.M. College of Engg. & Technology.
Dhavalagiri, Dharwad-580 002

Course Code details

Computer Aided Design of Structures (CADS)

Course Code	Course Title
24PCDC301	Analysis of Plates and Shells–Classical and FE Approach
24PCDE327	Design of Substructures
24PCDE328	Composite Materials

Computer Science & Engineering (SCS)

Course Code	Course Title
24PCSC301	Cloud computing concepts and applications
24PCSE325	Natural Language Processing and Text Mining
24PCSE331	Data stream mining

Power System Engineering (PSE)

Course Code	Course Title
24PEPC300	Distribution System Design and Control
24PEPE311	Planning & Management of Deregulated Power Systems
24PEPE313	Programmable Logic Controllers and Applications

Electric Vehicle Technology (EVT)

Course Code	Course Title
24PEVC300	IoT and Vehicle Communication
24PEVE311	Battery and Fuel cell Technology
24PEVE312	Noise, Vibrations and Harshness in EV

Digital Electronics (DE)

Course Code	Course Title
24PDEC300	Advances in VLSI Design
24PDEE351	Error Control Coding
24PDEE356	Digital Circuit Testing & Testability
24PDEE357	Analog and Mixed Mode VLSI Circuits