

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: I
Division: EC-A

TIME TABLE

Academic Year: 2025-26
Hall No.: 6

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm
MONDAY	22ETC100	22ENG16	BREAK	22IDT18	22BEE13(T)		BREAK	PHY Lab (22PHYE12): A1 batch	
TUESDAY	22ESC145	22MATE11		22BEE13	22PHYE12(T)			MATHS Lab(22MATE11)	
WEDNESDAY				22MATE11(T)	22MATE11(T)			PHY Lab(22PHYE12): A2 batch	
THURSDAY	22PHYE12	22ESC145		22BEE13	22ETC100				
FRIDAY				22PHYE12(T)	22ETC100			22BEE13(T)	22ESC145(T)
SATURDAY	22MATE11	22PHYE12		22ESC145(T)	22KSK17/ 22KBK17				

Note: TIME-TABLE IS EFFECTIVE FROM //2025

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	22MATE11	Mathematics –I for EEE stream	4	2-2-2	Dr. P.B. Badiger
2	22PHYE12	Physics for EEE stream	4	2-2-2	Prof. Malati P. Annigeri
3	22BEE13	Basic Electronics	3	2-2-0	Prof. Sumangala N.B
4	22ESC145	Principles of Programming Using C	3	2-2-0	Dr. S. V. Viraktamath
5	22ETC100	Introduction to AI & it's Applications	3	3-0-0	Dr. S.A.Joshi
6	22ENG16	Communicative English	1	1	Dr. Shashikant S. Kurodi
7	22KSK17/ 22KBK17	Samskrutika Kannada/Balake Kannada	1	1	Prof. Maruti Kadam
8	22IDT18	Innovation and Design Thinking	1	1	Prof. Channakka L.
		Total Credits	20		

HOD ECE

Prof. B. Hemanth
Dept. of Electronics & Communication Engg.
S.D.M. College of Engineering & Technology
Dharwad-580002, KARNATAKA

Dean (Academic Program)

PRINCIPAL
SHRI DHARMASTHA MATHESHWARA
COLLEGE OF ENGINEERING AND TECHNOLOGY
DHARWAD-580002, KARNATAKA

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: I
Division: EC-B

TIME TABLE

Academic Year: 2025-26
Hall No.: 7

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm
MONDAY	22ENG16	22BEE13	BREAK	22PHYE12	22ESC145(T)		BREAK	22ETC100	22MATE11(T)
TUESDAY	22IDT18	22BEE13(T)		22ESC145	22ETC100				
WEDNESDAY	22MATE11	22MATE11		PHY Lab (22PHYE12): B1 batch				MATHS Lab(22MATE11)	
THURSDAY	22BEE13	22PHYE12		22MATE11(T)	22ESC145(T)				
FRIDAY	22ETC100	22PHYE12(T)		22ESC145	22BEE13(T)			PHY Lab(22PHYE12): B2 batch	
SATURDAY				22KSK17/ 22KBK17	22PHYE12(T)				

Note: TIME-TABLE IS EFFECTIVE FROM //2025

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	22MATE11	Mathematics –I for EEE stream	4	2-2-2	Dr. P.B. Badiger
2	22PHYE12	Physics for EEE stream	4	2-2-2	Dr. Kumar Maddani
3	22BEE13	Basic Electronics	3	2-2-0	Prof. Sumangala N.B
4	22ESC145	Principles of Programming Using C	3	2-2-0	Prof. Reshma Nadaf
5	22ETC100	Introduction to AI & it's Applications	3	3-0-0	Dr. S.A.Joshi
6	22ENG16	Communicative English	1	1	Dr. Shashikant S. Kurodi
7	22KSK17/ 22KBK17	Sanskritika Kannada/Balake Kannada	1	1	Prof. Maruti Kadam
8	22IDT18	Innovation and Design Thinking	1	1	Prof. M. Vijaykumar
		Total Credits	20		

HOD ECE

Dean (Academic Program)

Prof. & Heen
Dept of Electronics & Commu- ion Engrs
Sd Col lege of Engineering & Technology
Dharwad-580002, KARNATAKA

Principal
SHRI DHARMASTHALA MANJUNATHESHWARA
COLLEGE OF ENGINEERING AND TECHNOLOGY
DHARWAD-580002, KARNATAKA

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: III
Division: A

TIME TABLE

Academic Year: 2025-26
Hall No.: 26

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm
MONDAY	22UECL303(DCD Lab): A1 /22UECL304(AEC Lab): A2		BREAK	22UECC301	22USECC305(T)		BREAK	22UMAC300(T)	22UMAC300(T)
TUESDAY		22UMAC300		22UECC301	22UECE322	22UECC300		22UECL303(DCD Lab): A3 /22UECL304(AEC Lab): A4 22UNSK307(A1 & A2)	
WEDNESDAY		22UECC302		22UMAC300	22UECC300				22UECL303(DCD Lab): A2 /22UECL304(AEC Lab): A1 22UNSK307(A3 & A4)
THURSDAY		22USECC305		22UECC300	22UECL303(DCD Lab): A4 /22UECL304(AEC Lab): A3			22UMBA301	
FRIDAY		22UECC302		22USECC305	22UECC300	22UECC301		22UMBA301	
SATURDAY		22UHVK306		22UECC302	22USECC305(T)				

Note: TIME-TABLE IS EFFECTIVE FROM 01/09/2025

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	22UMAC300	Engineering Mathematics — III	3	2-2-0	Dr. Basavaraj Hoogar
2	22UECC300	Network Analysis	4	4-0-0	Dr. S. S. Navalgund
3	22UECC301	Digital Circuit Design	3	3-0-0	Prof. Vinayak Miskin
4	22UECC302	Analog Electronic Circuits	3	3-0-0	Dr. K. N. Hosur
5	22UECL303	Digital Circuit Design Laboratory	1	0-0-2	Prof. Vinayak Miskin & Prof. M.Vijaykumar
6	22UECL304	Analog Electronic Circuits Laboratory	1	0-0-2	Dr. K. N. Hosur & Dr. Vyas Murnal
7	22USECC305	Signals and Systems	3	2-2-0	Prof. Savitri Raju
8	22UHVK306	Universal Human Values-I	1	1-0-0	Dr. Shashikant S. Kurodi
9	22UECE322	Linear ICs and Applications	1	1-0-0	Prof. Megha Shidenur
10	22UMBA301	Mathematics	Audit	3-0-0	Mathematics Dept.
11	22UNSK307	National Service Scheme	Audit	0-0-2	Prof. S.S.Ravishankar
		Total Credits	20		

Prof. M. Hoogar
HOD ECE
Dept. of Electronics & Communication Engg.
S.D.M. College of Engineering & Technology
Dharwad - 580002

Dean (Academic Program)

PRINCIPAL
SRI DHARMASTHALA MANUNATH SWARA
COLLEGE OF ENGINEERING AND TECHNOLOGY
DHARWAD-580002, KARNATAKA

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: III
Division: B

TIME TABLE

Academic Year: 2025-26
Hall No.: 27

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm
MONDAY		22UECC301	BREAK	22UECC302	22USECC305(T)	22UECC300	BREAK	22UECL303(DCD Lab): B3 /22UECL304(AEC Lab): B4	
TUESDAY		22UECC300		22USECC305	22UECC301	22UECC302		22UNSK307	
WEDNESDAY		22UMAC300		22UHVK306	22UECL303(DCD Lab): B4 /22UECL304(AEC Lab): B3				
THURSDAY		22USECC305		22UMAC300	22UECC302	22UECC301		22UECL303(DCD Lab): B1 /22UECL304(AEC Lab): B2 22UMBA301(2:30-3:30)	
FRIDAY		22UECC300		22UMAC300(T)	22UMAC300(T)			22UECL303(DCD Lab): B2 /22UECL304(AEC Lab): B1 22UMBA301	
SATURDAY		22UECC300		22UECE322	22USECC305(T)				

Note: TIME-TABLE IS EFFECTIVE FROM 01/09/2025

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	22UMAC300	Engineering Mathematics — III	3	2-2-0	Dr. Basavaraj Hoogar
2	22UECC300	Network Analysis	4	4-0-0	Dr. S. S. Navalgund
3	22UECC301	Digital Circuit Design	3	3-0-0	Prof. M.VijayKumar
4	22UECC302	Analog Electronic Circuits	3	3-0-0	Dr. Vyas Murnal
5	22UECL303	Digital Circuit Design Laboratory	1	0-0-2	Prof. Vinayak Miskin & Prof. M.Vijaykumar
6	22UECL304	Analog Electronic Circuits Laboratory	1	0-0-2	Dr. K. N. Hosur & Dr. Vyas Murnal
7	22USECC305	Signals and Systems	3	2-2-0	Dr. Vijaya C.
8	22UHVK306	Universal Human Values-I	1	1-0-0	Dr. Shashikant S. Kurodi
9	22UECE322	Linear ICs and Applications	1	1-0-0	Prof. Raghuram K.M
10	22UMBA301	Mathematics	Audit	3-0-0	Mathematics Dept.
11	22UNSK307	National Service Scheme	Audit	0-0-2	
		Total Credits	20		

Prof. ECE
 Dept. of Electronics & Communication Engg
 S.D.M. College of Engineering & Technology
 Dharwad-580002, KARNATAKA

Signature
 26/8/25
 Dean (Academic Program)

Signature
PRINCIPAL
SHRI DHARMASAI BALANATHESWARA
COLLEGE OF ENGINEERING AND TECHNOLOGY
DHARWAD-580002, KARNATAKA

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: V
Division: A

TIME TABLE

Academic Year: 2025-26
Hall No.: 25

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm
MONDAY	22UECC501	22UECE522	BREAK	22UECC500	22UECC502(T)		BREAK	22UECL503(DSP Lab): A1 22UECL504(SD Lab): A2	
TUESDAY	22UECL503(DSP Lab): A3/ 22UECL504(SD Lab): A4			22UECC502	22UECE522			22UPYK509	
WEDNESDAY	22UECC502	22UECC500		22UECL503(DSP Lab): A2/ 22UECL504(SD Lab): A1				22UECL505	
THURSDAY	22URMK506	22UECC501		22UECE522	22UECC502(T)			22UECL503(DSP Lab): A4 22UECL504(SD Lab): A3	
FRIDAY	22URMK506	22UESK507		22UECC501	22UECE522			22UECL505	
SATURDAY		22UECC500		22UECC501	22UECC502				

Note: TIME-TABLE IS EFFECTIVE FROM 01/09/2025

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	22UECC500	Management, Economics and Entrepreneurship	3	3-0-0	Prof. S.S. Ravishankar
2	22UECC501	CMOS VLSI Design	4	4-0-0	Dr. J.C. Nidagundi
3	22UECC502	Digital Signal Processing	4	3-2-0	Prof. Kotresh Marali
4	22UECE522	Operating Systems	4	4-0-0	Dr. Hemalata Bhujle
5	22UECL503	Digital Signal Processing Laboratory	1	0-0-2	Prof. Kotresh Marali
6	22UECL504	System Design Laboratory	1	0-0-2	Dr. Vijaya C. & Dr. S. C. Sajjan
7	22UECL505	Minor Project-I	2	0-0-4	Prof. Sumangala N.B & Prof. Raghuram K.M
8	22URMK506	Research Methodology and IPR	2	2-0-0	Dr. S. S. Kerur
9	22UESK507	Environmental studies	1	1-0-0	Prof. Channakka L.
10	22USSK508	Soft Skills-I	Audit	0-0-2	C-III
11	22UPYK509	Physical Education and Yoga	Audit	0-0-2	Dr. Manjunath
		Total Credits	22		

HOD/ECET, Hostel
Dept. of Electronics & Communication Engineering
S.D.M. College of Engineering & Technology

Dean (Academic Program)

Principal
29/8/25

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: V
Division: B

TIME TABLE

Academic Year: 2024-25
Hall No.: 28

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm	
MONDAY	22URMK506	22UECC502	BREAK	22UECC501	22UECE522		BREAK	22UECL505		
TUESDAY	22UECE522	22UECC502		22UECC500	22UECC501			22UPYK509		
WEDNESDAY	22UECL503(DSP Lab): B3/ 22UECL504(SD Lab): B4			22UECC501	22UECC502(T)			22UECL503(DSP Lab): B1/ 22UECL504(SD Lab): B2		
THURSDAY		22UECE522		22UECC500	22UECC501			22UECL505		
FRIDAY	22UECC500	22UECE522		22UECC502(T)	22UESK507			22UECL503(DSP Lab): B2/ 22UECL504(SD Lab): B1		
SATURDAY	22UECC502	22URMK506		22UECL503(DSP Lab): B4/ 22UECL504(SD Lab): B3						

Note: TIME-TABLE IS EFFECTIVE FROM 01/09/2025

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	22UECC500	Management, Economics and Entrepreneurship	3	3-0-0	Prof. S. S. Ravishankar
2	22UECC501	CMOS VLSI Design	4	4-0-0	Dr. J.C. Nidagundi
3	22UECC502	Digital Signal Processing	4	3-2-0	Prof. Kotresh Marali
4	22UECE522	Operating Systems	4	4-0-0	Dr. Hemalata Bhujle
5	22UECL503	Digital Signal Processing Laboratory	1	0-0-2	Prof. Kotresh Marali
6	22UECL504	System Design Laboratory	1	0-0-2	Dr. Vijaya C. & Dr. S. C. Sajjan
7	22UECL505	Minor Project-I	2	0-0-4	Prof. Sumangala N.B & Prof. Raghuram K.M
8	22URMK506	Research Methodology and IPR	2	2-0-0	Prof. Vinayak Miskin
9	22UESK507	Environmental studies	1	1-0-0	Prof. Kotresh Marali
10	22USSK508	Soft Skills-I	Audit	0-0-2	C-III
11	22UPYK509	Physical Education and Yoga	Audit	0-0-2	Dr. Manjunath
		Total Credits	22		

HOD-ECB
 Head of Electronics & Communication Engineering
 S.D.M. College of Engineering & Technology, Dharwad

Dean (Academic Program)

Principal

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: VII
Division: A

TIME TABLE

Academic Year: 2025-26
Hall No.: 31

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm
MONDAY		22UECC700	BREAK	22UECE740	22UECE750	22UECL702	BREAK	22UECL701(CCN Lab): A4	
TUESDAY		22UECO761		22UECC700	22UECE740			22UECL701(CCN Lab): A1	
WEDNESDAY		22UECO761		22UECE750	22UECL701(CCN Lab): A2			22UECL702	
THURSDAY		22UECE750		22UECC700	22UECO761	22UECL702		22UECL701(CCN Lab): A3	
FRIDAY		22UECC700		22UECE740	22UECL702			22UECL702	
SATURDAY	22UECL702			22UECL702					

Note: TIME-TABLE IS EFFECTIVE FROM 01/09/2025

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	22UECC700	Wireless Communication	4	4-0-0	Dr. Sharada Sajjan
2	22UECE740	ASIC Design	3	3-0-0	Dr. Satish S.B
3	22UECE750	Optical Fiber Communication	3	3-0-0	Prof. Preeti S.B
4	22UECO761	Wireless Sensor Networks	3	3-0-0	Prof. Raghuram K.M
5	22UECL701	Communication Networks Laboratory	1	0-0-2	Dr. Sunil Mathad & Prof. Raghuram K.M
6	22UECL702	Major Project-I	6	0-0-12	Dr. Sunil Mathad & Dr. Vyas Murnal
		Total Credits	20		

HOD ECE
Prof. & Head

Dept. of Electronics & Commu- 40n Engg
1st Co' age of Engineering & Technology
Dharwad-Dharwad-580 002

Dean (Academic Program)

Principal
SHRI DHARMASHALA MANUNATHESHWARA
COLLEGE OF ENGINEERING AND TECHNOLOGY
Dharwad-580002, KARNATAKA

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: VII
Division: B

TIME TABLE

Academic Year: 2025-26
Hall No.: as indicated

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm
MONDAY			BREAK	22UECL702			BREAK	22UECL702	
TUESDAY		22UECO761(25)		22UECL701(CCN Lab): B1		22UECL702		22UECC700 (31)	22UECE740 (31)
WEDNESDAY		22UECO761(28)		22UECC700 (25)	22UECE750 (25)	22UECL702		22UECL701(CCN Lab): B2	
THURSDAY		22UECL701(CCN Lab): B3 (9:00 -11:00 am)			22UECO761 (26)	22UECE740 (26)		22UECL702	
FRIDAY		22UECL701(CCN Lab): B4 (9:00 -11:00 am)			22UECC700 (31)	22UECE750 (31)		22UECL702	
SATURDAY		22UECE740(31)		22UECE750 (31)	22UECC700 (31)	22UECL702			

Note: TIME-TABLE IS EFFECTIVE FROM 01/09/2025

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	22UECC700	Wireless Communication	4	4-0-0	Dr. Sharada Sajjan
2	22UECE740	ASIC Design	3	3-0-0	Dr. Satish S.B
3	22UECE750	Optical Fiber Communication	3	3-0-0	Prof. Megha Shidenur
4	22UECO761	Wireless Sensor Networks	3	3-0-0	Dr. Sunil Mathad
5	22UECL701	Communication Networks Laboratory	1	0-0-2	Dr. Sunil Mathad & Prof. Raghuram K.M
6	22UECL702	Major Project-I	6	0-0-12	Dr. Sunil Mathad & Dr. Vyas Murnali

Prof. Dr. C. E. ...
Dept. of Electronics & Communication Engg.
S.D.M. College of Engineering and Technology

Dean (Academic Program)

PRINCIPAL
SHRI DHARMASWALA MANJUNATH SWARA
COLLEGE OF ENGINEERING AND TECHNOLOGY
DUDHANUR, DUDHANUR, KARNATAKA