

# S.D.M. College of Engineering and Technology, Dharwad

## Department of Electronics and Communication Engineering

### TIME TABLE

Academic Year: 2023-24

Class: M.Tech

Division: -

Hall No.: PG-I

Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30/5 pm
<b>MONDAY</b>			<b>BREAK</b>				<b>BREAK</b>		
<b>TUESDAY</b>		22PDEE152		22PDEC102	22PDEC101			22PDEC100	
<b>WEDNESDAY</b>		22PDEC102		22PRIC100	22PDEE152			<b>22PDEL102</b>	
<b>THURSDAY</b>		22PDEE152		22PDEC101	22PRIC100			22PDEC100	
<b>FRIDAY</b>		22PDEE152		22PDEC102	22PDEC101			<b>22PDEL101</b>	
<b>SATURDAY</b>		22PRIC100		22PDEC101	22PDEC102				

**Note: TIME-TABLE IS EFFECTIVE FROM 26/02/2024**

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	22PRIC100	Research Methodology and IPR	3	3-0-0	Prof. Prakash Gani
2	22PDEC100	Applied Mathematics	4	4-0-0	Dr. Basavaraj Hugar
3	22PDEC101	Sequential Machines and Fault Analysis	4	4-0-0	Dr. Sharada C.Sajjan
4	22PDEC102	Digital System Design Using Verilog	4	4-0-0	Dr. S. S. Kerur
5	22PDEE152	Nano Electronics	4	4-0-0	Dr. Vyas Murnal
6	22PDEL101	Digital Circuits Simulation Laboratory	2	0-0-3	Dr. S.S.Kerur & Dr. Sunil S. Mathad
7	22PDEL102	Seminar	1	0-0-2	Dr. Vijaya C & Dr. Shreedhar A. Joshi
<b>Total Credits</b>			<b>22</b>		

**HOD ECE**

**Dean (Academic Program)**

**Principal**

**S.D.M. College of Engineering and Technology, Dharwad**  
**Department of Electronics and Communication Engineering**

Class: **PG-III SEM**

**TIME TABLE**

**Academic Year: 2023-24**

Hall No.: **PG Room 2**

Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 4.30 pm
<b>MONDAY</b>		22PDEE351	<b>BREAK</b>	22PDEC300	22PDEE350	22PDEE356	<b>BREAK</b>	22PDEL301
<b>TUESDAY</b>		22PDEE356		22PDEE351	22PDEC300	22PDEE350		22PDEL301
<b>WEDNESDAY</b>		22PDEC300		22PDEE350	22PDEE356			22PDEL301
<b>THURSDAY</b>		22PDEE356		22PDEE351	22PDEC300			
<b>FRIDAY</b>								
<b>SATURDAY</b>								

**Note: TIME-TABLE IS EFFECTIVE FROM 11/12/2023**

S.No.	Course Code	Course Title	Course Credits	Course Instructor
1	22PDEC300	VLSI for Signal Processing	4	Dr. Vijaya C.
2	22PDEE350	Advances in VLSI Design	3	Dr. Bhairu Saptalkar
3	22PDEE351	Error Control Coding	3	Dr. S. V. Virakthmath
4	22PDEE356	Advanced Computer Architecture	4	Prof. Kotresh Marali
5	22PDEL301	Project Phase-1	6	Dr. Vijaya C., Dr. Sharada C. Sajjan

**HOD ECE**

**Dean Academics**

**Principal**