

**S.D.M. College of Engineering and Technology, Dharwad**  
**Department of Electronics and Communication Engineering**

Class: I  
 Division: EC-A

**TIME TABLE**

Academic Year: 2024-25  
 Hall No.: 6

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm
MONDAY		22ENG16	<b>BREAK</b>	22ETC15D	22BEE13		<b>BREAK</b>	PHY Lab (22PHYE12): A1 batch	
TUESDAY	22BEE13	22MATE11		22ESC145	22PHYE12			MATHS Lab(22MATE11)	
WEDNESDAY				22MATE11(T)	22ESC145			PHY Lab(22PHYE12): A2 batch	
THURSDAY		22ETC15D		22MATE11(T)	22ESC145(T)			22PHYE12(T)	22BEE13(T)
FRIDAY				22PHYE12	22IDT18			22BEE13(T)	22ESC145(T)
SATURDAY	22MATE11	22KSK17/ 22KBK17		22ETC15D	22PHYE12(T)				

Note: TIME-TABLE IS EFFECTIVE FROM //2024

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	22MATE11	Mathematics –I for EEE stream	4	2-2-2	Dr. D.P. Basti
2	22PHYE12	Physics for EEE stream	4	2-2-2	Dr. Kumar Maddani
3	22BEE13	Basic Electronics	3	2-2-0	Prof. Sumangala N.B
4	22ESC145	Principles of Programming Using C	3	2-2-0	Dr. S. V. Viraktamath
5	22ETC15D	Introduction to Embedded Systems	3	3-0-0	Prof. Reshma Nadaf
6	22ENG16	Communicative English	1	1	Dr. Shashikant S. Kurodi
7	22KSK17/ 22KBK17	Sanskrutika Kannada/Balake Kannada	1	1	Prof. Maruti Kadam
8	22IDT18	Innovation and Design Thinking	1	1	Prof. M.Vijaykumar
		<b>Total Credits</b>	20		

HOD ECE

31/2/24  
 Dean (Academic Program)

Principal

**S.D.M. College of Engineering and Technology, Dharwad**  
**Department of Electronics and Communication Engineering**

Class: I  
 Division: EC-B

**TIME TABLE**

Academic Year: 2024-25  
 Hall No.: 7

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm
MONDAY		22BEE13	<b>BREAK</b>	22PHYE12	22MATE11		<b>BREAK</b>	22ESC145(T)	22ESC145(T)
TUESDAY	22ESC145	22ETC15D		22BEE13	22ENG16			22MATE11(T)	22MATE11(T)
WEDNESDAY	22ETC15D	22MATE11		<b>PHY Lab (22PHYE12): B1 batch</b>				<b>MATHS Lab(22MATE11)</b>	
THURSDAY	22ESC145	22PHYE12		22BEE13(T)	22ETC15D				
FRIDAY		22IDT18		22BEE13(T)	22PHYE12(T)			<b>PHY Lab(22PHYE12): B2 batch</b>	
SATURDAY				22KSK17/ 22KBK17	22PHYE12(T)				

Note: TIME-TABLE IS EFFECTIVE FROM //2024

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	22MATE11	Mathematics –I for EEE stream	4	2-2-2	Dr. Basavaraj H.
2	22PHYE12	Physics for EEE stream	4	2-2-2	Dr. Kumar Maddani
3	22BEE13	Basic Electronics	3	2-2-0	Prof. Sumangala N.B
4	22ESC145	Principles of Programming Using C	3	2-2-0	Dr. S. V. Viraktamath
5	22ETC15D	Introduction to Embedded Systems	3	3-0-0	Prof. Reshma Nadaf
6	22ENG16	Communicative English	1	1	Dr. Shashikant S. Kurodi
7	22KSK17/ 22KBK17	Sanskrutika Kannada/Balake Kannada	1	1	Prof. Maruti Kadam
8	22IDT18	Innovation and Design Thinking	1	1	Prof. Kotresh Marali
		<b>Total Credits</b>	20		

HOD/ECE

Dean (Academic Program)

Principal

**S.D.M. College of Engineering and Technology, Dharwad**  
**Department of Electronics and Communication Engineering**

Academic Year: 2024-25  
Hall No.: 26

Class: III  
Division: A

**TIME TABLE**

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm
MONDAY		22USECC305	BREAK	22UECC302	22UECC301		BREAK	22UECL304(AEC Lab): A1 /22UECL303(DCD Lab): A2	
TUESDAY	22UECC302	22UECC301		22UECC300	22UECE322			22UNSK307(NSS)	
WEDNESDAY	22UECL304(AEC Lab): A3 /22UECL303(DCD Lab): A4			22UECC300	22USECC305(T)			22UMAC300(T)	22UMAC300(T)
THURSDAY	22UECC300	22USECC305		22UECL304(AEC Lab): A4 /22UECL303(DCD Lab): A3				22UMBA301	
FRIDAY		22UMAC300		22USECC305(T)	22UECC300			22UECL304(AEC Lab): A2 /22UECL303(DCD Lab): A1/ 22UMBA301	
SATURDAY	22UMAC300	22UHVK306		22UECC301	22UECC302				

Note: TIME-TABLE IS EFFECTIVE FROM 02/09/2024

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	22UMAC300	Engineering Mathematics — III	3	2-2-0	Dr. Shailaja Shirakol
2	22UECC300	Network Analysis	4	4-0-0	Dr. S. S. Navalgund
3	22UECC301	Digital Circuit Design	3	3-0-0	Prof. M. Vijaykumar
4	22UECC302	Analog Electronic Circuits	3	3-0-0	Dr. K. N. Hosur
5	22UECL303	Digital Circuit Design Laboratory	1	0-0-2	Dr. S. S. Kerur and Prof. Vinayak Miskin
6	22UECL304	Analog Electronic Circuits Laboratory	1	0-0-2	Dr. K. N. Hosur and Dr. Vyas Murnal
7	22USECC305	Signals and Systems	3	2-2-0	Dr. Vijaya C.
8	22UHVK306	Universal Human Values-I	1	1-0-0	Dr. Shashikant S. Kurodi
9	22UECE322	Linear ICs and Applications	1	1-0-0	Dr. Vyas Murnal
10	22UMBA301	Mathematics	Audit	3-0-0	Mathematics Dept.
11	22UNSK307	National Service Scheme	Audit	0-0-2	
		<b>Total Credits</b>	20		

Prof. & Head  
**HODECE**  
Dept. of Electronics and Communication Engg.  
SDM College of Engineering & Technology,  
Dharwad-580 017

31/8/24  
Dean (Academic Program)

Principal  
*(Signature)*

**S.D.M. College of Engineering and Technology, Dharwad**  
**Department of Electronics and Communication Engineering**

Class: III  
 Division: B

**TIME TABLE**

Academic Year: 2024-25  
 Hall No.: 27

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm
MONDAY		22UECC300	<b>BREAK</b>	22UECC301	22USECC305(T)		<b>BREAK</b>	22UMAC300 (T)	22UMAC300 (T)
TUESDAY	22UECC300	22UECC301		22UMAC300	22USECC305(T)			22UNSK307(NSS)	
WEDNESDAY		22UECC300		22UHVK306	22UMAC300			22UECL304(AEC Lab): B1 /22UECL303(DCD Lab): B2	
THURSDAY	22UECC301	22USECC305		22UECC300	22UECC302			22UECL304(AEC Lab): B2 /22UECL303(DCD Lab): B1/ 22UMBA301(2:30-3:30)	
FRIDAY	22UECL304(AEC Lab): B3 /22UECL303(DCD Lab): B4			22USECC305	22UECC302			22UMBA301	
SATURDAY	22UECE322	22UECC302		22UECL304(AEC Lab): B4 /22UECL303(DCD Lab): B3					

**Note: TIME-TABLE IS EFFECTIVE FROM 02/09/2024**

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	22UMAC300	Engineering Mathematics — III	3	2-2-0	Dr. Basavaraj H.
2	22UECC300	Network Analysis	4	4-0-0	Dr. S. S. Navalgund
3	22UECC301	Digital Circuit Design	3	3-0-0	Dr. S. S. Kerur
4	22UECC302	Analog Electronic Circuits	3	3-0-0	Dr. K. N. Hosur
5	22UECL303	Digital Circuit Design Laboratory	1	0-0-2	Dr. S. S. Kerur and Prof. Vinayak Misikin
6	22UECL304	Analog Electronic Circuits Laboratory	1	0-0-2	Dr. K. N. Hosur and Dr. Vyas Murnal
7	22USECC305	Signals and Systems	3	2-2-0	Prof. Savitri Raju
8	22UHVK306	Universal Human Values-I	1	1-0-0	Dr. Shashikant S. Kurodi
9	22UECE322	Linear ICs and Applications	1	1-0-0	Prof. Raghuram K.M
10	22UMBA301	Mathematics	Audit	3-0-0	Mathematics Dept.
11	22UNSK307	National Service Scheme	Audit	0-0-2	Dr. Manjunath
		<b>Total Credits</b>	20		

Prof. & Head  
 HOD/ECE  
 Dept. of Electronics & Communication Engineering  
 S.D.M. College of Engineering & Technology

31/8/24  
 Dean (Academic Program)

Principal  
 31/8/24

**S.D.M. College of Engineering and Technology, Dharwad**  
**Department of Electronics and Communication Engineering**

Class: V  
 Division: A

**TIME TABLE**

Academic Year: 2024-25  
 Hall No.: 25

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm
MONDAY	22UECL503(DSP Lab): A1/ 22UECL504(SD Lab): A2		<b>BREAK</b>	22UECC501	22UECC502		<b>BREAK</b>	22UNSK509(NSS)	
TUESDAY		22UECC501		22UECE522	22UECC500			22UECL503(DSP Lab): A2/ 22UECL504(SD Lab): A3	
WEDNESDAY	22UECC501	22UECC502		22URMK506	22UECE522			22UECL503(DSP Lab): A3/ 22UECL504(SD Lab): A1	
THURSDAY	22UECE522	22UESK507		22UECC500	22UECC502(T)			22UECL505(Minor Project-I)	
FRIDAY		22UECE522		22UECC502	22URMK506			22UECL505(Minor Project-I)	
SATURDAY		22UECC501		22UECC500	22UECC502(T)				

**Note: TIME-TABLE IS EFFECTIVE FROM 02/09/2024**

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	22UECC500	Management, Economics and Entrepreneurship	3	3-0-0	Dr. Satish S.B
2	22UECC501	CMOS VLSI Design	4	4-0-0	Dr. J.C. Nidagundi
3	22UECC502	Digital Signal Processing	4	3-2-0	Prof. Kotresh Marali
4	22UECE522	Operating Systems	4	4-0-0	Dr. Hemalata Bhujle
5	22UECL503	Digital Signal Processing Laboratory	1	0-0-2	Prof. Kotresh M & Prof. Shrikant K.S
6	22UECL504	System Design Laboratory	1	0-0-2	Dr. S. C. Sajjan & Prof. M. Vijaykumar
7	22UECL505	Minor Project-I	2	0-0-4	Prof. Sumangala N.B & Prof. Raghuram K.M
8	22URMK506	Research Methodology and IPR	2	2-0-0	Prof. Vinayak Miskin
9	22UESK507	Environmental studies	1	1-0-0	Prof. Channakka L.
10	22USSK508	Soft Skills-I	Audit	0-0-2	C-III
11	22UNSK509	National Service Scheme	Audit	0-0-2	Prof. S. S. Ravishankar
		<b>Total Credits</b>	22		

Prof. & Head  
**HOD ECE**  
 Dept. of Electronics & Communication Engineering  
 S.D.M. College of Engineering & Technology  
 Dharwad, Maharashtra - 581 002

21/9/24  
 Dean (Academic Program)

Principal  
 21/9/24

**S.D.M. College of Engineering and Technology, Dharwad**  
**Department of Electronics and Communication Engineering**

Class: V  
 Division: B

**TIME TABLE**

Academic Year: 2024-25  
 Hall No.: 28

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm
MONDAY		22UECE522	<b>BREAK</b>	22UECC500	22UECC502(T)		<b>BREAK</b>	22UNSK509(NSS)	
TUESDAY	22UECL503(DSP Lab): B2/ 22UECL504(SD Lab): B3			22UECC502(T)	22UECC500			22UECL505(Minor Project-I)	
WEDNESDAY	22URMK506	22UECE522		22UECC502	22UECC501			22UECL505(Minor Project-I)	
THURSDAY		22UECC501		22UECE522	22UECC502			22UECL503(DSP Lab): B3/ 22UECL504(SD Lab): B1	
FRIDAY		22UECC501		22UESK507	22UECC500			22UECL503(DSP Lab): B1/ 22UECL504(SD Lab): B2	
SATURDAY	22UECC502	22URMK506		22UECE522	22UECC501				

**Note: TIME-TABLE IS EFFECTIVE FROM 02/09/2024**

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	22UECC500	Management, Economics and Entrepreneurship	3	3-0-0	Prof. S. S. Ravishankar
2	22UECC501	CMOS VLSI Design	4	4-0-0	Dr. J.C. Nidagundi
3	22UECC502	Digital Signal Processing	4	3-2-0	Prof. Shrikant K.S
4	22UECE522	Operating Systems	4	4-0-0	Dr. Hemalata Bhujle
5	22UECL503	Digital Signal Processing Laboratory	1	0-0-2	Prof. Kotresh M & Prof. Shrikant K.S
6	22UECL504	System Design Laboratory	1	0-0-2	Dr. S. C. Sajjan & Prof. M. Vijaykumar
7	22UECL505	Minor Project-I	2	0-0-4	Prof. Sumangala N.B & Prof. Raghuram K.M
8	22URMK506	Research Methodology and IPR	2	2-0-0	Dr. S. S. Kerur
9	22UESK507	Environmental studies	1	1-0-0	Prof. Channakka L.
10	22USSK508	Soft Skills-I	Audit	0-0-2	C-III
11	22UNSK509	National Service Scheme	Audit	0-0-2	Prof. S. S. Ravishankar
		<b>Total Credits</b>	22		

Prof. & Head  
 HOD/ECB  
 Dept. of Electronics & Communication Engg.  
 S.D.M. College of Engineering & Technology  
 Dharwad-580 002

31/8/24  
 Dean (Academic Program)

Principal  
 31/8/24

**S.D.M. College of Engineering and Technology, Dharwad**  
**Department of Electronics and Communication Engineering**

Class: VII  
 Division: A

**TIME TABLE**

Academic Year: 2024-25  
 Hall No.: 31

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm	
MONDAY		21UECE731	BREAK	21UECE740	21UECE750		BREAK	21UECL701(CCN Lab): A1		
TUESDAY		21UECO760		21UECE750	21UHUC700			21UECE731	21UECC700	
WEDNESDAY	21UECO760	21UECE740		21UECL701(CCN Lab): A2				21UECL702 (Major Project Phase-1)		
THURSDAY	21UECE750	21UHUC700		21UECC700	21UECO760			21UECL702 (Major Project Phase-1)		
FRIDAY	21UECE740	21UECC700		21UECL701(CCN Lab): A3						
SATURDAY				21UECC700	21UECE731					

Note: TIME-TABLE IS EFFECTIVE FROM 02/09/2024

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	21UECC700	Wireless Communication	3	2-2-0	Dr. S. C. Sajjan
2	21UECE731	Wireless Sensor Networks	3	3-0-0	Prof. Raghuram K.M
3	21UECE740	ASIC Design	3	3-0-0	Dr. Satish S.B
4	21UECE750	Optical Fiber Communication	3	3-0-0	Dr. S. A. Joshi
5	21UECO760	MEMS	3	3-0-0	Dr. Sunil Mathad
6	21UHUC700	Research Methodology	2	2-0-0	Prof. Vinayak Miskin
7	21UECL701	Communication Networks Laboratory	1	0-0-2	Dr. Sunil Mathad & Prof. Preeti S.B. & Prof. Raghuram K.M
8	21UECL702	Major Project Phase-I	2	0-0-4	Dr. Sunil Mathad & Dr. Vyas Murnal
9	21UECL703	Internship-II	3	4-6 weeks	Dr. Hemalata B. & Prof. Preeti S.B
		<b>Total Credits</b>	23		

HOD ECE

Dean (Academic Program)

Principal

**S.D.M. College of Engineering and Technology, Dharwad**  
**Department of Electronics and Communication Engineering**

Class: VII  
 Division: B

**TIME TABLE**

Academic Year: 2024-25  
 Hall No.: as indicated

Day	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30 pm	
MONDAY	21UECC700 (25)	21UHUC700 (25)	<b>BREAK</b>	21UECL701(CCN Lab): B1			<b>BREAK</b>			
TUESDAY	21UECE750 (28)	21UECO760 (28)		21UECL701(CCN Lab): B2					21UECL702 (Major Project Phase-1)	
WEDNESDAY	21UECO760 (26)	21UECC700 (26)		21UECE731 (31)	21UECE740 (31)				21UECL702 (Major Project Phase-1)	
THURSDAY	21UECL701(CCN Lab): B3			21UECE731 (26)	21UECO760 (26)				21UECE740 (25)	21UECE750 (25)
FRIDAY		21UECE731 (27)		21UECE740 (31)	21UECC700 (31)					
SATURDAY		21UECE750 (31)		21UHUC700 (27)	21UECC700 (27)					

Note: TIME-TABLE IS EFFECTIVE FROM 02/09/2024

S.No.	Course Code	Course Title	Course Credits	L-T-P	Course Instructor
1	21UECC700	Wireless Communication	3	2-2-0	Prof. Preeti S.B
2	21UECE731	Wireless Sensor Networks	3	3-0-0	Prof. Raghuram K.M
3	21UECE740	ASIC Design	3	3-0-0	Dr. Satish S.B
4	21UECE750	Optical Fiber Communication	3	3-0-0	Dr. S. A. Joshi
5	21UECO760	MEMS	3	3-0-0	Dr. Vyas Murnal
6	21UHUC700	Research Methodology	2	2-0-0	Dr. S. S. Kerur
7	21UECL701	Communication Networks Laboratory	1	0-0-2	Dr. Sunil Mathad & Prof. Preeti S.B & Prof. Raghuram K.M
8	21UECL702	Major Project Phase-1	2	0-0-4	Dr. Sunil Mathad & Dr. Vyas Murnal
9	21UECL703	Internship-II	3	4-6 weeks	Dr. Hemalata B. & Prof. Preeti S.B
		<b>Total Credits</b>	23		

HOD ECE

25/8/24  
 Dean (Academic Program)

Principal