

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: **III Semester B.E.**

TIME TABLE

Academic Year: 2019-20

Division: **A**

Hall No.: **26**

Lab Batches: A1 :

A2 :

A3 :

A4 :

Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30pm
MONDAY	ECL305 A1 ECL306 A2 <small>(8am to 11am) (11am to 11.30am Break)</small>		BREAK	-	ECC300	ECC303	BREAK	ECC301	ECC302
TUESDAY	-	MAC300		ECC304	ECC303	-		ECL305 A2 ECL306 A1 <small>(2pm to 5pm)</small>	
WEDNESDAY	ECL305 A3 ECL306 A4 <small>(8am to 11am) (11am to 11.30am Break)</small>			-	MAC300	ECC304		ECC300	ECC300(T)
THURSDAY	-	ECC304		ECC302	ECC300	-		ECL305 A4 ECL306 A3 <small>(2pm to 5pm)</small>	
FRIDAY	ECC301	ECC303		MAC300	ECC302	ECC302(T)			
SATURDAY	ECC300	ECC301		ECC302	ECC303				

Note: i.) TIME-TABLE IS EFFECTIVE FROM 01-08-2019

ii.) Laboratory Duration : 03 Hrs

S.No.	Course Code	Course Title	Course Credits	Course Instructor
1	18UMAC300	Engineering Mathematics-III	3	Prof. Preeti B. Jinagouda
2	18UECC300	Electromagnetic Theory	4	Prof. M. Vijay Kumar
3	18UECC301	Digital Circuit Design	3	Prof. Mala L.M.
4	18UECC302	Network Analysis	4	Prof. Kalmeshwar N. Hosur
5	18UECC303	Analog Electronic Devices and Circuits	4	Prof. Vyas R. Murnal
6	18UECC304	Signal and Systems	3	Prof. Savitri Raju
7	18UECL305	Analog Electronic Devices and Circuits Laboratory	1.5	Prof. Kalmeshwar N. Hosur; Prof. Vyas R. Murnal
8	18UECL306	Digital Circuit Design Laboratory	1.5	Prof. Mala L.M. ; Prof. Vinayak Miskin

Note: T—Tutorial Class

HOD ECE

Dean (Academic Program)

Principal

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: **III Semester B.E.**

TIME TABLE

Academic Year: 2019-20

Division: **B**

Hall No.: **27**

Lab Batches: B1 :

B2 :

B3 :

B4 :

Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30pm
MONDAY	-	ECC300	BREAK	ECC303	ECC302	-	BREAK	ECL305 B1 ECL306 B2 (2pm to 5pm)	
TUESDAY	ECL305 B2 ECL306 B1 (8am to 11am) (11am to 11.30am Break)			-	ECC302	ECC302(T)		ECC303	ECC301
WEDNESDAY	ECC303	ECC304		ECC302	ECC301	-		ECL305 B3 ECL306 B4 (2pm to 5pm)	
THURSDAY	ECL305 B4 ECL306 B3 (8am to 11am) (11am to 11.30am Break)			-	MAC300	ECC304		ECC303	ECC300
FRIDAY	ECC302	ECC301		MAC300	ECC300	ECC300(T)			
SATURDAY	-	MAC300		ECC300	ECC304				

Note: i.) TIME-TABLE IS EFFECTIVE FROM 01-08-2019

ii.) Laboratory Duration : 03 Hrs

S.No.	Course Code	Course Title	Course Credits	Course Instructor
1	18UMAC300	Engineering Mathematics-III	3	Prof. Varsha Joshi
2	18UECC300	Electromagnetic Theory	4	Prof. M. Vijay Kumar
3	18UECC301	Digital Circuit Design	3	Prof. Vinayak Miskin
4	18UECC302	Network Analysis	4	Prof. Kalmeshwar N. Hosur
5	18UECC303	Analog Electronic Devices and Circuits	4	Prof. Vyas R. Murnal
6	18UECC304	Signal and Systems	3	Prof. Shrikanth K. Shirakol
7	18UECL305	Analog Electronic Devices and Circuits Laboratory	1.5	Prof. Kalmeshwar N. Hosur; Prof. Vyas R. Murnal
8	18UECL306	Digital Circuit Design Laboratory	1.5	Prof. Mala L.M. ; Prof. Vinayak Miskin

Note: T—Tutorial Class

HOD ECE

Dean (Academic Program)

Principal

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: V Semester B.E.

TIME TABLE

Academic Year: 2019-20

Division: A

Hall No.: 25

Lab Batches: A1 :

A2 :

A3 :

A4 :

Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30pm
MONDAY	15UECL504 A1 15UECL505 A2		BREAK	ECC500	ECE514	ECC502	BREAK	ECC501	ECE521
TUESDAY	ECC500	ECC503		ECE521	ECC502	-		15UECL504 A2 15UECL505 A1	
WEDNESDAY	15UECL504 A3 15UECL505A4			ECC503	ECE521	-		ECC500	ECE514
THURSDAY	-	ECC501		ECE514	ECC503	ECC502		15UECL504 A4 15UECL505 A3	
FRIDAY	-	ECE514		ECE521	ECC501	ECC503			
SATURDAY	-	ECC501		ECC500	ECC502				

Note: i.) TIME-TABLE IS EFFECTIVE FROM 01-08-2019

S.No.	Course Code	Course Title	Course Credits	Course Instructor
1	15UECC500	Information Theory and Coding	4	Prof. S.V. Viraktamath
2	15UECC501	Digital Signal Processing	4	Prof. Vijaya C.
3	15UECC502	Digital Communication	4	Prof. Sharada C. Sajjan
4	15UECC503	CMOS VLSI Design	4	Prof. Jayashree C. Nidagundi
5	15UECE514	Scientific Computing Using Python	3	Prof. Siddalingesh S. Navalgund
6	15UECE521	Operating System	3	Prof. J.V. Sangeethagouda
7	15UECL504	Microcontroller and VLSI Laboratory	1	Prof. Jayashree C. Nidagundi; Prof. Raghuram K.M.
8	15UECL505	Digital Signal Processing Laboratory	1	Prof. Vijaya C.; Prof. Kotresh E. Marali

Note: T—Tutorial Class

HOD ECE

Dean (Academic Program)

Principal

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: V Semester B.E.

TIME TABLE

Academic Year: 2019-20

Division: B

Hall No.: 28

Lab Batches: B1 :

B2 :

B3 :

B4 :

Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30pm
MONDAY	ECC500	ECE521	BREAK	ECC503	ECC501	-	BREAK	15UECL504 B1 15UECL505 B2	
TUESDAY	15UECL504 B2 15UECL505 B1			ECE514	ECC501	ECC503		ECE521	ECC500
WEDNESDAY	ECC503	ECC502		ECE521	ECE514	-		15UECL504 B3 15UECL505 B4	
THURSDAY	15UECL504 B4 15UECL505 B3			ECC501	ECE521	ECC503		ECE514	ECC502
FRIDAY	-	ECC501		ECC502	ECC500	-			
SATURDAY	-	ECC502		ECC500	ECE514				

Note: i.) TIME-TABLE IS EFFECTIVE FROM 01-08-2019

S.No.	Course Code	Course Title	Course Credits	Course Instructor
1	15UECC500	Information Theory and Coding	4	Prof. Reshma Nadaf
2	15UECC501	Digital Signal Processing	4	Prof. Kotresh E. Marali
3	15UECC502	Digital Communication	4	Prof. Sharada C. Sajjan
4	15UECC503	CMOS VLSI Design	4	Prof. Sunil S. Mathad
5	15UECE514	Scientific Computing Using Python	3	Prof. Siddalingesh S. Navalgund
6	15UECE521	Operating System	3	Prof. S.V. Viraktamath
7	15UECL504	Microcontroller and VLSI Laboratory	1	Prof. Jayashree C. Nidagundi; Prof. Raghuram K.M.
8	15UECL505	Digital Signal Processing Laboratory	1	Prof. Vijaya C.; Prof. Kotresh E. Marali

Note: T—Tutorial Class

HOD ECE

Dean (Academic Program)

Principal

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: VII Semester B.E.

TIME TABLE

Academic Year: 2019-20

Division: A

Hall No.: 31

Lab Batches: A1 :

A2 :

A3 :

A4 :

Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30pm
MONDAY	ECE761	ECC703	BREAK	15UECL705 A1		-	BREAK	ECE756	ECC700
TUESDAY	ECC701	ECC700		15UECL705 A2		-		ECC703	ECC702
WEDNESDAY	ECC700	ECE756		ECC703	ECC702	-		ECC701	ECE761
THURSDAY	ECE761	ECC701		ECC702	ECC703	ECE756		15UECL706	
FRIDAY	15UECL705 A3			ECC700	ECE761	ECE756		15UECL706	
SATURDAY	15UECL705 A4			ECC702	ECC701				

Note: i.) TIME-TABLE IS EFFECTIVE FROM 01-08-2019

S.No.	Course Code	Course Title	Course Credits	Course Instructor
1	15UECC700	Real Time Operating System	4	Prof. S.S. Kerur
2	15UECC701	ASIC Design	4	Prof. Satish S. Bhairannawar
3	15UECC702	Antenna and Wave Propagation	4	Prof. S.S. Ravishankar
4	15UECC703	Computer Communication Networks	4	Prof. Gopal A. Bidkar
5	15UECE756	Automotive Electronics	3	Prof. Megha G. Shidenur
6	15UECE761	Optical Fiber Communication	3	Prof. Shreedhar A. Joshi
7	15UECL705	CCN and Embedded System Laboratory	1	Prof. Gopal A. Bidkar; Prof. J.V. Sangeethagouda
8	15UECL706	Major Project Phase-I		Prof. Sunil S. Mathad; Prof. Vyas R. Murnal

Note: T—Tutorial Class

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Principal

S.D.M. College of Engineering and Technology, Dharwad
Department of Electronics and Communication Engineering

Class: VII Semester B.E.

TIME TABLE

Academic Year: 2019-20

Division: B

Hall No.: As Indicated

Lab Batches: B1 :

B2 :

B3 :

B4 :

Days	8 to 9 am	9 to 10 am	10 to 10.30 am	10.30 to 11.30 am	11.30 to 12.30 pm	12.30 to 1.30pm	1.30 to 2.30 pm	2.30 to 3.30 pm	3.30 to 4.30pm
MONDAY	ECC700 R26	ECE756 R26	BREAK	ECC701 R31	ECE761 R31	-	BREAK	ECC702 R27	ECC703 R27
TUESDAY	ECC703 R27	ECC700 R27		ECE756 R31	ECC702 R31	-		ECC701 R26	ECE761 R26
WEDNESDAY	ECE756 R26	ECE761 R26		15UECL705 B1		ECC701 R31		ECC700 R27	ECC702 R27
THURSDAY	ECC703 R27	ECE756 R27		15UECL705 B2		ECC702 R26		15UECL706	
FRIDAY	ECC701 R31	ECC703 R31		15UECL705 B3		-		15UECL706	
SATURDAY	ECE761 R31	ECC700 R31		15UECL705 B4					

Note: i.) TIME-TABLE IS EFFECTIVE FROM 01-08-2019

S.No.	Course Code	Course Title	Course Credits	Course Instructor
1	15UECC700	Real Time Operating System	4	Prof. Bairu K. Saptalakar
2	15UECC701	ASIC Design	4	Prof. Satish S. Bhairannawar
3	15UECC702	Antenna and Wave Propagation	4	Prof. S.S. Ravishankar
4	15UECC703	Computer Communication Networks	4	Prof. Gopal A. Bidkar
5	15UECE756	Automotive Electronics	3	Prof. Raghuram K. M.
6	15UECE761	Optical Fiber Communication	3	Prof. Shreedhar A. Joshi
7	15UECL705	CCN and Embedded System Laboratory	1	Prof. Gopal A. Bidkar; Prof. J.V. Sangeethagouda
8	15UECL706	Major Project Phase-I		Prof. Sunil S. Mathad; Prof. Vyas R. Murnal

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