

**Sri Dharmasthala Manjunatheshwara College of Engineering & Technology,
Davalagiri, Dharwad-2
DEPARTMENT OF INFORMATION SCIENCE AND ENGINEERING
TIME TABLE (even)**

Academic Year: 2017-18

IV SEM

HALL NO. 30

Days	8:00 to 9:00	9:00 to 10:00	10:00 to 10:30	10:30 to 11:30	11:30 to 12:30	12:30 to 1:30	1:30 to 2:30	2:30 to 3:30	3:30 to 4:30	4:30 to 5:00
Mon	FAFL	ADA	B r e a k	MC	DC	OOPS	B r e a k	FAFL(T)		
Tue	ADA	DC		OOPS	MATHS	FAFL		OOPS-B1 /ADA-B2 / MC Lab-B3		
Wed	MATHS	MC		OOPS -B2 /ADA -B3 / MC LabB4						
Thu	MC	FAFL		MATHS	OOPS	ADA		OOPS -B3/ADA -B4/ MC Lab -B1		
Fri	DC	MC		OOPS -B4/ADA -B1/ MC Lab -B2						
Sat	OOPS	MATHS		DC						

Note: Time Table will be in force from: 16-01-2018

Sr.No	Subjects:	Code	Faculty
1	Engineering Mathematics	15UMAC400	Sri Srikant Kanguri
2	Object Oriented Programming	15UISC400	Sri Naveen Mirajkar
3	Analysis and Design of Algorithms	15UISC401	Ms. Anita Dixit
4	Data Communication	15UISC402	Ms. Deepa Bendigeri
5	Microcontroller, 8051	15UISC403	Dr. J.D Pujari
6	Finit Automata & Formal Language	15UISC404	Ms. Pushpalatha N
7	Microcontroller Lab	15UISL405	SJ, DB
8	OOPS Lab	15UISL406	NM, TAB
9	Analysis and Design of Algorithms lab	15UISC401	AD, AN

**Lab Batches
B1-1 to19
B2-20 to 38
B3-39 to 57
B4-58 onwards**

HEAD ISE DEPT.