

**Sri Dharmasthala Manjunatheshwara College of Engineering & Technology,  
Davalagiri, Dharwad-2  
DEPARTMENT OF INFORMATION SCIENCE AND ENGINEERING  
TIME TABLE (Even)**

Academic Year: 2017-18 odd

VIII SEM

SEMINAR HALL

Days	8:00 to 9:00	9:00 to 10:00	10:00 to 10:30	10:30 to 11:30	11:30 to 12:30	12:30 to 1:30	1:30 to 2:30	2:30 to 3:30	3:30 to 4:30	4:30 to 5:00	
Mon			B r e a k	BDA	IOT		B r e a k	PROJECT -B1			
Tue		BDA		SA / WSN (Lab5)							
Wed		SA / WSN (Lab5)		BDA	IOT				PROJECT - B2		
Thu		IOT		SA / WSN (Lab5)							
Fri		SA / WSN (Lab5)		IOT							
Sat											

Note: Time Table will be in force from: 16-01-2018

Sr.No	Subjects:	Code	Faculty
1	Big data Analytics	11UISC800	Dr. Vandana Bhat
2	Phase Project II	11UISL801	Ms. Varsha Jadhav,VKP
3	Seminar	11UISL802	Sri Tanweer Ahmed
4	Internet of things	11UISE852	Dr. S.R Biradar
5	Software Architecture	11UISE860	Sri Tanweer Ahmed
6	Wireless Sensor Networks	11UISE862	Sri Sachidanand joshi

HEAD ISE DEPT.





