

# SDM College of Engineering & Technology, Dharwad-02

## Department of Electronics & Communication Engineering

### Details of Research Centre

#### I. Research guides details of the department:

Sl.No	Name	Research Centre	Research Area	Total no. of students
01	Prof. Dr. G.A Bidkar	SDMCET, Dharwad	Smart Antennas	07
02	Prof. Dr. Vijaya C	SDMCET, Dharwad	Signal Processing	05
03	Prof. Dr. Shreedhar Joshi	SDMCET, Dharwad	Wireless Communication	06
04	Prof. Dr.Satish Bhairannavar	SDMCET Dharwad	VLSI for Image processing applications	05
05	Dr. S. S. Kerur	SDMCET, Dharwad	VLSI Architecture & Embedded System	04

## II. List of the research scholars awarded with Ph.D degree at our research centre (E&CE, SDMCET)

### Internal Candidates: 05

Sl. No.	Name	Name of the Institution	Mobile No	Date of Registration	Area of research	Research center	Guide	Remarks
1	Dr. Siddalingappa S. Kerur, Asst. Professor	Department of Electronics & Communication Engineering, SDMCET, Dharwad	94487-89455	10-05-2006 (VTU, Belagavi)	"Performance Analysis of Arithmetic VLSI Architectures for Computational Applications"	Department of Electronics & Communication Engineering, SDMCET, Dharwad	Prof. Dr. Harish Kittur VIT, Vellore & Prof. Dr. Girish.V.Attimarad, DSCE, Bangalore Co-Guide	Awarded on 02.04,2016  (SDMCET, Center)
2	Dr. S.V. Virktamath Asst. Professor	Department of Electronics & Communication Engineering, SDMCET, Dharwad	9844265817	05.05.2009	Wireless Communication	Department of Electronics & Communication Engineering, SDMCET, Dharwad	Prof. Dr.Girish.V.Attimarad, DSCE, Bangalore	Awarded on 21.01.2017  (SDMCET, Center)
3	Dr. K.N Hosur	Department of Electronics & Communication Engineering, SDMCET, Dharwad	9611032571	01.11.2009  (VTU, Belagavi)	VLSI	Department of Electronics & Communication Engineering, SDMCET, Dharwad	Guide Dr. Girish.V.Attimarad, DSCE, Bangalore Co-Guide: Dr. Harish Kittur VIT, Vellore	Awarded March 2018  (SDMCET, Center)
4	Dr.. Sharada C Sajjan	Department of Electronics & Communication Engineering, SDMCET, Dharwad	9449500828	1/11/2009	Speech Processing	Department of Electronics & Communication Engineering, SDMCET, Dharwad	Prof. Dr. Vijaya C SDMCET Dharwad	Awarded on 12.03.2018  (SDMCET, Center)
5	Dr. Mahendra Dixit	Department of Electronics & Communication Engineering, SDMCET, Dharwad	9731869101	30-10-2013	Image processing and embedded system	Department of Electronics & Communication Engineering, SDMCET, Dharwad	Prof. Dr. Vijaya C SDMCET Dharwad	Awarded on 26.01.2019  (SDMCET, Center)

<b>Sl. No.</b>	<b>Name</b>	<b>Name of the Institution</b>	<b>Mobile No</b>	<b>Date of Registration</b>	<b>Area of research</b>	<b>Research center</b>	<b>Guide</b>	<b>Remarks</b>
6	Dr. S.S. Navalgund	Department of Electronics & Communication Engineering, SDM CET, Dharwad	9880863708	10/05/2006	Manual_Routing_in_FPGAs_For_DSP_Implementations	Department of Electronics & Communication Engineering, SDM CET, Dharwad	Dr. Mritunjaya V. Latte	Awarded
7	Dr. Vyas R. Murnal	Department of Electronics & Communication Engineering, SDM CET, Dharwad	8884277007	24/10/2013	VLSI	Department of Electronics & Communication Engineering, SDM CET, Dharwad	Dr. Vijaya C.	Awarded

**External Candidates:03**

Sl. No.	Name	Name of the Institution	Date of Registration	Title of the thesis	Research center	Guide	Remarks
1	Mr. K. Chandrasekhar	SSIT, Tumkur	12-12-2008	Dielectric waveguides with discontinuities analysis and designing two dimensional finite element method	SDMCET	Prof. Dr. Girish V.Attimarad DSCE, Bangalore	Awarded on May 2016
2	Mr. Eshwarappa M.N	SSIT, Tumkur	12-12-2006	Multimodal biometric personal authentication system	SDMCET	Prof. Dr. M.V.Latte SBJCE, Bangalore	Awarded on April 2012
3	Mrs. Lalita P.S	AEC, Gulbarga	12-12-2006	Wavelets for image compression	SDMCET	Prof. Dr. M.V.Latte SBJCE, Bangalore	Awarded on June 2012

**List of internal candidate awarded with Ph.D degree at external research centre:01**

Sl. No.	Name	Name of the Institution	Mobile No	Date of Registration	Area of research	Research center	Guide	Remarks
1	Mrs. Jayashree C Nidagundi	Department of Electronics & Communication Engineering, PDA College of Engineering, Kalaburagi	9242149952	20-11-2010 (VTU, Belagavi)	VLSI AND COMMUNICATION	Department of Electronics & Communication Engineering, PDA College of Engineering, Kalaburagi	Prof. Dr. Siddaram Patil, PDA college of Engg, Gulbarga	Awarded on 20-07-2019

III. Ph.D – Presently registered at our (E&CE, SDMCET) research center  
(Internal Candidates) :12

Sl. No.	Name of the faculty members	Name of the guide	Year of registration	University	Area of research	Full time / Part time	Course work Tick (√) appropriate column		Comprehensive Viva (√) appropriate column		Thesis work (√) appropriate column		Status of Final Defense Tick (√) appropriate column		Probable date of Ph.D award	
							Completed	In Progress	Completed	Not Completed	Submitted	Writing	Completed	Not Completed		
1	Mr. Sunil S.Mathad	Prof. Dr.Vijaya C. SDMCET Dharwad	30.10.2013	VTU, Belagavi	Communication & VLSI	Part time	√		√			√		√	2020 (Likely)	
2	Mr. Vyas R Murnal	Prof. Dr.Vijaya C. SDMCET Dharwad	24.10.2013	VTU, Belagavi	Digital Signal Processing & VLSI	Part time	√		√		√			√	2020 (Likely)	
3	Sri.S.S.Navalgund	Dr.M.V.Latte, JSS, Bangalore	10-05-2006	VTU, Belagavi	Manual Routing in FPGA for DSP Implementation	Part time	√		√			√		√	submitted	
4	Smt. Savitri Raju	Prof. Dr.Satish Bhairannavar, SDMCET, Dharwad	10-08-2016	VTU, Belagavi	FPGA Implementation of Image Compression	Part time	√		√			√		√	2021 (Likely)	
5	Mr.Bairu K.Saptalakar	Dr.M.V.Latte JSS, Bangalore	28.11.2013	VTU, Belagavi	Architecture optimization for image processing using verilog	Part time	√		√			√		√	2021 (Likely)	
6	Mr.Vinayak Miskin	Prof. Dr.G.A Bidkar SDMCET, Dharwad	08.01.2015	VTU, Belagavi	Design Implementation and Performance Analysis of Low Power Digital Circuits	Part time		√		√				√	2022 (Likely)	
7	Mr. Raghuram K M	Prof. Dr.S. A. Joshi SDMCET,	2017	VTU, Belagavi	Energy Efficient Communication Schemes for Wireless Sensor	Part time	<b>Registered and preparing for course work</b>									

		Dharwad			Networks		
8	Mr. Shrikanth Shirakol	Dr. S S Kerur SDMCET, Dharwad	2017	VTU, Belagavi	Design and Performance Analysis of VLSI Architectures for Image Transforms	Part time	<b>Comprehensive viva completed</b>
9	Mr.Gopal Krishna Mane	Dr. S S Kerur SDMCET, Dharwad	2017	VTU, Belagavi	Design and Performance Analysis of Low power VLSI Circuits for Advanced Embedded Applications	Part time	<b>Course work completed</b>
10	Mrs. Preeti S Bellarimath	Dr. S S Kerur SDMCET, Dharwad	2017	VTU, Belagavi	Performance Analysis of Low power arithmetic logic unit for various embedded applications	Part time	<b>Course work completed</b>
11	Mrs. Sumangala Bhavikatti	Prof. Dr. Satish Bhairannavar, SDMCET, Dharwad	2017	VTU, Belagavi	Performance Analysis and Recognition for Face Covariates and its Implementation	Part time	<b>Course work completed</b>
12	Mrs Reshma Nadaf		2019	VTU, Belagavi	An efficient privacy preserving technique for identity based cryptography & its implementation	Part time	<b>Preparing for course work</b>

**External candidates Registered Ph.D at our (E&CE, SDMCET) research center:17**

S.No	Name of the faculty members	Designation	Registered for Ph.D (Yes/No)	If registered					
				Title	Name of the guide & Institution	University	Research Centre	Present status	Expected date of completion of Ph.D
1	Md. Shoaibuddin Madni	Asst. Prof	Yes	Gesture Recognition based on shape parameters	Prof. Dr. Vijaya C SDMCET Dharwad	VTU, Belagavi	SDMCET Dharwad	Comprehensive viva-voce completed	Dec. 2020
2	Mrs. Akshata Konnur		Yes	Image Processing	Prof. Dr. Vijaya C SDMCET Dharwad	VTU, Belagavi	SDMCET Dharwad	Comprehensive viva-voce	Dec. 2020

								completed	
3	Mr. Avinash Shrihari Joshi	Asst. Prof	Yes	A Novel Design of Channel Equalizer for Cognitive MIMO-OFDM Radios U:sing Adapative V-BLAST Technique for Modern Wireless Communication System	Prof. Dr.G.A Bidkar SDMCET, Dharwad	VTU, Belagavi	SDMCET Dharwad	Preparing for course work	Dec 2020
4	Mrs. Deepa Bendigeri	Asst. Prof	Yes	Offline Writer Identification Using DIP Techniques	Prof. Dr.G.A Bidkar SDMCET, Dharwad	VTU, Belagavi	SDMCET Dharwad	Comprehensive viva-voce completed	Dec 2020
5	Mr. Ajit Bhovi	Asst. Prof	Yes	Development of Routing Protocol for Data Aggregation and Secured Data Transmission in Wireless Sensor Network	Prof. Dr.G.A Bidkar SDMCET, Dharwad	VTU, Belagavi	SDMCET Dharwad	Preparing for course work	Dec 2020
6	Mr.Pradeep A.S	Asst. Prof	Yes	Development of Novel Techniques for Reduction of Mutual Coupling to Improve the Performance in Steered Array – Lens Antenna System	Prof. Dr.G.A Bidkar SDMCET, Dharwad	VTU, Belagavi	SDMCET Dharwad	Comprehensive viva-voce completed	Dec 2020
7	Mr. Krishna G Deshpande	Asst. Prof	Yes	Development of Adaptive Scheduling Algorithms for Multiuser MIMO System to Improve the Performance of Wireless Communication Network	Prof. Dr.G.A Bidkar SDMCET, Dharwad	VTU, Belagavi	SDMCET Dharwad	Course work completed	Dec 2020
8	Mr. Veeresh Hiremath	Asst. Prof	Yes	Optimization of Power Consumption Wireless Sensor	Prof. Dr.G.A Bidkar SDMCET, Dharwad	VTU, Belagavi	SDMCET Dharwad	Comprehensive viva-voce completed	Dec 2020

				Nodes by Load Sharing, Cluster based routing, Multiple Mobile Base Stations and Auxiliary Power Source.					
9	Mr. Sachin Shatigouda Patil	Asst. Prof HIT, Nidassosi	Yes	Study, Design and Numerical Analysis of MIMO Wireless Sy	Prof. Dr. Shreedhar A Joshi SDMCET, Dharwad	VTU, Belagavi	SDMCET Dharwad	Preparing for course work	Dec 2020
10	Mr. Shreenivas Gudi	Asst. Prof. DON BOSCO, Bangalore	Yes	Optimal Resource Allocation with Spectrum Sensing in Wireless Sensor Network	Prof. Dr. Shreedhar A Joshi SDMCET, Dharwad	VTU, Belagavi	VTU, Belagavi	Preparing for course work	Dec 2020
11	Mrs. Deepa M. Bhangi	Asst. Prof. MCE, Hassan	Yes	Detection of Malingat Brain Tumour by an Integration Algorithm	Prof. Dr. Shreedhar A Joshi SDMCET, Dharwad	VTU, Belagavi	SDMCET Dharwad	Course work completed	Dec 2020
12	Mr. Mahesh M. Gadag	Associate. Prof & HOD HIT, Nidassosi	Yes	Wireless communication	Prof. Dr. Shreedhar A Joshi SDMCET, Dharwad	VTU, Belagavi	SDMCET Dharwad	Course work COMPLETED	Dec 2021
13	Mr. Sayantam Sarkar	Assistant Professor, VJIT, Bangalore	Yes	Design of Efficient Reconfigurable VLSI Architectures for Real Time Video Processing	Prof. Dr. Satish Bhairannavar, SDMCET, Dharwad	VTU, Belagavi	SDMCET Dharwad	Preparing for course work	Dec 2021
14	Mr. Supreetha Rao	Research Scholar	Yes	Design of Efficient Fault Tolerant and Partial Reconfigurable Architectures	Prof. Dr. Satish Bhairannavar, SDMCET, Dharwad	VTU, Belagavi	SDMCET Dharwad	Preparing for course work	Dec 2021
15	Mr. Kunjan D Shinde	Asst. Prof. PES Institute of Technology	Yes	Parallel signal processing Architecture: A	Prof. Dr. Vijaya C SDMCET Dharwad	VTU, Belagavi	SDMCET Dharwad	Preparing for course work	Dec. 2022



		& Management, Shivamogga		VLSI Based Approach					
16	Mrs. Nalina S B	Asst. Prof. JNNCE Shivamogge	Yes	Wireless sensor network	Prof. Dr. Shreedhar A Joshi SDMCET, Dharwad	VTU, Belagavi	SDMCET Dharwad	Preparing for course work	Dec. 2022
17	Mr. Santosh Herur	Asst. Prof. GMT, Davangere	Yes	Image processing & VLSI Technique	Dr. S.S.Kerur SDMCET, Dharwad	VTU, Belagavi	SDMCET Dharwad	Comprehensive-Viva completed	July 2020

#### IV. Details of research guides and students

S.No	Name of the Guide	Details of Candidates Registered				
		Name	Gen der	Catego ry	Year of registration	VTU/ Other University
1	Prof. Dr. Vijaya C	Mr. Sunil S.Mathad	M	GEN	2013	VTU, Belagavi
		Mr. Vyas R Murnal	M	GEN	2013	VTU, Belagavi
		Mr.Md. Shoaibuddin Madani	M	SC	Dec.2013	VTU, Belagavi
		Mrs. Akshata Konnur	F	SC	26.12.2013	VTU, Belagavi
		Mr. Kunjan D Shinde	M	SC	29/05/2017	VTU, Belagavi
		Mr. Avinash Shrihari Joshi	M	GEN	13.03.2015	VTU, Belagavi
2	Prof. Dr. G.A Bidkar	Mrs. Deepa Bendigeri	F	GEN	12.12.2014	VTU, Belagavi
		Mr. Ajit Bhovi	M	GEN	13.03.2015	VTU, Belagavi
		Mr.Pradeep A.S	M	GEN	12..12.2014	VTU, Belagavi
		Mr. Krishna G Deshpande	M	GEN	12.12.2014	VTU, Belagavi
		Mr.Veeresh Hirmath	M	GEN	12.12.2014	VTU, Belagavi

		Mr. Vinayak Miskin	M	GEN		VTU, Belagavi
3	Prof. Dr. Shreedhar Joshi	Mr.Sachin Shatigouda Patil , Asst. Prof HIT, Nidassosi	M	GEN	25.01.2016	VTU, Belagavi
		Mr. Shreenivas Gudi, Asst. Prof. DON BOSCO, Bangalore	M	GEN	08.12.2015	VTU, Belagavi
		Mrs. Deepa M. Bhangi, Asst. Prof. MCE, Hassan	F	SC	16.03.2016	VTU, Belagavi
		Mr. Mahesh M. Gadag, Associate. Prof & HOD HIT, Nidassosi	M	GEN	04.03.2016	VTU, Belagavi
		Mr. Raghuram Hegde	M	GEN	2016	VTU, Belagavi
		Mrs. Nalini S B	F	GEN	2017	VTU, Belagavi
4	Dr. S. S. Kerur	Mrs.Preeti S Bellarimath	F	GEN	22.5.2017	VTU, Belagavi
		Mr. Shrikanth Shirakol	M	OBC	22.5.2017	VTU, Belagavi
		Mr. GopalKrishna Mane	M	OBC	23.3.2017	VTU, Belagavi
		Mr. Santosh Herur	M	OBC	2015	VTU, Belagavi
5	Prof. Dr. Satish Bairannavar	Mrs. Savitri V Dhotrad	F	GEN	10.08.2016	VTU, Belagavi
		Mr. Sayantam Sarkar	M	OBC	2017	VTU, Belagavi
		Mr. Supreetha Rao	F	GEN	2017	VTU, Belagavi
		Mrs. Sumangala Bhavikatti	F	GEN	25.03.2017	VTU, Belagavi
		Mrs. Reshma Nadaf	F	CAT-1	22.06.2019	VTU, Belagavi